

# UNISONIC TECHNOLOGIES CO., LTD

### 30N20

Preliminary

## 30A, 200V N-CHANNEL POWER MOSFET

#### DESCRIPTION

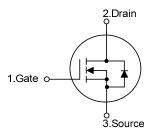
The UTC **30N20** is an N-channel mode Power FET, it uses UTC's advanced technology. This technology allows a minimum on-state resistance, superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

#### FEATURES

\*  $R_{DS(ON)}$ <75m $\Omega$  @  $V_{GS}$ =10V, $I_D$ =15A

- \* Low Gate Charge (Typical 60nC)
- \* High Switching Speed

#### SYMBOL



# 1 TO-220F2

#### ORDERING INFORMATION

Ordering Number		Daakaga	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
30N20L-TF2-T 30N20G-TF2-T		TO-220F2	G	D	S	Tube	
Note: Pin Assignment: G: Gate D: Drain S: Source							
30N20L-TF2-T (1)Packing Type (2)Package Type		(1) T: Tube (2) TF2: TO-220F2					
(3)Lead Free		(3) G: Halogen Free, L: Lead Free					

#### ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	200	V
Gate-Source Voltage		V <sub>GSS</sub>	±30	V
Drain Current	Continuous	ID	30	А
	Pulsed	I <sub>DM</sub>	124	А
Avalanche Current		I <sub>AR</sub>	30	А
Avalanche Energy	Single Pulsed	E <sub>AS</sub>	640	mJ
	Repetitive	E <sub>AR</sub>	18	mJ
Power Dissipation		PD	42	W
Junction Temperature		TJ	+150	°C
Storage Temperature Range		T <sub>STG</sub>	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	200			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =200V			1	μA
Gate-Source Leakage Current	ard	V <sub>GS</sub> =+30V, V <sub>DS</sub> =0V			+100	nA
Revel	rse I <sub>GSS</sub>	V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	I <sub>D</sub> =250μΑ			5	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =15A			75	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz		2400	3100	рF
Output Capacitance	Coss			430	560	рF
Reverse Transfer Capacitance	C <sub>RSS</sub>			55	70	рF
SWITCHING PARAMETERS						
Total Gate Charge	$Q_{G}$	V <sub>DD</sub> =50V, V <sub>GS</sub> =10V , I <sub>D</sub> =1.3A		60	78	nC
Gate to Source Charge	$Q_{GS}$			17		nC
Gate to Drain Charge	$Q_{GD}$			27		nC
Turn-ON Delay Time	t <sub>D(ON)</sub>			40		ns
Rise Time	t <sub>R</sub>	$V_{DD}$ =30V, I <sub>D</sub> =0.5A, R <sub>G</sub> =25 $\Omega$ ,		280		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	V <sub>GS</sub> =0~10V		125		ns
Fall-Time	t <sub>F</sub>			115		ns
SOURCE- DRAIN DIODE RATINGS AND	CHARACTERISTI	CS				
Maximum Body-Diode Continuous Current	t I <sub>s</sub>				30	Α
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				124	Α
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =30A, V <sub>GS</sub> =0V			1.5	V



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