

# **HAT2105R**

Silicon N Channel Power MOS FET
High Speed Power Switching

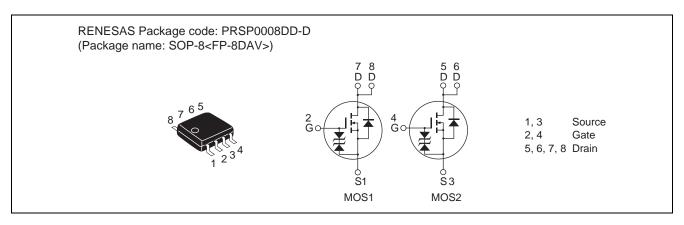
R07DS0552EJ0200 (Previous: REJ03G1369-0100)

> Rev.2.00 Oct 11, 2011

## **Features**

- Low on-resistance  $R_{DS(on)}=1.6~\Omega~typ.~(at~I_D=0.5~A,~V_{GS}=10~V,~Ta=25^{\circ}C)$
- Capable of 4 V gate drive
- High density mounting

## **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	200	V
Gate to source voltage	$V_{GSS}$	±15	V
Drain current	I <sub>D</sub>	0.5	А
Drain peak current	I <sub>D(pulse)</sub> Note1	2	A
Body-drain diode reverse drain current	$I_{DR}$	0.5	А
Channel dissipation	Pch Note2	1.3	W
	Pch Note3	2	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1 %

- 2. 1 Drive operation ; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10 s
- 3. 2 Drive operation ; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10 s

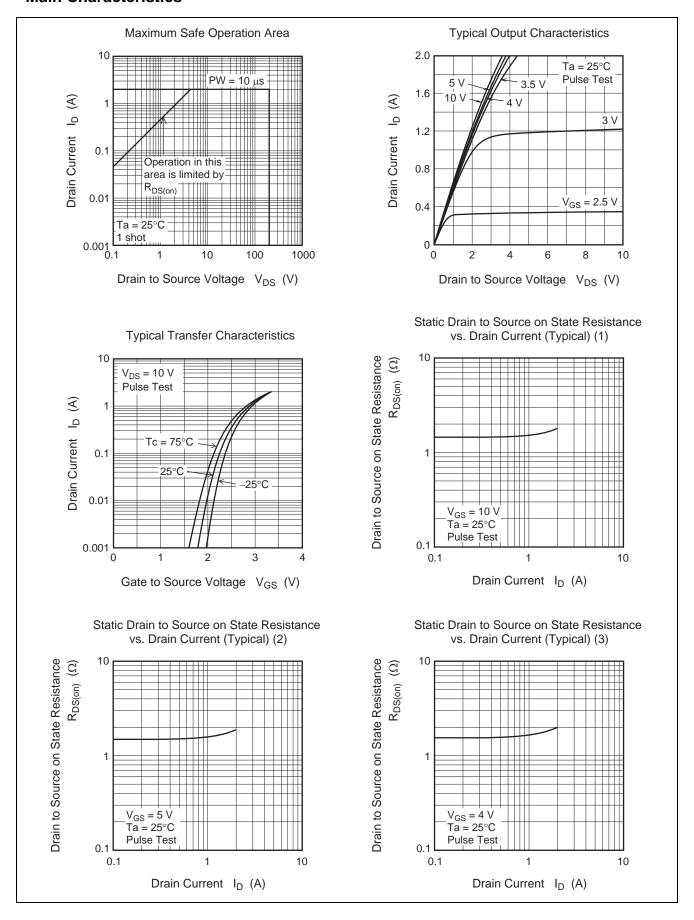
# **Electrical Characteristics**

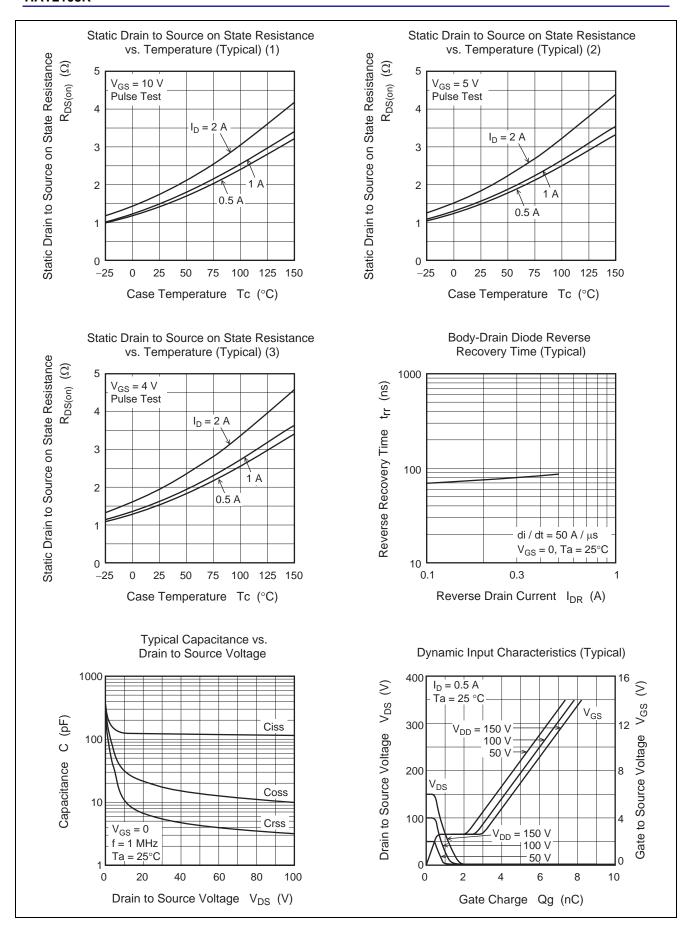
 $(Ta = 25^{\circ}C)$ 

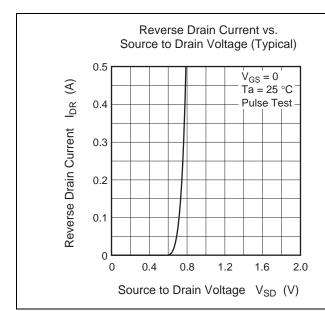
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	200	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±15	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	5	μΑ	$V_{DS} = 200 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.1	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	1.6	2.2	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	1.9	2.7	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note4}}$
	R <sub>DS(on)</sub>	_	2.4	5.5	Ω	$I_D = 2 \text{ A}, V_{GS} = 5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	0.56	0.86	_	S	$I_D = 0.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	120	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$
Output capacitance	Coss	_	29	_	pF	
Reverse transfer capacitance	Crss	_	10	_	pF	
Turn-on delay time	t <sub>d(on)</sub>	_	10	_	ns	$V_{GS} = 5 \text{ V}, I_D = 0.5 \text{ A},$
Rise time	t <sub>r</sub>	_	14	_	ns	$V_{DD} \cong 30 \text{ V}$
Turn-off delay time	t <sub>d(off)</sub>	_	24	_	ns	
Fall time	t <sub>f</sub>	_	9	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.9	1.4	V	$I_F = 0.5 \text{ A}, V_{GS} = 0^{\text{Note4}}$

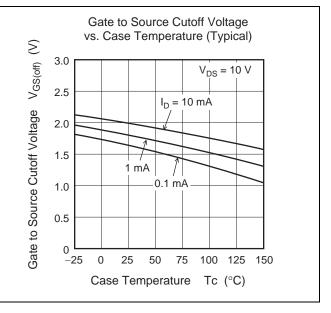
Notes: 4. Pulse test

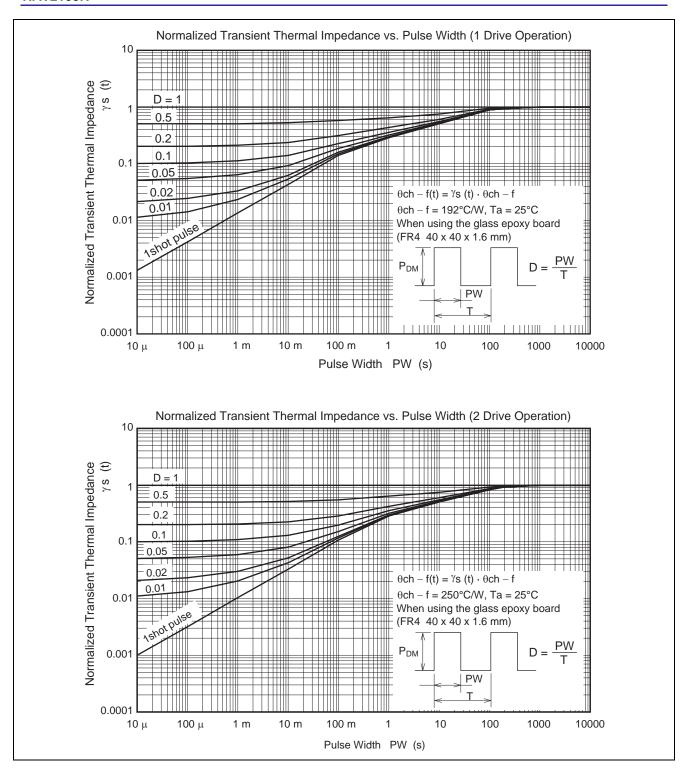
## **Main Characteristics**



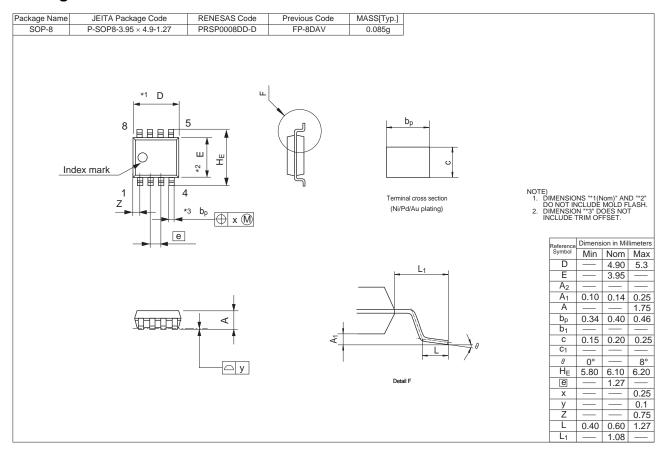








## **Package Dimensions**



# **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
HAT2105R-EL-E	2500 pcs	Taping

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