

**General Description**

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for active power factor correction , electronic lamp ballasts based on half bridge topology and switching mode power supplies.

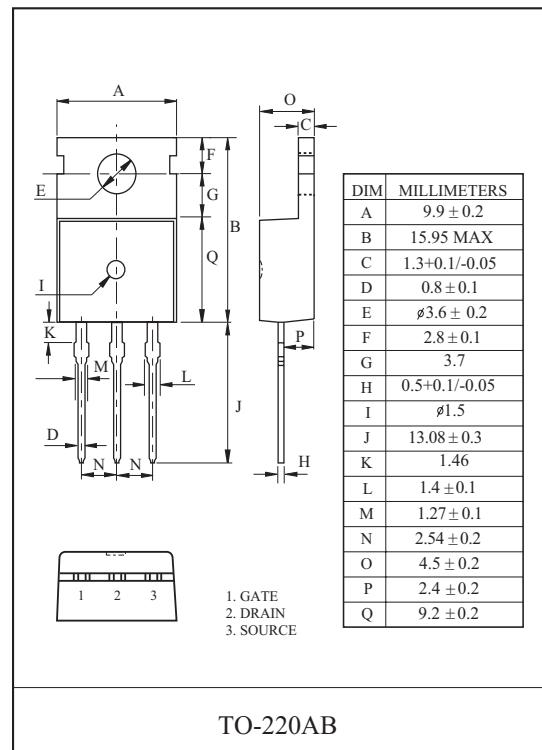
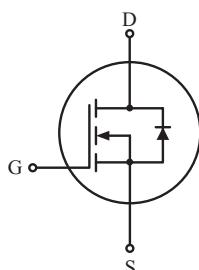
**FEATURES**

- $V_{DSS} = 60V$ ,  $I_D = 50A$
- Drain-Source ON Resistance :  
 $R_{DS(ON)} = 17m\Omega$  (Max.) @  $V_{GS} = 10V$
- $Q_g(\text{typ.}) = 39.5nC$

**MOSFET MAXIMUM RATING (Ta=25 °C Unless otherwise noted)**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	$V_{DSS}$	60	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current	$I_D$ *	50	A
		32	
	$I_{DP}$	170	
Single Pulsed Avalanche Energy (Note 2)	$E_{AS}$	330	mJ
Repetitive Avalanche Energy (Note 1)	$E_{AR}$	9	mJ
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5	V/ns
Drain Power Dissipation	$P_D$	96	W
		0.77	W/
Maximum Junction Temperature	$T_j$	150	
Storage Temperature Range	$T_{stg}$	-55 150	
<b>Thermal Characteristics</b>			
Thermal Resistance, Junction-to-Case	$R_{thJC}$	1.3	/W
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62.5	/W

\* : Drain current limited by maximum junction temperature.

**PIN CONNECTION**

# KF50N06P

## ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25 °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250 μA, V <sub>GS</sub> =0V	60	-	-	V
Breakdown Voltage Temperature Coefficient	BV <sub>DSS</sub> / T <sub>j</sub>	I <sub>D</sub> =250 μA, Referenced to 25	-	0.08	-	V/°C
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V,	-	-	10	μA
Gate Threshold Voltage	V <sub>th</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	2	-	4	V
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =25A	-	14.2	17.0	mΩ
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =48V, I <sub>D</sub> =50A V <sub>GS</sub> =10V (Note 4,5)	-	39.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	8	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	16	-	
Turn-on Delay time	t <sub>d(on)</sub>	V <sub>DD</sub> =30V I <sub>D</sub> =50A R <sub>G</sub> =25 (Note 4,5)	-	30	-	ns
Turn-on Rise time	t <sub>r</sub>		-	100	-	
Turn-off Delay time	t <sub>d(off)</sub>		-	80	-	
Turn-off Fall time	t <sub>f</sub>		-	64	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz	-	1560	-	pF
Output Capacitance	C <sub>oss</sub>		-	405	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	76	-	
<b>Source-Drain Diode Ratings</b>						
Continuous Source Current	I <sub>S</sub>	V <sub>GS</sub> <V <sub>th</sub>	-	-	50	A
Pulsed Source Current	I <sub>SP</sub>		-	-	200	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =50A, V <sub>GS</sub> =0V	-	-	1.4	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>S</sub> =50A, V <sub>GS</sub> =0V, dI <sub>S</sub> /dt=100A/μs	-	72	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	185	-	nC

Note 1) Repetitve rating : Pulse width limited by junction temperature.

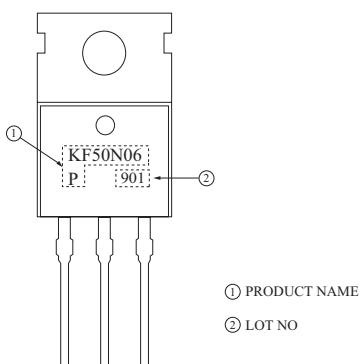
Note 2) L=132 μH, I<sub>S</sub>=50A, V<sub>DD</sub>=30V, R<sub>G</sub>=25 Ω, Starting T<sub>j</sub>=25 °C.

Note 3) I<sub>S</sub>=50A, dI/dt=200A/μs, V<sub>DD</sub>=BV<sub>DSS</sub>, Starting T<sub>j</sub>=25 °C.

Note 4) Pulse Test : Pulse width = 300μs, Duty Cycle = 2%.

Note 5) Essentially independent of operating temperature.

## Marking



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Fig1.  $I_D$  -  $V_{DS}$

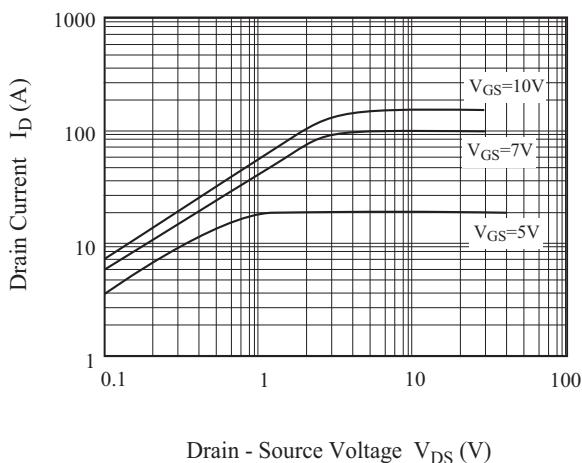


Fig2.  $I_D$  -  $V_{GS}$

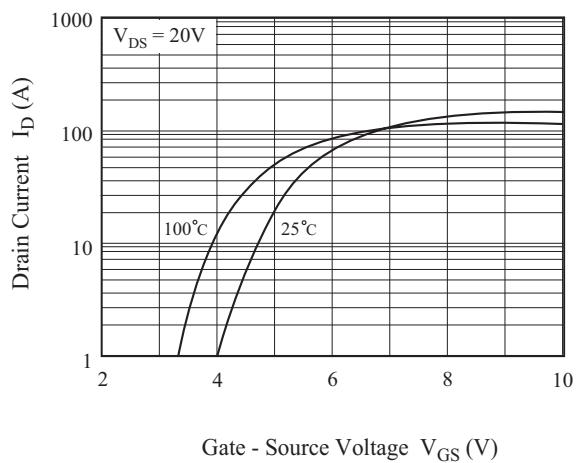


Fig3.  $BV_{DSS}$  -  $T_j$

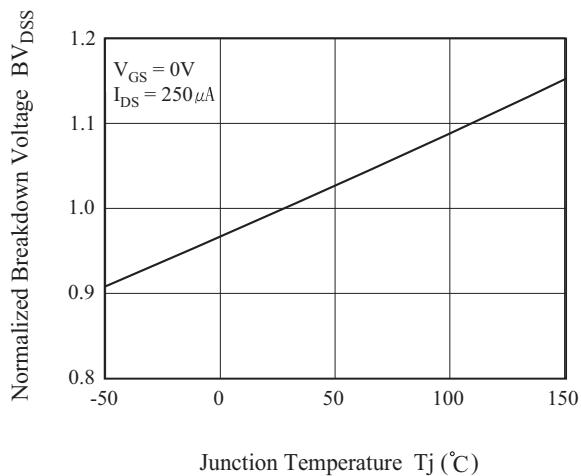


Fig4.  $R_{DS(ON)}$  -  $I_D$

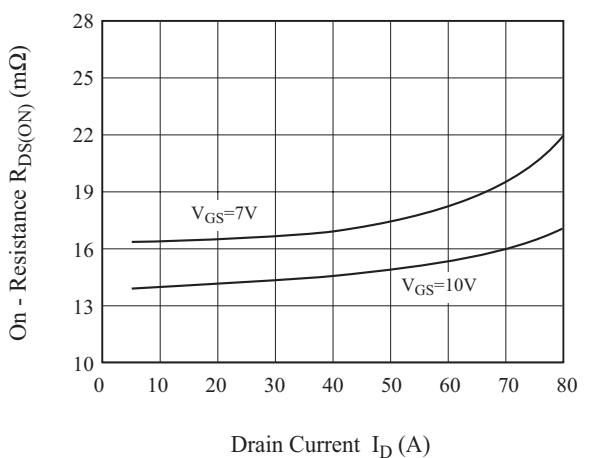


Fig5.  $I_S$  -  $V_{SD}$

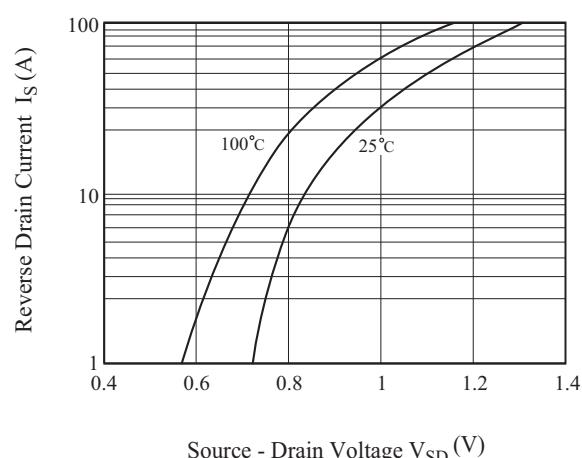
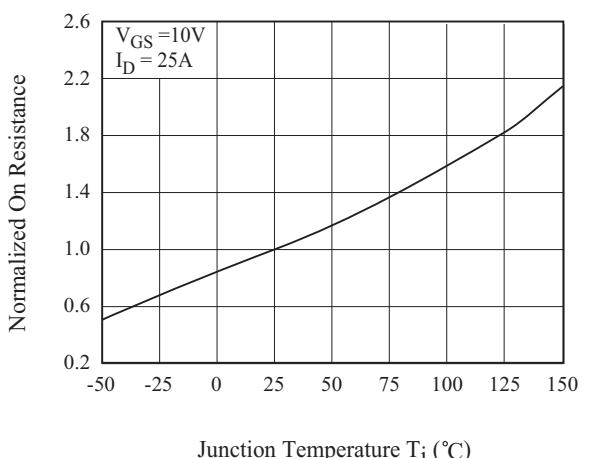


Fig6.  $R_{DS(ON)}$  -  $T_j$



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Fig 7. C - V<sub>DS</sub>

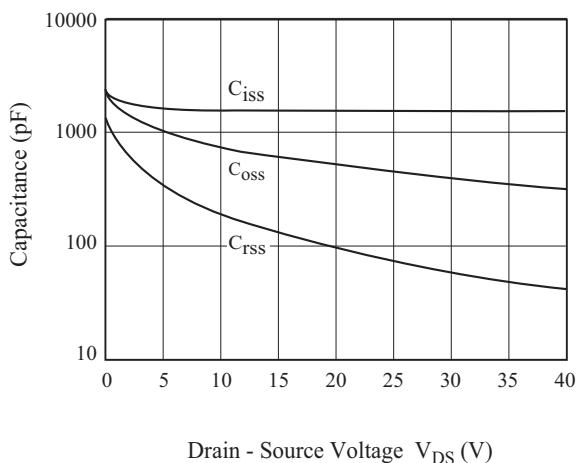


Fig 8. Q<sub>g</sub> - V<sub>DS</sub>

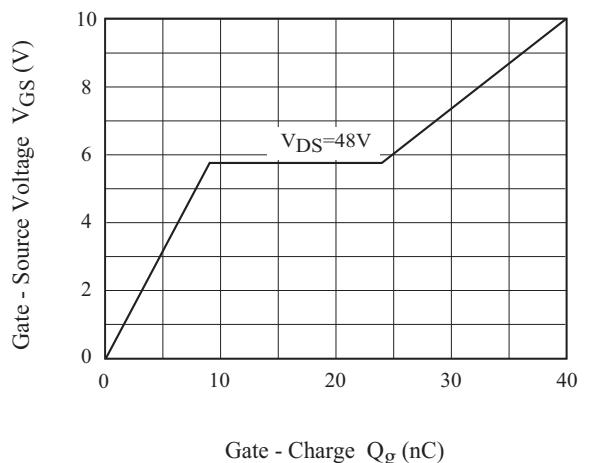


Fig 9. Safe Operation Area

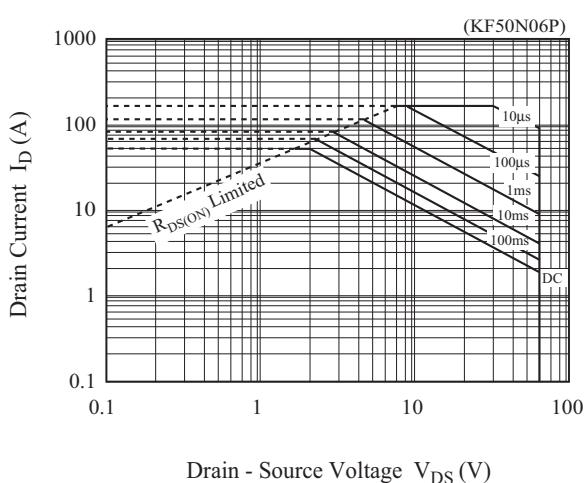
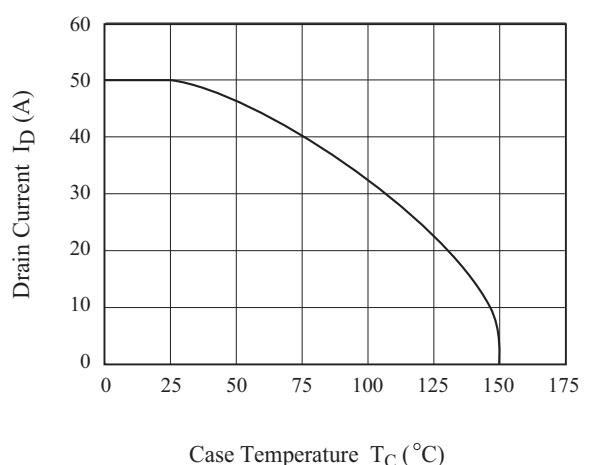


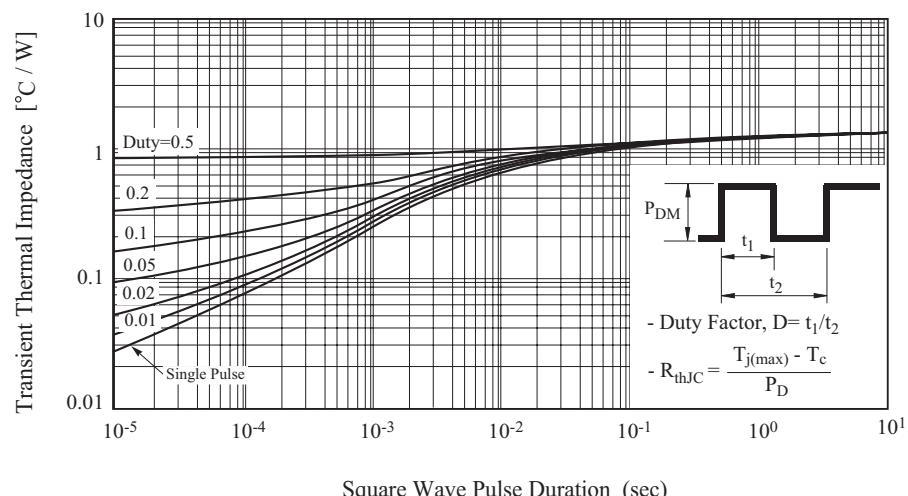
Fig 10. I<sub>D</sub> - T<sub>C</sub>



Drain - Source Voltage V<sub>DS</sub> (V)

Case Temperature T<sub>C</sub> (°C)

Fig 11. R<sub>th</sub> of KF50N06P



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Fig12. Gate Charge

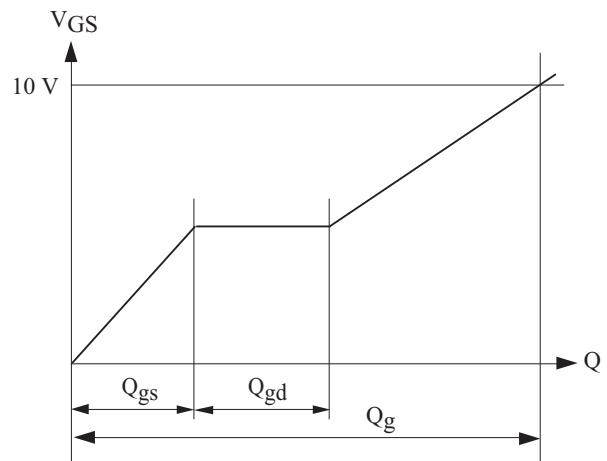
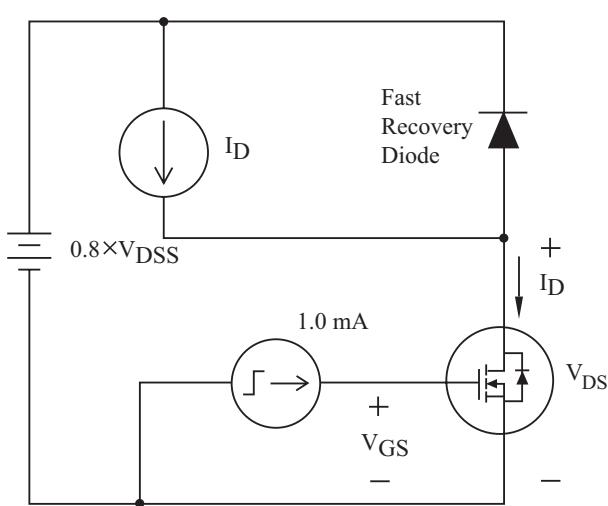


Fig13. Single Pulsed Avalanche Energy

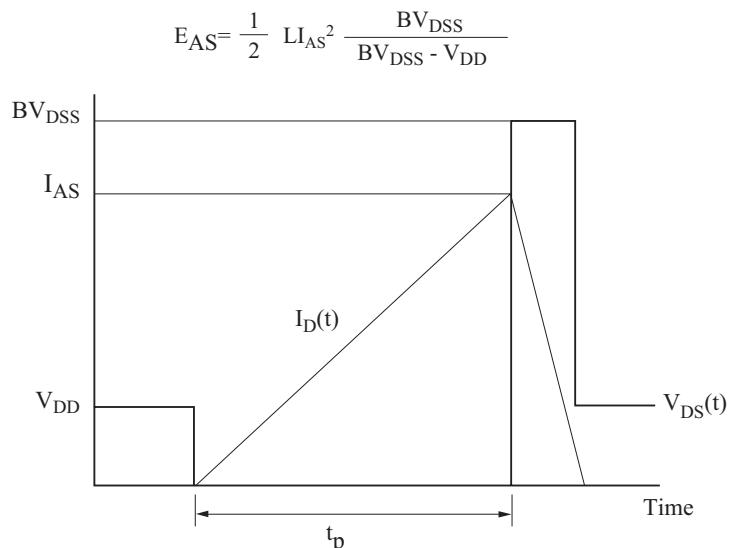
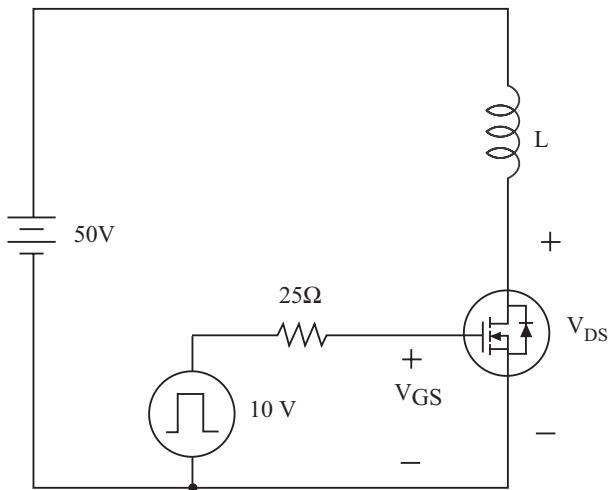
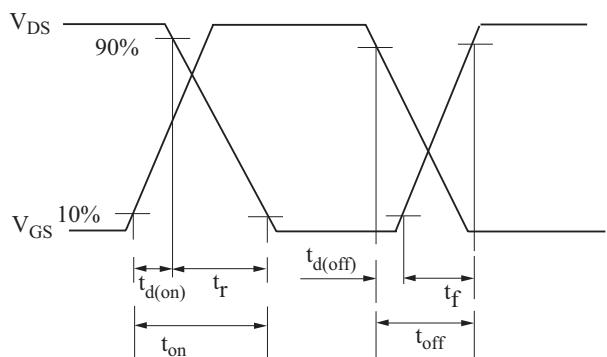
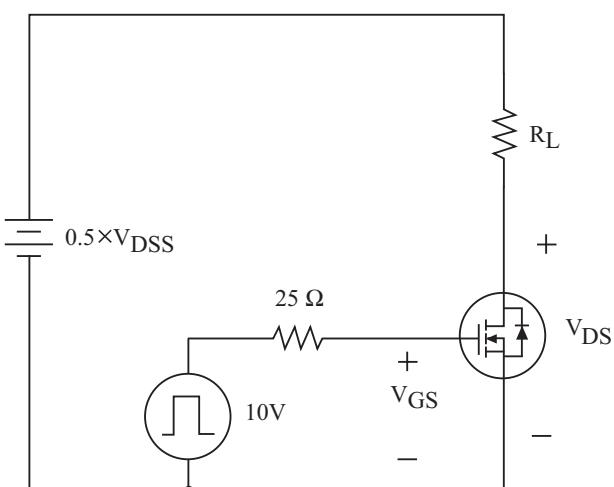


Fig14. Resistive Load Switching



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Fig15. Source - Drain Diode Reverse Recovery and dv /dt

