

UTC UNISONIC TECHNOLOGIES CO., LTD

US2829

Preliminary

CMOS IC

SINGLE-CHANNEL **HIGH-SPEED MOSFET DRIVER**

DESCRIPTION

The UTC US2829 is a single-channel high-speed MOS-FET driver. The device is fabricated by use of BICMOS outputs to achieve high switching speed. The outputs are capable of delivering peak currents up to 2A into capacitive loads.

FEATURES

- * Low-cost single-channel high-speed MOSFET driver
- * 2A peak output current
- * 25ns max rise/fall times and 40ns max propagation delay,1nF load
- * Low power dissipation: I_{CC}=15µA(Max) @ Ta=25°C
- * Broad V_{CC} operating range:4V to 14V
- * Halogen Free

ORDERING INFORMATION

Ordering Number	Package	Packing
US2829G-AF5-R	SOT-25	Tape Reel



MARKING





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PIN CONFIGURATION



PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	NC	Not connected
2	GND	Ground Connection
3	IN	Driver input
4	OUT	Driver output, OUT= IN
5	V _{cc}	Driver supply voltage/regulator output voltage

FUNCTION TABLE

INPUT(IN)	OUTPUT(OUT)
Н	Н
L	L

H: High Level

L: Low Level

LOGIC DIAGRAM





■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{CC}	-0.3 ~ +15	V
Input Voltage	V _{IN}	-0.3 ~ V _{CC} +0.5	V
Output Voltage	V _{OUT}	-0.5 ~ V _{CC} +0.5	V
Continuous Output Current	lout	±100	mA
Power Dissipation		437	mW
Derated Above 25°C	PD	3.5	mW/°C
Operating Temperature	T _{OPR}	-40 ~ + 125	°C
Storage Temperature	T _{STG}	-65 ~ + 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	V _{CC}	4		14	V
Input Voltage	V _{IN}	-0.3		V _{CC}	V
Operating Temperature	T _{OPR}	-40		125	°C

ELECTRICAL CHARACTERISTICS (Ta=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-Level Output Voltage	Vон	V _{CC} =10V, I _{OH} =-1mA		9.9		V
		V _{CC} =10V, I _{OH} =-100mA	8	9.1		V
	V _{OL}	V _{CC} =10V, I _{OL} =1mA		0.18	0.25	V
Low-Level Output Voltage		V _{CC} =10V, I _{OL} =100mA		1	2	V
Desitive asing input threshold	V _T +	V _{CC} =5V		3.3	4	V
Positive-going input threshold voltage		V _{CC} =10V		6.6	7	V
		V _{CC} =14V		9.3	10	V
Negative-going input threshold voltage	V _{T-}	V _{CC} =5V	1	1.7		V
		V _{CC} =10V	2	3.3		V
		V _{CC} =14V	2.5	4.6		V
Input voltage hysteresis	$V_{T+}-V_{T-}$			1.3		V
Input Leakage Current	I _{I(LEAK)}	V_{CC} =10V, V_{IN} =0 or V_{CC}		0.2		μA
Supply Current	Icc	V _{CC} =10V, V _{IN} =V _{CC} or GND, I _{OUT} =0		0.1	15	μA
Input Capacitance	C _{IN}	V _{CC} =10V		5	10	рF

SWITCHING CHARACTERISTICS (see TEST CIRCUIT AND WAVEFORMS)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from input (IN) to output(OUT)	t _{PLH} t _{PHL}	V_{CC} =14V, C _L =1nF			40	ns
		V_{CC} =10V, C _L =1nF		24	45	ns
		V_{CC} =5V, C _L =1nF			50	ns
Output transition time	t _r /t _f	V _{CC} =14V, C _L =1nF			25	ns
		V_{CC} =10V, C _L =1nF		14	30	ns
		V _{CC} =5V, C _L =1nF			35	ns



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TEST CIRCUIT AND WAVEFORMS



Note: CL includes probe and jig capacitance.

All input pulses are supplied by generators having the following characteristics: PRR \leq 1MHz, Z₀=50 Ω , tr \leq 6ns, tf \leq 6ns.

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