

SINGLE 2 INPUT POSITIVE NAND GATE

Description

The Advanced Ultra Low Power (AUP) CMOS logic family is designed for low power and extended battery life in portable applications.

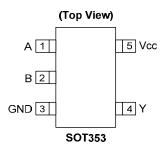
The 74AUP1G00 is a single 2-input positive NAND gate with a standard push-pull output designed for operation over a power supply range of 0.8V to 3.6V. The device is fully specified for partial power down applications using I_{OFF}. The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down. The gate performs the positive Boolean function:

$$Y = \overline{A \bullet B}$$
 or $Y = \overline{A} + \overline{B}$

Features

- Advanced Ultra Low Power (AUP) CMOS
- Supply Voltage Range from 0.8V to 3.6V
- ±4 mA Output Drive at 3.0V
- Low Static power consumption
 - I_{CC} < 0.9µA
- Low Dynamic Power Consumption
 - C_{PD} = 6pF (Typical at 3.6V)
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time. The hysteresis is typically 250 mV at $V_{CC} = 3.0V$
- I_{OFF} Supports Partial-Power-Down Mode Operation
- ESD Protection Exceeds JESD 22
 - 2000-V Human Body Model (A114-A)
 - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 100mA per JESD 78, Class II
- Range of Package Options SOT353, DFN1410, and DFN1010
- Leadless packages per JESD30E
 - DFN1010 denoted as X2-DFN1010-6
 - DFN1014 denoted as X2-DFN1014-6
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments







DFN1010

Applications

- Suited for battery and low power needs
- Wide array of products such as:
 - Tablets, E-readers
 - Cell Phones, Personal Navigation / GPS
 - MP3 players ,Cameras, Video Recorders
 - PCs ultrabooks, notebooks, netbooks,
 - Computer peripherals, hard drives, CD/DVD ROM
 - TV, DVD, DVR, set top box

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

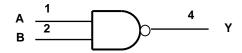
Click here for ordering information, located at the end of datasheet



Pin Descriptions

Pin Name	Function
Α	Data Input
В	Data Input
GND	Ground
Υ	Data Output
V _{CC}	Supply Voltage

Logic Diagram



Function Table

Inp	Inputs					
Α	A B					
L	L	Н				
L	Н	Н				
Н	L	Н				
Н	Н	L				



Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
V _{CC}	Supply Voltage Range	-0.5 to +4.6	V
VI	Input Voltage Range	-0.5 to +4.6	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V _{CC} +0.5	V
l _{IK}	Input Clamp Current V _I < 0	50	mA
I _{OK}	Output Clamp Current (V _O < 0)	50	mA
Io	Continuous Output Current (V _O = 0 to V _{CC})	±20	mA
Icc	Continuous Current Through V _{CC}	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Note:

Recommended Operating Conditions (Note 5) (@TA = +25°C, unless otherwise specified.)

Symbol	P	arameter	Min	Max	Unit
Vcc	Operating Voltage		0.8	3.6	V
VI	Input Voltage		0	3.6	V
Vo	Output Voltage		0	Vcc	V
		V _{CC} = 0.8V		-20	μΑ
		V _{CC} = 1.1V		-1.1	
1	High Loyal Output Current	V _{CC} = 1.4V		-1.7	
Іон	High-Level Output Current	V _{CC} = 1.65V		-1.9	mA
		V _{CC} = 2.3V		-3.1	
		V _{CC} = 3.0V		-4	
		V _{CC} = 0.8V		20	uA
		V _{CC} = 1.1V		1.1	
	Low-Level Output Current	V _{CC} = 1.4V		1.7	
I _{OL}	Low-Level Output Current	V _{CC} = 1.65V		1.9	mA
		V _{CC} = 2.3V		3.1	
		V _{CC} = 3.0V		4	
Δt/ΔV	Input Transition Rise or Fall Rate	V _{CC} = 0.8V to 3.6V		200	ns/V
TA	Operating Free-Air Temperature		-40	125	°C

Note: 5. Unused inputs should be held at V_{CC} or Ground.

^{4.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



Electrical Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	V	T _A = -	+25°C	T _A = -40°C	C to +85°C	Unit
Symbol	Parameter	lest Conditions	V _{CC}	Min	Max	Min	Max	Unit
			0.8V to 1.65V	0.80 X V _{CC}		0.80 X V _{CC}		
\	High-Level Input		1.65V to 1.95V	0.65 X V _{CC}		0.65 X V _{CC}		V
V_{IH}	Voltage		2.3V to 2.7V	1.6		1.6		V
			3.0V to 3.6V	2.0		2.0		
			0.8V to 1.65V		0.30 X V _{CC}		0.30 X V _{CC}	
VIL	Low-Level Input		1.65V to 1.95V		0.35 X V _{CC}		0.35 X V _{CC}	V
VIL	Voltage		2.3V to 2.7V		0.7		0.7	ď
			3.0V to 3.6V		0.9		0.9	
		$I_{OH} = -20\mu A$	0.8V to 3.6V	V _{CC} – 0.1		V _{CC} – 0.1		
		$I_{OH} = -1.1 \text{mA}$	1.1V	0.75 X V _{CC}		0.7 X V _{CC}		
		$I_{OH} = -1.7 \text{mA}$	1.4V	1.11		1.03		
	High-Level Output	$I_{OH} = -1.9 \text{mA}$	1.65V	1.32		1.3		٧
V_{OH}	OH Voltage	I _{OH} = -2.3mA	2.2)/	2.05		1.97		
	I _{OH} = -3.1mA	2.3V	1.9		1.85			
		I _{OH} = -2.7mA	0)/	2.72		2.67		
		I _{OH} = -4mA	3V	2.6		2.55		
		$I_{OL} = 20\mu A$	0.8V to 3.6V		0.1		0.1	
		I _{OL} = 1.1mA	1.1V		0.3 X V _{CC}		0.3 X V _{CC}	
		I _{OL} = 1.7mA	1.4V		0.31		0.37	
	High-Level Input	I _{OL} = 1.9mA	1.65V		0.31		0.35	١.,
V_{OL}	Voltage	I _{OL} = 2.3mA	2.01/		0.31		0.33	V
		I _{OL} = 3.1mA	2.3V		0.44		0.45	
		$I_{OL} = 2.7 \text{mA}$	21.6		0.31		0.33	
		I _{OL} = 4mA	- 3V		0.44		0.45	
II	Input Current	A or B Input V _I = GND to 3.6V	0 to 3.6V		± 0.1		± 0.5	μΑ
loff	Power Down Leakage Current	V_I or $V_O = 0V$ to 3.6V	0	_	0.2		0.6	μΑ
ΔI _{OFF}	Delta Power Down Leakage Current	V _I or V _O = 0V to 3.6V	0 V to 0.2 V		0.2		0.6	μA
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	0.8 V to 3.6V		0.5		0.9	μΑ
ΔI _{CC}	Additional Supply Current	One input at V _{CC} -0.6V Other inputs at V _{CC} or GND	3.3V		40		50	μΑ



Electrical Characteristics (cont.) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	V	T _A = -40°C	to +125°C	Unit
Symbol	Farameter	Test Conditions	V _{CC}	Min	Max	Offic
			0.8V to 1.65V	0.80 X V _{CC}		
ViH	High-Level Input Voltage		1.65V to 1.95V	0.70 X V _{CC}		V
VIH	High-Level Input Voltage		2.3V to 2.7V	1.6		V
			3.0V to 3.6V	2.0		
			0.8V to 1.65V		0.25 X V _{CC}	
VIL	Low-Level Input Voltage		1.65V to 1.95V		0.30 X V _{CC}	V
VIL	Low-Level input voitage		2.3V to 2.7V		0.7	V
			3.0V to 3.6V		0.9	
		$I_{OH} = -20\mu A$	0.8V to 3.6V	V _{CC} – 0.11		
		I _{OH} = -1.1mA	1.1V	0.6 X V _{CC}		
		I _{OH} = -1.7mA	1.4V	0.93		
	High Lavel Output Valtage	I _{OH} = -1.9mA	1.65V	1.17		V
V _{OH}	High-Level Output Voltage	I _{OH} = -2.3mA	0.01/	1.77		
		I _{OH} = -3.1mA	2.3V	1.67		
		I _{OH} = -2.7mA	0)/	2.40		
		I _{OH} = -4mA	3V	2.30		
		I _{OL} = 20μA	0.8V to 3.6V		0.11	
		I _{OL} = 1.1mA	1.1V		0.33 X V _{CC}	
		I _{OL} = 1.7mA	1.4V		0.41	
		I _{OL} = 1.9mA	1.65V		0.39	.,
V_{OL}	High-Level Input Voltage	I _{OL} = 2.3mA	2.21/		0.36	V
		I _{OL} = 3.1mA	2.3V		0.50	
		I _{OL} = 2.7mA	2) /		0.36	
		I _{OL} = 4mA	3V		0.50	
II	Input Current	A or B Input, V _I = GND to 3.6V	0 to 3.6V		± 0.75	μA
l _{OFF}	Power Down Leakage Current	V_1 or $V_0 = 0V$ to 3.6V	0		± 3.5	μA
Δl _{OFF}	Delta Power Down Leakage Current	V_I or $V_O = 0V$ to 3.6V	0V to 0.2V		± 2.5	μA
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	0.8V to 3.6V		3.0	μA
ΔI _{CC}	Additional Supply Current	Input at V _{CC} -0.6V Other inputs at V _{CC} or GND	3.3V		75	μΑ



Switching Characteristics

$C_L = 5pF$ see Figure 1

Parameter	From	то	·O V	T _A = +25°C			$T_A = -40$ °C to +85°C		T _A = -40°C to +125°C		Unit
Faranietei	Input	OUTPUT	Vcc	Min	Тур	Max	Min	Max	Min	Max	Onn
			0.8 V		17.5						
		or B Y	1.2 V ± 0.1 V	2.5	5.3	11.0	2.1	12.2	2.1	13.5]
	A or B		1.5 V ± 0.1 V	2.0	3.8	6.8	1.8	7.8	1.8	8.6	
t _{pd}	AUID		1.8 V ± 0.15 V	1.6	3.1	5.3	1.4	6.2	1.4	6.9	ns
			2.5 V ± 0.2 V	1.3	2.5	4.0	1.1	4.7	1.1	5.2	
			3.3 V ± 0.3 V	1.0	2.2	3.6	1.0	4.2	1.0	4.7	

C_L = 10pF see Figure 1

Parameter From Input	From	то	V	7	T _A = +25°	С	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		$T_A = -40$ °C	to +125°C	Unit
	OUTPUT	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit	
		V8.0		21.0							
		Y	1.2V ± 0.1V	2.4	6.1	13.0	2.2	14.4	2.2	15.9	ns
	A or B		1.5V ± 0.1V	2.4	4.4	7.9	2.2	9.2	2.2	10.2	
t _{pd}	AUID		1.8V ± 0.15V	2.0	3.7	6.2	1.9	7.3	1.9	8.1	
			$2.5V \pm 0.2V$	1.4	3.0	4.7	1.3	5.6	1.3	6.2	
			$3.3V \pm 0.3V$	1.3	2.8	4.3	1.2	4.9	1.2	5.4	

$C_L = 15pF$ see Figure 1

Parameter From Input		то	V	7	T _A = +25°0	2	$T_A = -40^{\circ}C$	to +85°C	$T_A = -40$ °C t	o +125°C	Unit
		OUTPUT	V _{CC}	Min	Тур	Max	Min	Max	Min	Max	Unit
		V8.0		24.5							
			1.2V ± 0.1V	3.4	6.9	14.8	3.1	16.5	3.1	18.2]
	۸ D		1.5V ± 0.1V	2.8	5.0	8.9	2.5	10.5	2.5	11.6	
t _{pd}	A or B	Y	1.8V ± 0.15V	2.0	4.1	7.0	2.0	8.3	2.0	9.2	ns
			2.5V ± 0.2V	1.7	3.5	5.3	1.5	6.4	1.5	7.1	1
			$3.3V \pm 0.3V$	1.6	3.2	4.9	1.4	5.7	1.4	6.3	1

$C_L = 30pF$ see Figure 1

Parameter	From	то	V _{CC}	7	Γ _A = +25°0	;	T _A = -40°C	to +85°C	$T_A = -40$ °C t	o +125°C	Unit
i arameter	Input	OUTPUT	V CC	Min	Тур	Max	Min	Max	Min	Max	Oiiit
			V8.0		34.8						
			1.2V ± 0.1V	4.6	17.5	22.0	4.1	22.6	4.1	24.9	
	Λ o π D		1.5V ± 0.1V	3.0	6.5	11.8	2.9	14.0	2.9	15.4	
t _{pd}	A or B	T T	1.8V ± 0.15V	2.6	5.4	9.3	2.3	11.1	2.3	12.3	ns
			2.5V ± 0.2V	2.4	4.6	7.1	2.1	8.5	2.1	9.4	
			$3.3V \pm 0.3V$	2.3	2.6	6.5	2.1	7.6	2.1	8.4	

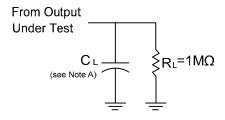


Operating and Package Characteristics (@T_A = +25°C, unless otherwise specified.)

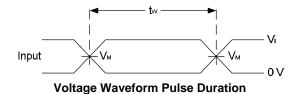
	Parameter		est litions	V _{CC}	Тур	Unit
				0.8V	6.5	
	C _{pd} Power Dissipation Capacitance			1.2V ± 0.1V	6.3	
0		f = 1	MHz	1.5V ± 0.1V	6.3	
$C_{\sf pd}$		No	Load	1.8V ± 0.15V	6.2	pF
				2.5V ± 0.2V	6.2	
				3.3V ± 0.3V	6.1	
Ci	Input Capacitance	$V_i = V_{CO}$	or GND	0V or 3.3V	1.5	pF
		SOT353			371	
θ_{JA}	Thermal Resistance Junction-to-Ambient	X2-DFN1410-6	(Note 6)		430	°C/W
	Junction-to-Ambient	X2-DFN1010-6			445	
		1 X 2-1)= N1 /4 1 ()-6 1 (NOTA 6			143	
θ_{JC}	θ _{JC} Thermal Resistance Junction-to-Case				190	°C/W
		X2-DFN1010-6			250	

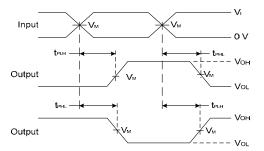
Note:

Parameter Measurement Information



Vcc	In	puts	V	6
VCC	Vı	t _r /t _f	V _M	CL
0.8V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30 pF
1.2V±0.1V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30 pF
1.5V±0.1V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30 pF
1.8V ±0.15V	Vcc	≤3ns	V _{CC} /2	5, 10, 15, 30 pF
2.5V±0.2V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30 pF
3.3V±0.3V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30 pF





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

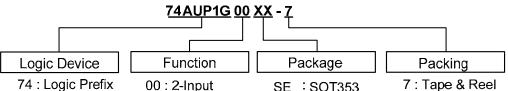
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD.}

^{6.} Test condition for SOT353, DFN1410, and DFN1010 devices mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



Ordering Information



AUP: 0.8 V to 3.6 V

Logic Family 1G : One gate 2-Input SE : SOT353 7 : T NAND-Gate FZ4 : X2-DFN1410-6

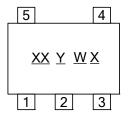
FZ4: X2-DFN1410-6 FW4: X2-DFN1010-6

	Device	Package Code	Packaging	7" Tape and Reel		
	Device	Fackage Code	Fackaging	Quantity	Part Number Suffix	
Pb,	74AUP1G00SE-7	SE	SOT353	3000/Tape & Reel	-7	
Pb,	74AUP1G00FZ4-7	FZ4	X2-DFN1410-6	5000/Tape & Reel	-7	
Pb,	74AUP1G00FW4-7	FW4	X2-DFN1010-6	5000/Tape & Reel	-7	

Marking Information

(1) SOT353

(Top View)



XX: Identification code

Y: Year 0~9

<u>W</u>: Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents

52 and 53 week X: A~Z: Internal code

Part Number	Package	Identification Code
74AUP1G00SE	SOT353	XH

(2) X2-DFN1410-6 and X2-DFN1010-6

(Top View)

<u>XX</u> • <u>Y W X</u> XX : Identification Code

Y : Year : 0~9

 $\overline{\underline{W}}$: Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents

52 and 53 week X: A~Z: Internal code

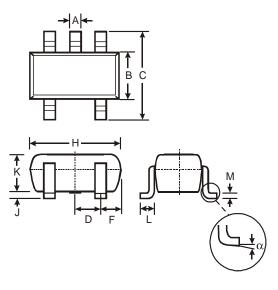
Part Number	Package	Identification Code
74AUP1G00FZ4	X2-DFN1410-6	XH
74AUP1G00FW4	X2-DFN1010-6	XH



Package Outline Dimensions (All dimensions in mm.)

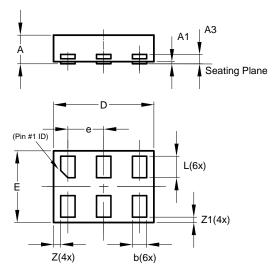
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

(1) SOT353



SOT353				
Dim	Min	Max	Тур	
Α	0.10	0.30	0.25	
В	1.15	1.35	1.30	
၁	2.00	2.20	2.10	
D	0.65 Typ			
F	0.40	0.45	0.425	
H	1.80	2.20	2.15	
ے	0	0.10	0.05	
K	0.90	1.00	1.00	
٦	0.25	0.40	0.30	
M	0.10	0.22	0.11	
α	0°	8°	-	
All Dimensions in mm				

(2) X2-DFN1410-6



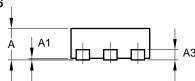
X2-DFN1410-6				
Dim	Min	Max	Тур	
Α	_	0.40	0.39	
A 1	0.00	0.05	0.02	
A3		_	0.13	
b	0.15	0.25	0.20	
D	1.35	1.45	1.40	
Е	0.95	1.05	1.00	
е	_		0.50	
L	0.25	0.35	0.30	
Z	_	_	0.10	
Z 1	0.045	0.105	0.075	
All Dimensions in mm				

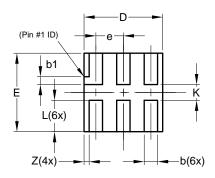


Package Outline Dimensions (cont.) (All dimensions in mm.)

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

(3) X2-DFN1010-6



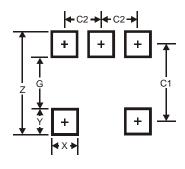


X2-DFN1010-6				
Dim	Min	Max	Тур	
Α		0.40	0.39	
A1	0.00	0.05	0.02	
A3	_		0.13	
b	0.14	0.20	0.17	
b1	0.05	0.15	0.10	
D	0.95	1.05	1.00	
Е	0.95	1.05	1.00	
е	_		0.35	
L	0.35	0.45	0.40	
K	0.15		_	
Z	_	_	0.065	
All Dimensions in mm				

Suggested Pad Layout

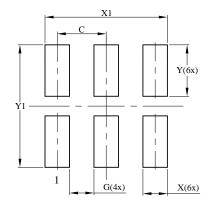
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version

(1) SOT353



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65

(2) X2-DFN1410-6



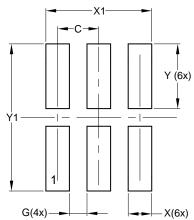
Dimensions	Value	
Dilliensions	(in mm)	
С	0.500	
G	0.250	
Х	0.250	
X1	1.250	
Y	0.525	
Y1	1.250	



Suggested Pad Layout (cont.)

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

(3) X2-DFN1010-6



Dimensions	Value (in mm)
С	0.350
G	0.150
Х	0.200
X1	0.900
Y	0.550
Y1	1.250

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