MORNSUN®

B_XT-1WR2 Series 1W, FIXED INPUT, ISOLATED & UNREGULATED SINGLE OUTPUT





Patent Protection RoHS

PART NUMBER SYSTEM

B0505XT-1WR2

Rated Power
Package Style
Output Voltage
Input Voltage
Product Series

FEATURES

- Small Footprint
- SMD Package Style
- 1500VDC Isolation
- Low Temperature rise
- Internal SMD construction
- No External Component Required
- Industry Standard Pinout

APPLICATIONS

The B_XT-1WR2 Series are designed for application where isolated output is required from a distributed power system.

These products apply to where:

- 1) Input voltage variation ≤ ±10%;
- 2) 1.5KVDC input and output isolation;
- 3) Regulated and low ripple noise is not required.

Such as: digital circuits, low frequency analog circuits, and IGBT power device driving circuits.

Model Number	Input Voltage(VDC)	Output Voltage	Output Current (mA)		Input Current (mA)(Typ.)		Reflected Ripple	Max. Capacitive	Efficiency (%, Typ.)	Approva
	Nominal (Range)	(VDC)	Max.	Min.	@Max. Load	@No Load	Current (mA,Typ.)	Load(µF)	@Max. Approva	Арргоча
B0505XT-1WR2		5	200	20	250				80	
*B0509XT-1WR2		9	111	12	250				80	
B0512XT-1WR2	5 (4.5-5.5)	12	84	9	250	20	15		80	
B0515XT-1WR2	(4.5-5.5)	15	67	7	250				80	
B0524XT-1WR2	-	24	42	4	250				80	
B1205XT-1WR2	12 (10.8-13.2)	5	200	20	104	15	5	220	80	
*B1209XT-1WR2		9	111	12	104				80	
B1212XT-1WR2		12	84	9	103				81	
*B1215XT-1WR2	(10.0 10.2)	15	67	7	103				81	
*B1224XT-1WR2		24	42	4	103				81	
*B1505XT-1WR2	15	5	200	20	83	10	5		80	
*B1515XT-1WR2	(13.5-16.5)	15	67	7	82	10	Э		81	
B2405XT-1WR2		5	200	20	52				80	
*B2409XT-1WR2		9	110	11	52		5	5	80	
*B2412XT-1WR2	24 (21.6-26.4)	12	83	8	51	7			81	
B2415XT-1WR2	(21.0 20.1)	15	67	7	51				81	
*B2424XT-1WR2		24	42	4	51				81	

INPUT SPECIFICATIO	NS				
Item	Test Conditions	Min.	Тур.	Max.	Unit
Input Surge Voltage (1 sec. max.)	5VDC Input	-0.7		9	- VDC
	12VDC Input	-0.7		18	
	15VDC Input	-0.7		21	
	24VDC Input	-0.7		30	
Input Filter			Capacita	nce Filter	

OUTPUT SPECIFICAT	IONS						
Item	Test Conditions	Test Conditions		Тур.	Max.	Unit	
Output Power					1	W	
Output Voltage Accuracy			See tolerance envelope curve				
Line Regulation	For Vin change of±19	For Vin change of±1%			±1.2		
	10% to 100% load	5Voutput		12	15	%	
		9V output		8	10		
Load Regulation		12V output		7	10		
		15V output		6	10		
		24V output		5	10		
Temperature Drift	100% full load	·			±0.03	%/°C	
Dinala 9 Naisa*	20111 5 1 1 11	Output Voltage ≤12V		30		,,	
Ripple & Noise*	20MHz Bandwidth	Output Voltage: 15V, 24V		60		mVp-p	
Short Circuit Protection				Continuous, automatic recovery			
Note: * Ripple and noise tested by '	"parallel cable" method. See det	ailed operation instructions at Testing	of Power Converte	r section, applicat	ion notes.	(

COMMON SPECIFICA	ATIONS					
Item	Test Conditions	Test Conditions			Max.	Unit
Isolation Voltage	Tested for 1 minute and leakage	Tested for 1 minute and leakage current less than 1 mA				VDC
Isolation Resistance	Test at 500VDC	Test at 500VDC		- 1		МΩ
Isolation Capacitance	Input/Output,100KHz/0.1V	B2424XT-1WR2		30		pF
	input/Output, 100KH2/0.1V	Others		20		
Switching Frequency	Full load, nominal input			100	300	KHz
MTBF	MIL-HDBK-217F@25℃		3500			K hours
Case Material				Epoxy Resi	n (UL94-V0)	
Weight				1.52		g

ENVIRONMENTAL SPECIFICATIONS						
Item	Test Conditions	Min.	Тур.	Max.	Unit	
Storage Humidity	Non condensing			95	%	
Operating Temperature	Power derating (above100°C)	-40		105		
Storage Temperature		-55		125	°C	
Temp. rise at full load	Ta=25°C		25			
Lead Temperature	1.5mm from case for 10 seconds			300		
Cooling			Free air	convection		

EMC SPECIFICATIONS						
ЕМІ	CE	CISPR22/EN55022 CLASS B (External Circuit Refer to Figure1)				
EMS	ESD	IEC/EN61000-4-2 Contact ±8KV perf. Criteria B				

EMI RECOMMENDED CIRCUIT

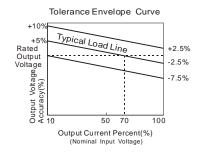
EMI Recommended External Circuit (CLASS B):

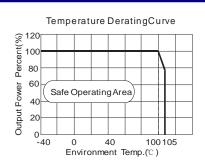
Vin O Vin +Vo
EUT
GND OV
(Figure1)

Recommended external circuit parameters:

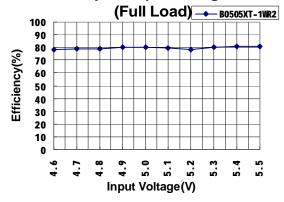
Vin: 5V/12V C1: 4.7μF/50V LDM: 6.8μH Vin: 15V/24V C1: 4.7μF/50V LDM: 6.8μH C2: 470pF/2KV

PRODUCT TYPICAL CURVE

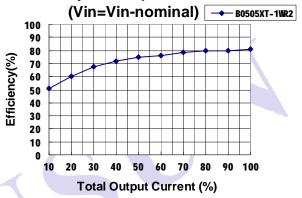




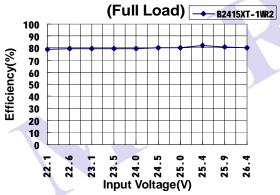




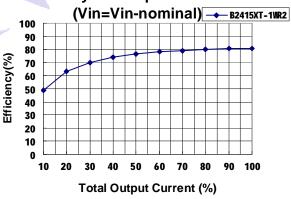
Efficiency VS Output Load curve



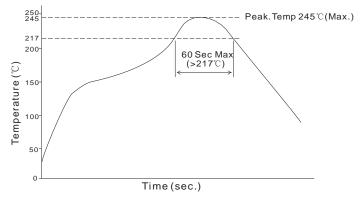
Efficiency VS Input Voltage curve



Efficiency VS Output Load curve

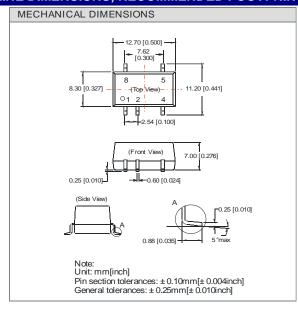


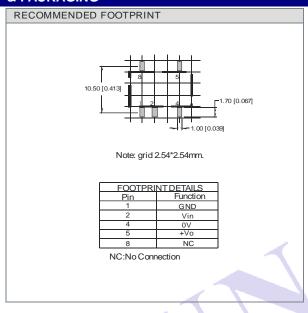
Recommended reflow soldering profile refer to IPC/JEDEC J-STD-020D standard, our products recommend reflow soldering profile as follows:

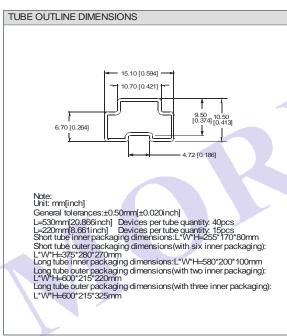


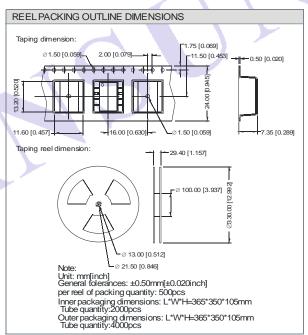
Note: The curve applies only to the hot air reflow soldering

OUTLINE DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING





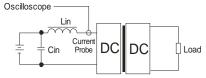




TEST CONFIGURATIONS

Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.



Lin(4.7μH) Cin(220μF, ESR < 1.0Ω at 100 KHz)

DESIGN CONSIDERATIONS

1) Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load could not be less than 10% of the full load. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load.

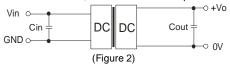
2) Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to add a circuit breaker to the circuit.

3) Recommended circuit

If you want to further decrease the input/output ripple, an capacitor filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 2).

It should also be noted that the capacitance of filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the recommended capacitance of its filter capacitor sees (Table 1).

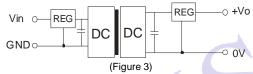


EXTERNAL CAPACITOR TABLE (Table 1)						
Vin (VDC)	Cin (µF)	Vo (VDC)	Cout (µF)			
5	4.7	5	10			
12	2.2	9	4.7			
15	2.2	12	2.2			
24	1	15	1			
-		24	0.47			

It's not recommended to connect any external capacitor in the application field with less than 0.5 watt output.

4) Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear regulator and an capacitor filtering network with overheat protection that is connected to the input or output end in series (Figure 3), the recommended capacitance of its filter capacitor sees (Table 1), linear regulator based on the actual voltage and current to reasonable selection.



5) Cannot use in parallel and hot swap

Note:

- 1. Operation under minimum load will not damage the converter; However, they may not meet all specification listed.
- 2. Max. Capacitive Load tested at input voltage range and full load.
- 3. All specifications measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 4. In this datasheet, all the test methods of indications are based on our corporate standards.
- 5. All characteristics are for listed model only, non-standard models may perform differently, please contact our technical person for more detail.
- 6. Contact us for your specific requirement.
- 7. Specifications subject to change without prior notice.

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