

## Features

- Dual P-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **ESD Protected Up To 3KV**
- **"Green" Device (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

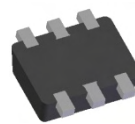
- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish - Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.006 grams (approximate)



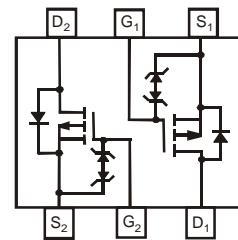
ESD PROTECTED



TOP VIEW



BOTTOM VIEW



TOP VIEW

## Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	-20	V
Gate-Source Voltage			$V_{GSS}$	$\pm 6$	V
Continuous Drain Current (Note 3) $V_{GS} = -4.5\text{V}$	Steady State	$T_A = 25^\circ\text{C}$ $T_A = 85^\circ\text{C}$	$I_D$	-1.03 -0.68	A
Pulsed Drain Current (Note 4)			$I_{DM}$	-3	A

## Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	$P_D$	530	mW
Thermal Resistance, Junction to Ambient @ $T_A = 25^\circ\text{C}$ (Note 3)	$R_{\theta JA}$	235	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

- Notes:
1. No purposefully added lead.
  2. Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).
  3. Device mounted on FR-4 PCB, with minimum recommended pad layout.
  4. Repetitive rating, pulse width limited by junction temperature.

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition	
<b>OFF CHARACTERISTICS (Note 5)</b>							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	-	-	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	-	-	-100	nA	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±2.0	μA	V <sub>GS</sub> = ±4.5V, V <sub>DS</sub> = 0V	
<b>ON CHARACTERISTICS (Note 5)</b>							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.5	-	-1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	-	0.5	0.75	Ω	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -430mA	
			0.7	1.05			V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -300mA
			1.0	1.5			V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -150mA
			-	20			V <sub>GS</sub> = -1.7V, I <sub>D</sub> = -100mA
			-	25			V <sub>GS</sub> = -1.5V, I <sub>D</sub> = -100mA
Forward Transfer Admittance	Y <sub>fs</sub>	-	0.9	-	S	V <sub>DS</sub> = -10V, I <sub>D</sub> = -250mA	
Diode Forward Voltage	V <sub>SD</sub>	-	-0.8	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -150mA	
<b>DYNAMIC CHARACTERISTICS (Note 6)</b>							
Input Capacitance	C <sub>iss</sub>	-	59.76	-	pF	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Output Capacitance	C <sub>oss</sub>	-	12.07	-	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	6.36	-	pF		
Total Gate Charge	Q <sub>g</sub>	-	622.4	-	pC	V <sub>GS</sub> = -4.5V, V <sub>DS</sub> = -10V, I <sub>D</sub> = -250mA	
Gate-Source Charge	Q <sub>gs</sub>	-	100.3	-	pC		
Gate-Drain Charge	Q <sub>gd</sub>	-	132.2	-	pC		
Turn-On Delay Time	t <sub>D(on)</sub>	-	5.1	-	ns	V <sub>DD</sub> = -10V, V <sub>GS</sub> = -4.5V, R <sub>L</sub> = 47Ω, R <sub>G</sub> = 10Ω, I <sub>D</sub> = -200mA	
Turn-On Rise Time	t <sub>r</sub>	-	8.1	-	ns		
Turn-Off Delay Time	t <sub>D(off)</sub>	-	28.4	-	ns		
Turn-Off Fall Time	t <sub>f</sub>	-	20.7	-	ns		

Notes: 5. Short duration pulse test used to minimize self-heating effect.  
6. Guaranteed by design. Not subject to production testing.

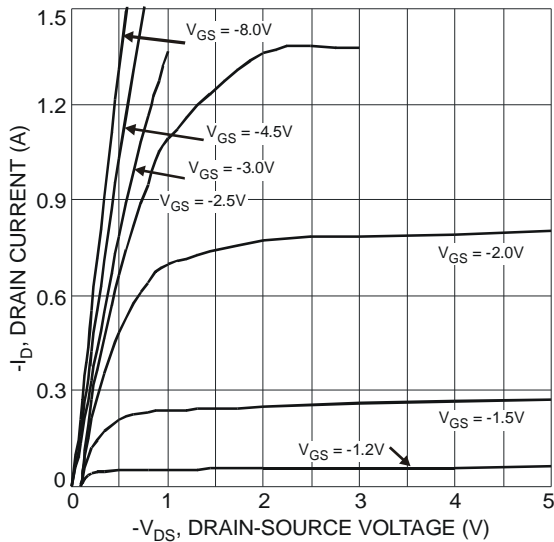


Fig. 1 Typical Output Characteristic

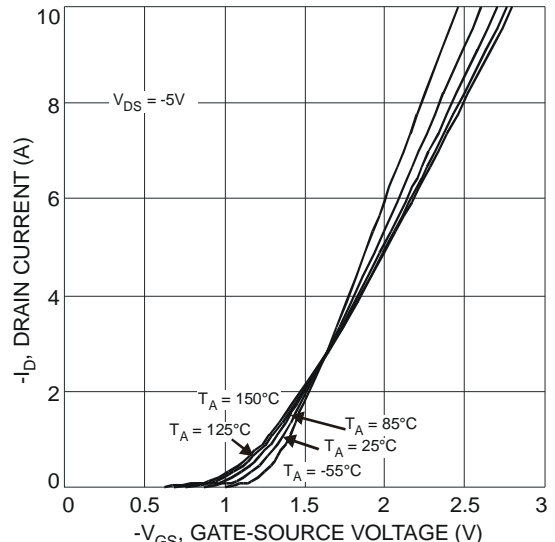
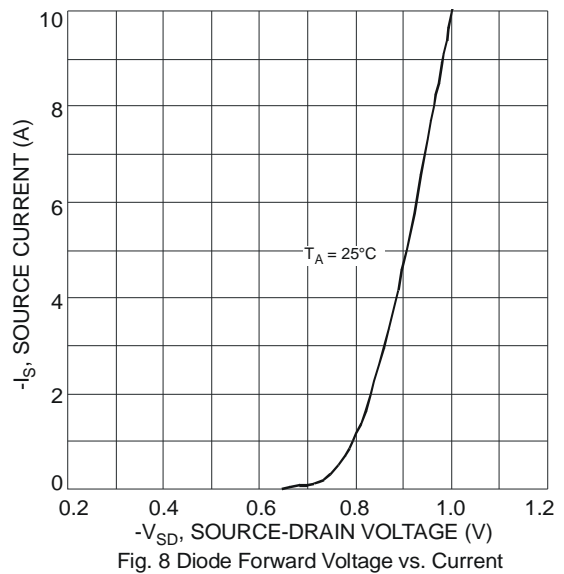
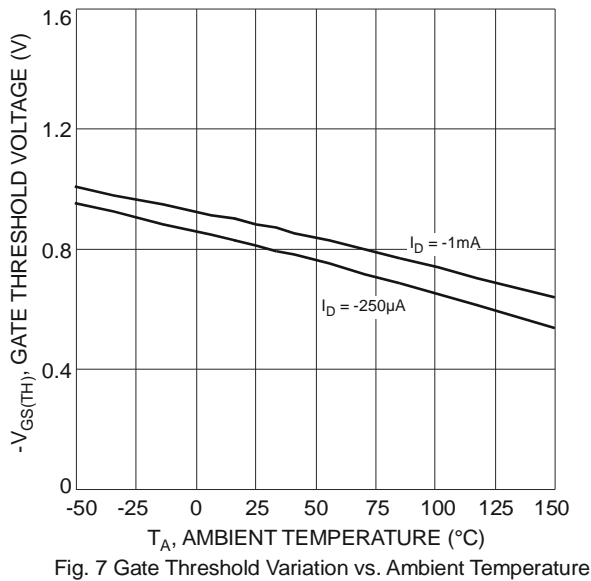
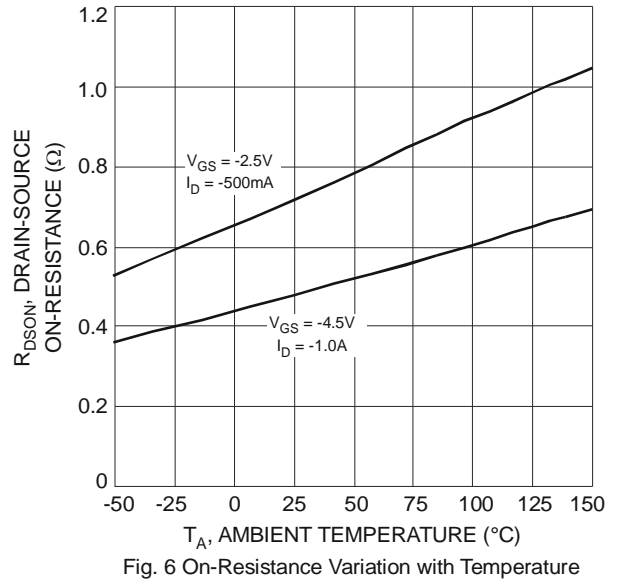
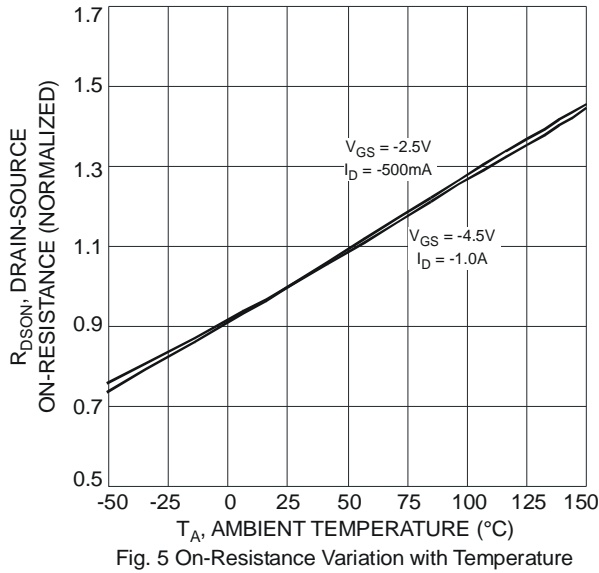
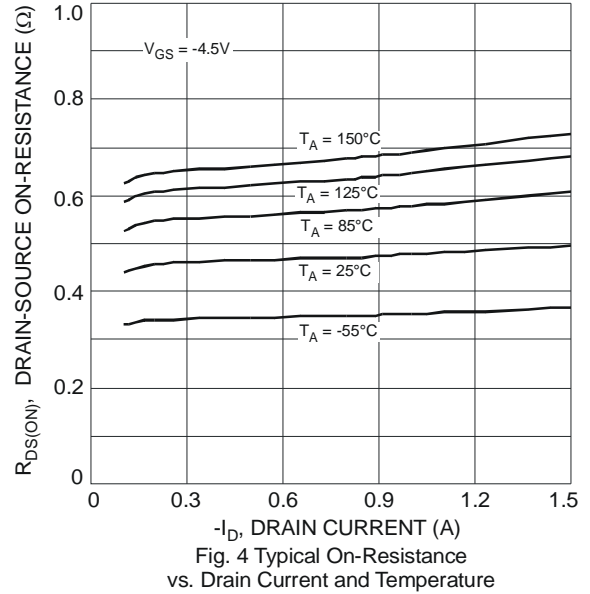
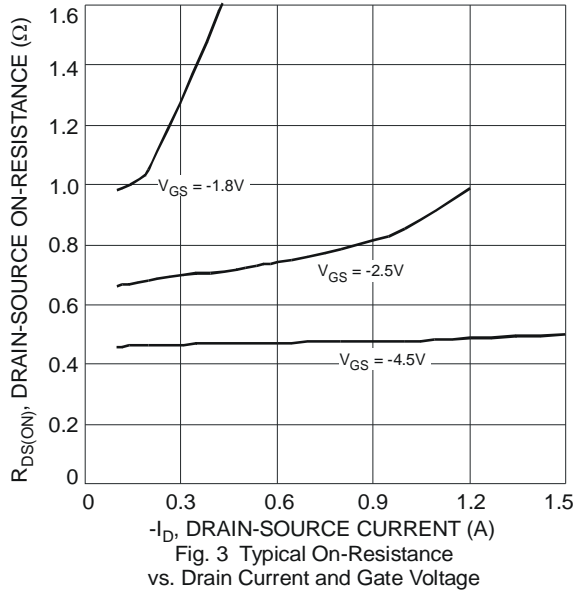


Fig. 2 Typical Transfer Characteristic



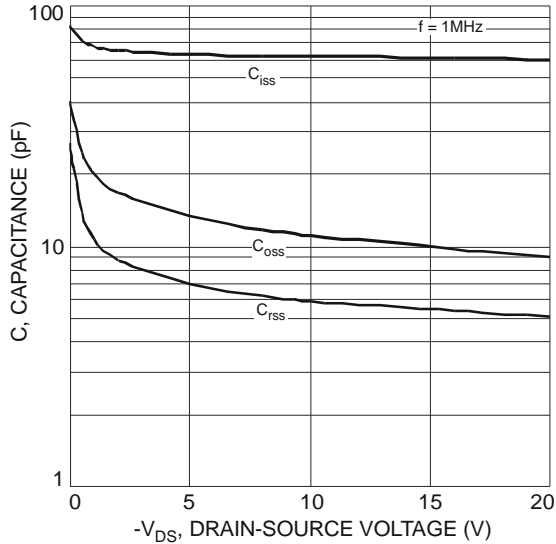


Fig. 9 Typical Total Capacitance

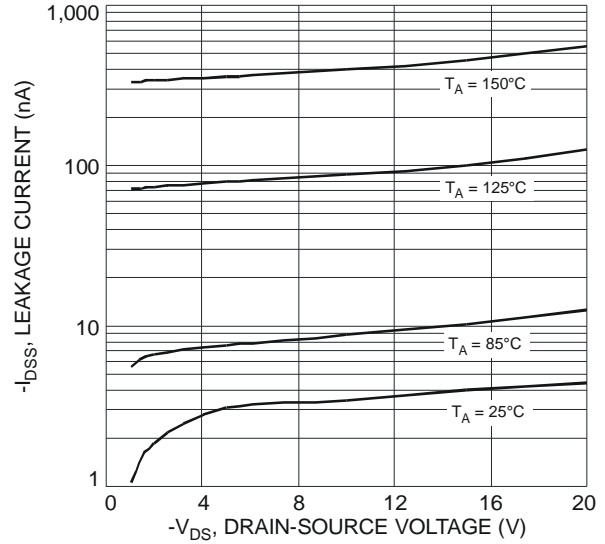


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

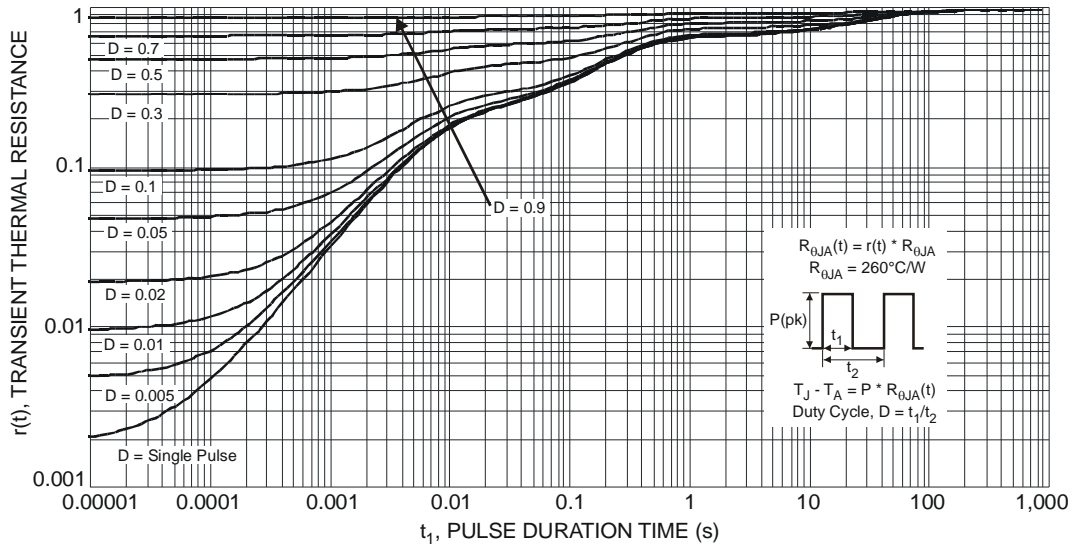


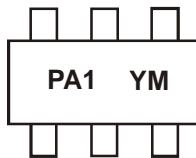
Fig. 11 Transient Thermal Response

**Ordering Information** (Note 7)

Part Number	Case	Packaging
DMG1023UV-7	SOT-563	3000 / Tape & Reel

Notes: 7. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

**Marking Information**



PA1 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: W = 2009)  
 M = Month (ex: 9 = September)

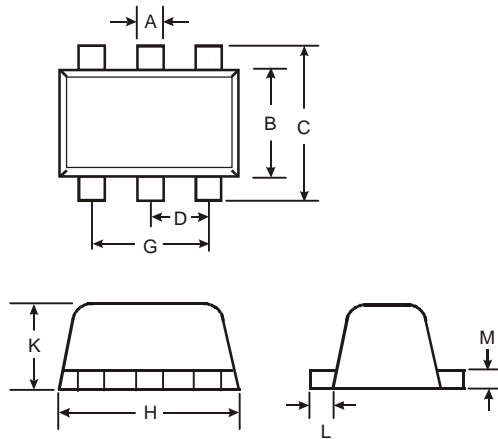
Date Code Key

Year	2008	2009	2010	2011	2012	2013	2014	2015
Code	V	W	X	Y	Z	A	B	C

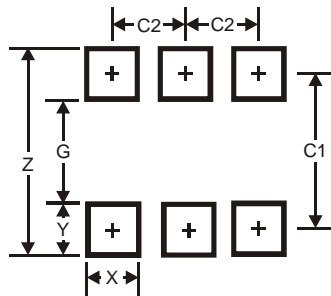
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Package Outline Dimensions**



SOT-563			
Dim	Min	Max	Typ
A	0.15	0.30	0.20
B	1.10	1.25	1.20
C	1.55	1.70	1.60
D	-	-	0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
K	0.55	0.60	0.60
L	0.10	0.30	0.20
M	0.10	0.18	0.11
All Dimensions in mm			

**Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Y	0.5
C1	1.7
C2	0.5

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