# **Dual General Purpose Transistor**

The MBT3906DW1T1G device is a spin-off of our popular SOT-23/SOT-323 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-363 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

### **Features**

- h<sub>FF</sub>, 100-300
- Low  $V_{CE(sat)}$ ,  $\leq 0.4 \text{ V}$
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- Available in 8 mm, 7-inch/3,000 Unit Tape and Reel
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V <sub>CEO</sub>	-40	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	-40	Vdc
Emitter – Base Voltage	V <sub>EBO</sub>	-5.0	Vdc
Collector Current - Continuous	Ic	-200	mAdc
Electrostatic Discharge	ESD	HBM Class 2 MM Class B	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Package Dissipation (Note 1) T <sub>A</sub> = 25°C	P <sub>D</sub>	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	833	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

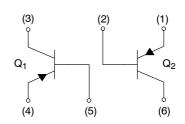
Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.

1



# ON Semiconductor®

http://onsemi.com





SOT-363/SC-88 CASE 419B STYLE 1

## **MARKING DIAGRAM**



A2 = Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

# **ORDERING INFORMATION**

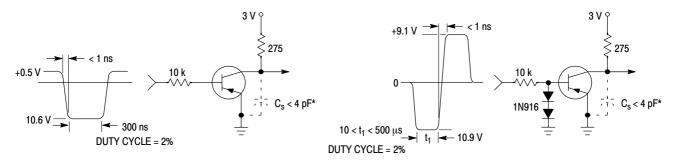
Device	Package	Shipping <sup>†</sup>		
MBT3906DW1T1G	SOT-363 (Pb-Free)	3000 / Tape & Reel		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

	Characteristic	Symbol	Min	Max	Unit	
OFF CHARACT	ERISTICS				•	
Collector - Emitte	r Breakdown Voltage (Note 2)	V <sub>(BR)CEO</sub>	-40	_	Vdc	
Collector - Base E	Breakdown Voltage	V <sub>(BR)CBO</sub>	-40	-	Vdc	
Emitter - Base Bro	eakdown Voltage	V <sub>(BR)EBO</sub>	-5.0	-	Vdc	
Base Cutoff Curre	ent	I <sub>BL</sub>	-	-50	nAdc	
Collector Cutoff C	Current	I <sub>CEX</sub>	-	-50	nAdc	
ON CHARACTE	ERISTICS (Note 2)	<u> </u>	•	•		
$(I_C = -1.0 \text{ mAd})$ $(I_C = -10 \text{ mAdd})$ $(I_C = -50 \text{ mAdd})$	c, $V_{CE} = -1.0 \text{ Vdc}$ ) c, $V_{CE} = -1.0 \text{ Vdc}$ ) c, $V_{CE} = -1.0 \text{ Vdc}$ ) c, $V_{CE} = -1.0 \text{ Vdc}$ ) dc, $V_{CE} = -1.0 \text{ Vdc}$ )	h <sub>FE</sub>	60 80 100 60 30	- - 300 - -	_	
$(I_C = -10 \text{ mAdd})$	r Saturation Voltage c, $I_B = -1.0$ mAdc) c, $I_B = -5.0$ mAdc)	V <sub>CE(sat)</sub>	- -	-0.25 -0.4	Vdc	
	sturation Voltage c, $I_B = -1.0$ mAdc) c, $I_B = -5.0$ mAdc)	V <sub>BE(sat)</sub>	-0.65 -	-0.85 -0.95	Vdc	
SMALL-SIGNA	L CHARACTERISTICS				•	
Current-Gain - E	Bandwidth Product	f <sub>T</sub>	250	_	MHz	
Output Capacitan	nce	C <sub>obo</sub>	-	4.5	pF	
Input Capacitance	е	C <sub>ibo</sub>	-	10.0	pF	
Input Impedance (V <sub>CE</sub> = -10 Vdd	c, I <sub>C</sub> = -1.0 mAdc, f = 1.0 kHz)	h <sub>ie</sub>	2.0	12	kΩ	
Voltage Feedback (V <sub>CE</sub> = -10 Vdd	k Ratio c, I <sub>C</sub> = -1.0 mAdc, f = 1.0 kHz)	h <sub>re</sub>	0.1	10	X 10 <sup>-4</sup>	
Small – Signal Cu (V <sub>CE</sub> = –10 Vdd	rrent Gain c, I <sub>C</sub> = -1.0 mAdc, f = 1.0 kHz)	h <sub>fe</sub>	h <sub>fe</sub> 100		-	
Output Admittance (V <sub>CE</sub> = -10 Vdc	ne c, I <sub>C</sub> = -1.0 mAdc, f = 1.0 kHz)	h <sub>oe</sub>	h <sub>oe</sub> 3.0 60		μmhos	
Noise Figure (V <sub>CE</sub> = -5.0 Vd	lc, $I_C = -100 \mu Adc$ , $R_S = 1.0 k \Omega$ , $f = 1.0 kHz$ )	NF	-	4.0	dB	
SWITCHING CH	IARACTERISTICS					
Delay Time	$(V_{CC} = -3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc})$	t <sub>d</sub>	_	35	35 ns	
Rise Time	$(I_C = -10 \text{ mAdc}, I_{B1} = -1.0 \text{ mAdc})$	t <sub>r</sub>	-	35		
Storage Time	$(V_{CC} = -3.0 \text{ Vdc}, I_{C} = -10 \text{ mAdc})$	t <sub>s</sub>	_	225		
Fall Time	(lp4 - lp01 0 mAdc)	te	_	75	ns	

Fall Time  $(I_{B1} = I_{B2} = -1.0 \text{ mAdc})$ 2. Pulse Test: Pulse Width  $\leq$  300  $\mu$ s; Duty Cycle  $\leq$  2.0%.



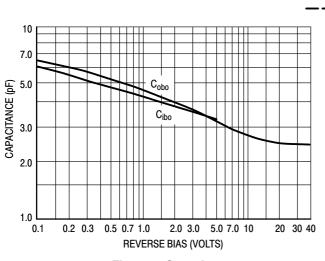
\* Total shunt capacitance of test jig and connectors

Figure 1. Delay and Rise Time Equivalent Test Circuit

Figure 2. Storage and Fall Time Equivalent Test Circuit

# TYPICAL TRANSIENT CHARACTERISTICS

- T<sub>J</sub> = 25°C



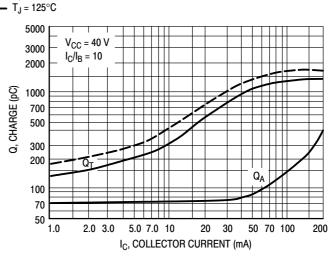
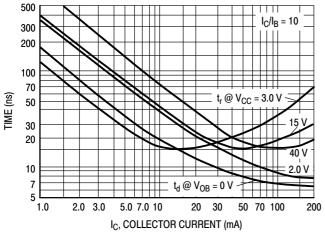


Figure 3. Capacitance

Figure 4. Charge Data



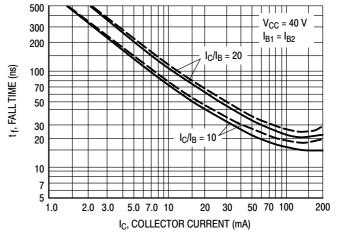
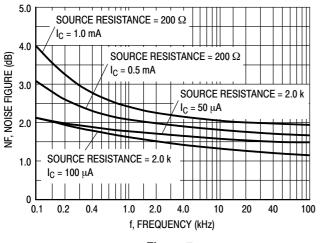


Figure 5. Turn - On Time

Figure 6. Fall Time

# TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

 $(V_{CE} = -5.0 \text{ Vdc}, T_A = 25^{\circ}\text{C}, Bandwidth = 1.0 \text{ Hz})$ 

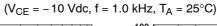


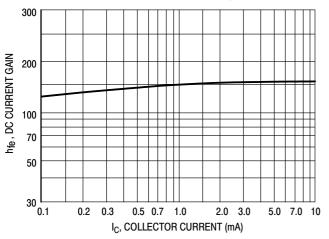
12 f = 1.0 kHz10 NF, NOISE FIGURE (dB) 8 6 4  $I_C$  = 50  $\mu$ A I<sub>C</sub> = 100 μA 2 0.1 0.2 0.4 1.0 2.0 4.0 10 40 100 R<sub>0</sub>, SOURCE RESISTANCE (k OHMS)

Figure 7.

Figure 8.

# **h PARAMETERS**





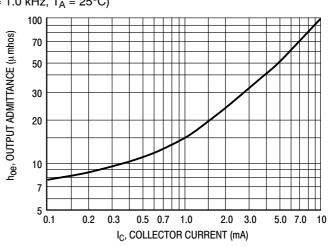
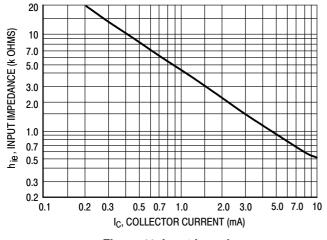


Figure 9. Current Gain

Figure 10. Output Admittance



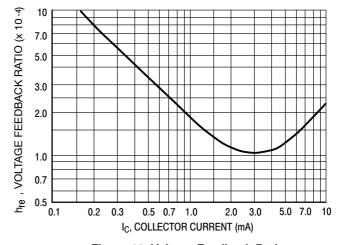


Figure 11. Input Impedance

Figure 12. Voltage Feedback Ratio

## TYPICAL STATIC CHARACTERISTICS

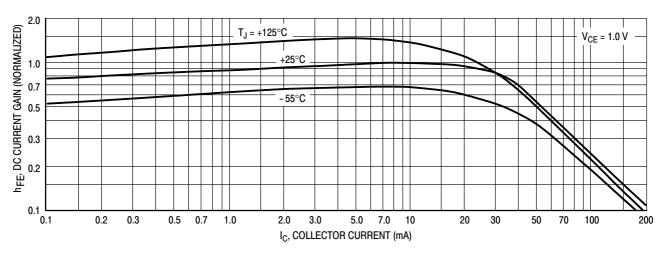


Figure 13. DC Current Gain

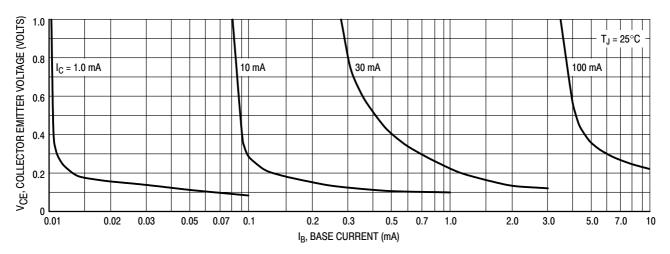


Figure 14. Collector Saturation Region

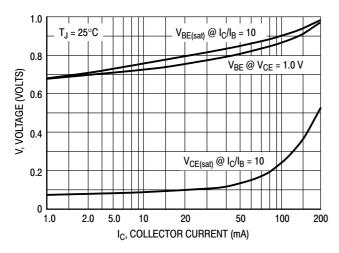


Figure 15. "ON" Voltages

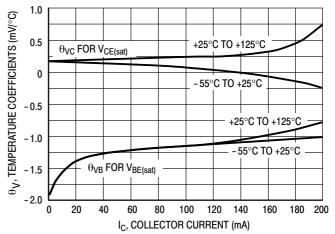
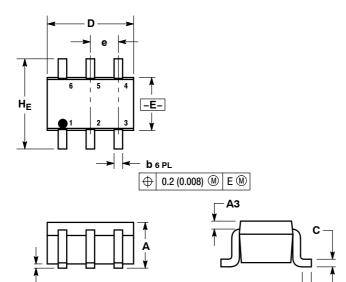


Figure 16. Temperature Coefficients

### PACKAGE DIMENSIONS

## SC-88/SC70-6/SOT-363 CASE 419B-02 **ISSUE W**



#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI
- 2 CONTROLLING DIMENSION: INCH
- 3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

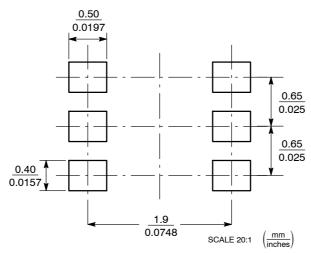
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
А3	0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012
С	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
Е	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65 BSC		0.026 BSC			
L	0.10	0.20	0.30	0.004	0.008	0.012
He	2 00	2 10	2 20	0.078	0.082	0.086

### STYLE 1:

PIN 1. EMITTER 2

- 2. BASE 2
- 3. COLLECTOR 1
- 4. EMITTER 1
- 5. BASE 1 6. COLLECTOR 2

## **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice on semiconductor and war engineer trademarks of semiconductor components industries, Ite (SciLLC) solitate services are injective to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### **PUBLICATION ORDERING INFORMATION**

### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative