

# **SDB330B**

**Schottky Barrier Rectifier** 

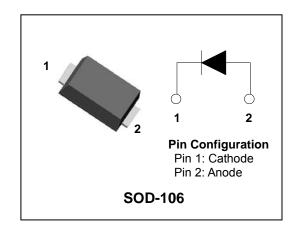
## 30V, 3A SCHOTTKY BARRIER RECTIFIER

#### **Features**

- · Low forward voltage drop
- Low power loss and High efficiency
- · Low leakage current
- · High surge capability
- Full lead (Pb)-free and RoHS compliant device

### **Applications**

- High efficiency SMPS
- · Output rectification
- · High frequency switching
- Freewheeling
- DC-DC converter systems



## **Description**

The SDB330B is suited for Switch Mode Power Supply and high frequency DC to DC converters. This device is especially intended for use in low voltage, high frequency inverters, freewheeling and polarity protection applications.

## **Ordering Information**

Device	Marking Code	Package	Packaging
SDB330B	3A30B	SOD-106	Tape & Reel

### **Marking Information**



3A30B = Specific Device Code

YWW = Year & Week Code Marking

-. Y = Year Code

-. WW = Week Code

= Color band denote cathode

KSD-D6A002-002

## **Absolute Maximum Ratings** (Rating at 25℃ ambient temperature unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak reverse voltage	$V_{RM}$	30	٧
Reverse voltage	$V_R$	30	٧
Forward current	I <sub>F</sub>	3	Α
Peak surge forward current (Non-repetitive 60Hz sine wave)	I <sub>FSM</sub>	30	Α
Junction temperature	TJ	150	°C
Storage temperature range	$T_{stg}$	-55~150	°C

## **Electrical Characteristics** (Rating at 25℃ ambient temperature unless otherwise specified.)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Forward voltage	$V_F^{1)}$	I <sub>F</sub> =3A	-	-	0.5	V
Reverse current	I <sub>R</sub>	V <sub>R</sub> =30V	-	-	0.35	mA
Total capacitance	Ст	V <sub>R</sub> =10V, f=1MH <sub>Z</sub>	-	160	-	pF
Thermal resistance	R <sub>th(j-a)</sub>	Junction to ambient 2)	-	-	76	°C/W

<sup>\* 1)</sup> Pulse test : t<sub>P</sub>≤380 \( \mu\_S \), Duty cycle≤2%

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<sup>\* 2)</sup> Device mounted on glass epoxy PCB (recommanderable minimum solder land)

## **Electrical Characteristic Curves**

Fig. 1  $I_F$  -  $V_F$ 

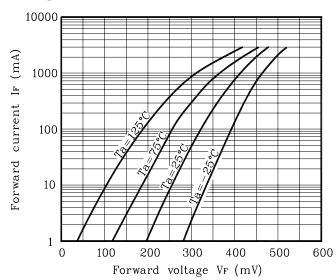


Fig. 2  $I_{R}$  -  $V_{R}\,$ 

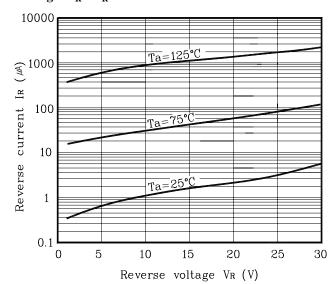
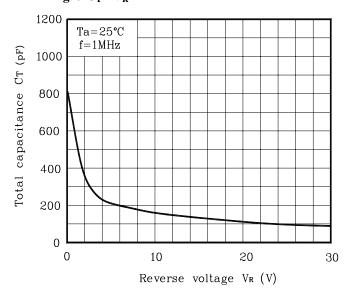
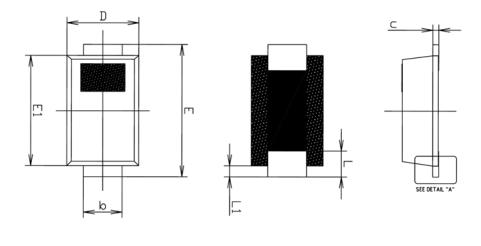
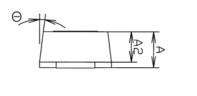


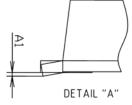
Fig. 3  $C_T$  -  $V_R$ 



## **Package Outline Dimension**

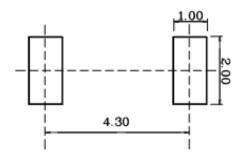






SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	NOTE
Α	1.25	1.30	1.35	
A1	0.00	_	0.10	
A2	1.05	1.10	1.15	
Ь	1.35	1.42	1.49	
С	0.17	0.22	0.27	
D	2.50	2.60	2.70	
Ε	4.60	4.80	5.00	
E1	3.90	4.00	4.10	
L	0.79	0.94	1.09	
L1	0.30	0.40	0.50	
Θ	4°	_	10°	

## Recommend PCB Solder Land Dimension (Unit: mm)



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