

RoHS Compliant Product  
A suffix of "-C" specifies halogen & lead-free

## DESCRIPTION

The GM3055 provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness. The SOT-89 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

## FEATURES

- Fast Switching
- Dynamic dv/dt Rating
- Repetitive Avalanche Rated
- Simple Drive Requirement

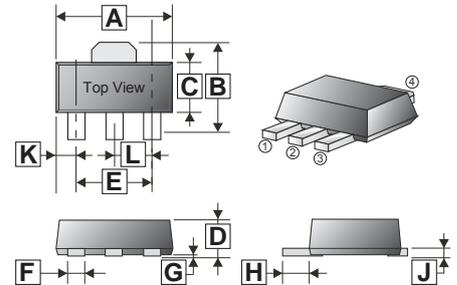
## MARKING



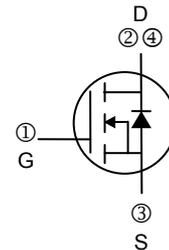
## PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-89	1K	7 inch

## SOT-89



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.40	4.60	G	-	-
B	4.05	4.25	H	0.89	1.20
C	2.40	2.60	J	0.35	0.41
D	1.40	1.60	K	0.70	0.80
E	3.00 REF.		L	1.50 REF.	
F	0.40	0.52			



## ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current <sup>3</sup>	I <sub>D</sub>	T <sub>A</sub> = 25°C	6
		T <sub>A</sub> = 70°C	4.8
Pulsed Drain Current <sup>1,2</sup>	I <sub>DM</sub>	20	A
Power Dissipation	P <sub>D</sub>	1.2	W
Linear Derating Factor		0.016	W / °C
Operating Junction & Storage Temperature	T <sub>J</sub> , T <sub>STG</sub>	-55~150	°C
<b>Thermal Resistance Rating</b>			
Thermal Resistance Junction-Ambient <sup>3</sup> (Max).	R <sub>θJA</sub>	104	°C / W

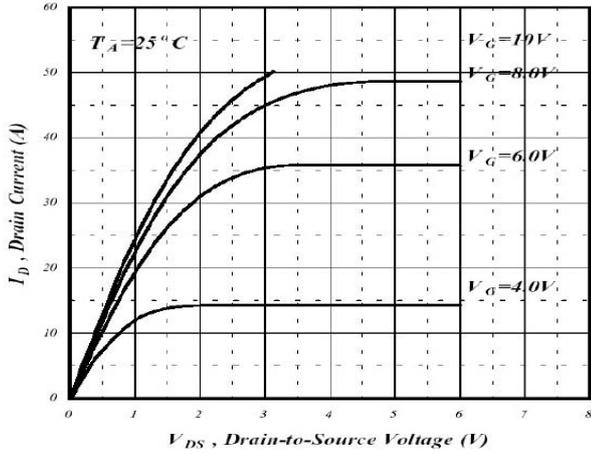
**ELECTRICAL CHARACTERISTICS** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
Drain-Source Breakdown Voltage	$BV_{DSS}$	30	-	-	V	$V_{GS}=0, I_D=250\mu\text{A}$	
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	-	0.037	-	V / $^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	
Gate Threshold Voltage	$V_{GS(th)}$	1	-	3	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	
Gate-Source Leakage Current	$I_{GSS}$	-	-	$\pm 100$	nA	$V_{GS}=\pm 20\text{V}$	
Drain-Source Leakage Current	$I_{DSS}$	$T_J=25^\circ\text{C}$	-	-	25	$\mu\text{A}$	$V_{DS}=30\text{V}, V_{GS}=0$
		$T_J=70^\circ\text{C}$	-	-	250		$V_{DS}=24\text{V}, V_{GS}=0$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	-	-	26	m $\Omega$	$V_{GS}=10\text{V}, I_D=4\text{A}$	
		-	-	40		$V_{GS}=4.5\text{V}, I_D=3\text{A}$	
Total Gate Charge <sup>2</sup>	$Q_g$	-	5.4	-	nC	$I_D=4\text{A}$ $V_{DS}=24\text{V}$ $V_{GS}=5\text{V}$	
Gate-Source Charge	$Q_{gs}$	-	1.3	-			
Gate-Drain ("Miller") Charge	$Q_{gd}$	-	3.6	-			
Turn-on Delay Time <sup>2</sup>	$T_{d(on)}$	-	3.6	-	nS	$V_{DD}=15\text{V}$ $I_D=1\text{A}$ $V_{GS}=10\text{V}$ $R_G=3.3\Omega$ $R_D=1.9\Omega$	
Rise Time	$T_r$	-	19.8	-			
Turn-off Delay Time	$T_{d(off)}$	-	13	-			
Fall Time	$T_f$	-	3.2	-			
Input Capacitance	$C_{iss}$	-	260	-	pF	$V_{GS}=0$ $V_{DS}=30\text{V}$ $f=1.0\text{MHz}$	
Output Capacitance	$C_{oss}$	-	144	-			
Reverse Transfer Capacitance	$C_{rss}$	-	13	-			
<b>Source-Drain Diode</b>							
Forward On Voltage <sup>2</sup>	$V_{SD}$	-	-	1.3	V	$I_S=2\text{A}, V_{GS}=0, T_J=25^\circ\text{C}$	
Continuous Source Current(Body Diode)	$I_S$	-	-	4	A	$V_D=V_G=0, V_S=1.3\text{V}$	
Pulsed Source Current(Body Diode) <sup>1</sup>	$I_{SM}$	-	-	20	A		

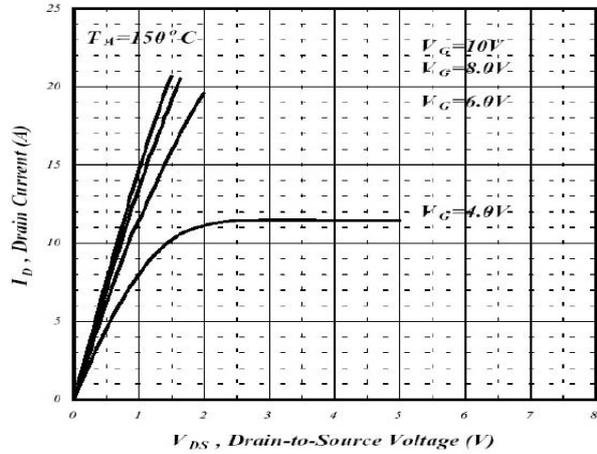
Notes:

1. Pulse width limited by safe operating area.
2. Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .

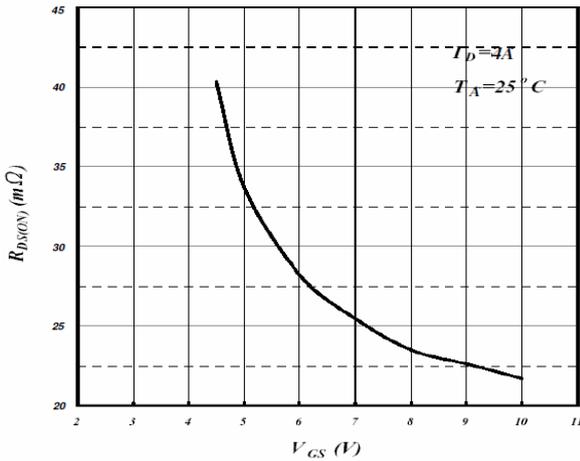
**CHARACTERISTIC CURVES**



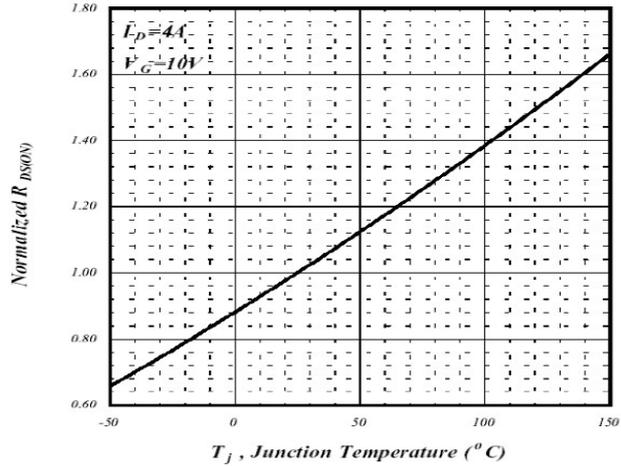
**Fig 1. Typical Output Characteristics**



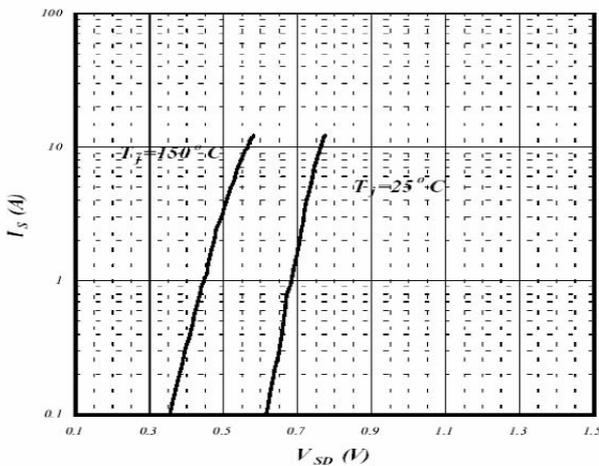
**Fig 2. Typical Output Characteristics**



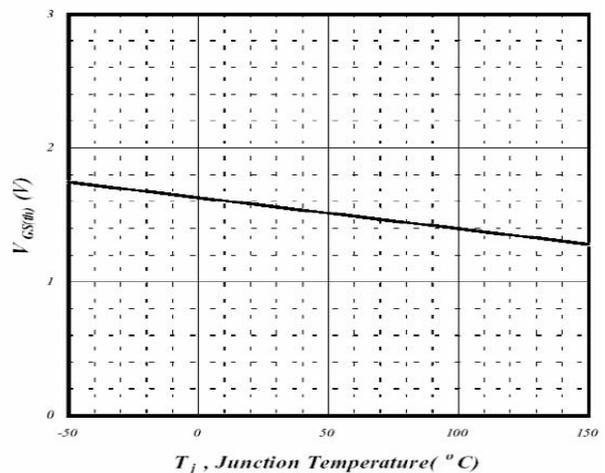
**Fig 3. On-Resistance vs. Gate Voltage**



**Fig 4. Normalized On-Resistance vs. Junction Temperature**

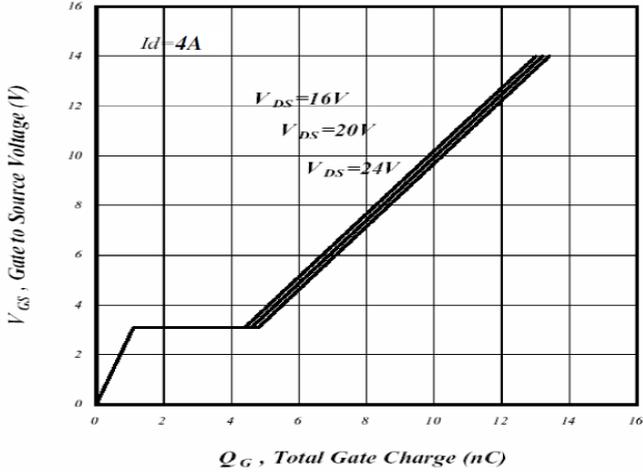


**Fig 5. Forward Characteristics of Reverse Diode**

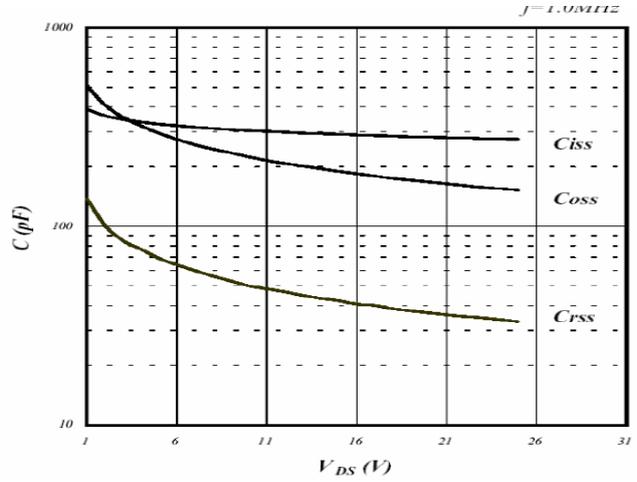


**Fig 6. Gate Threshold Voltage vs. Junction Temperature**

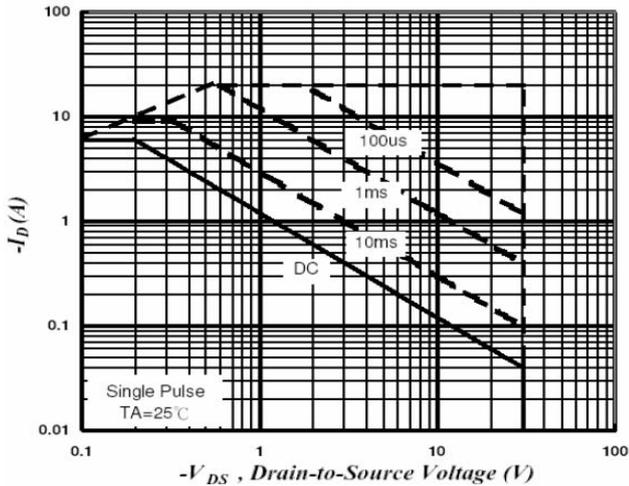
**CHARACTERISTIC CURVES**



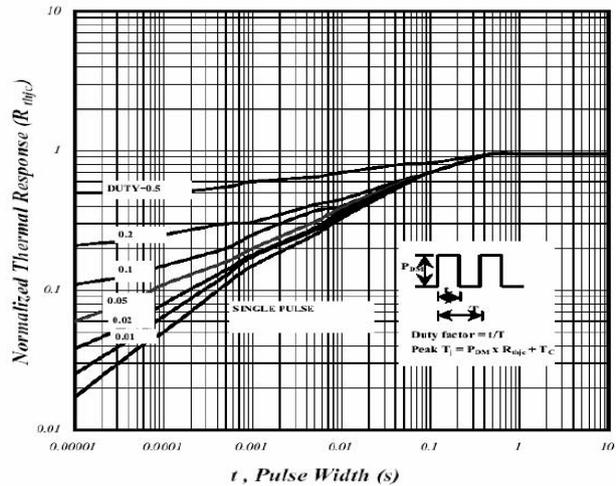
**Fig 7. Gate Charge Characteristics**



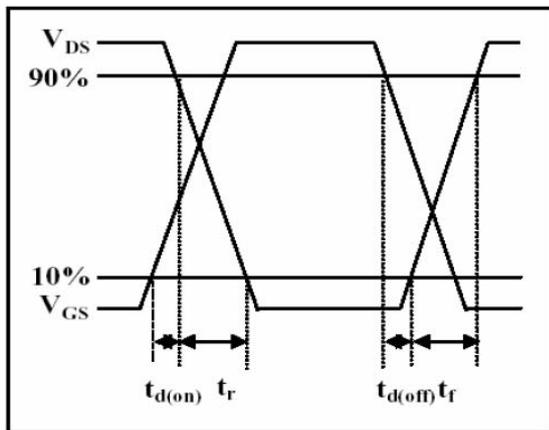
**Fig 8. Typical Capacitance Characteristics**



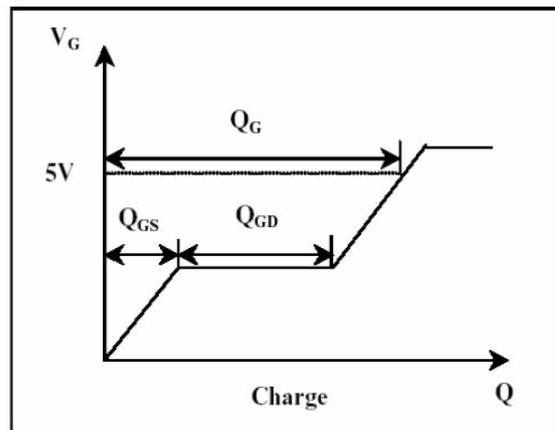
**Fig 9. Maximum Safe Operating Area**



**Fig 10. Effective Transient Thermal Impedance**



**Fig 11. Switching Time Circuit**



**Fig 12. Gate Charge Waveform**