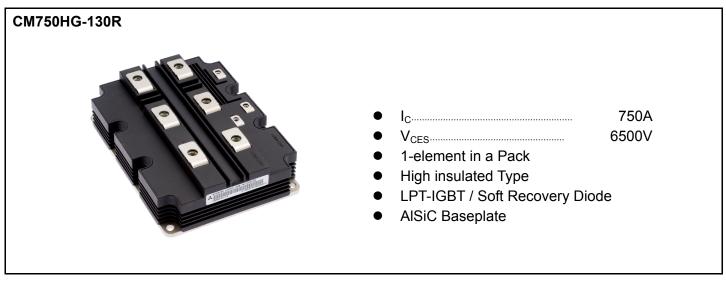


< HVIGBT MODULES >

CM750HG-130R

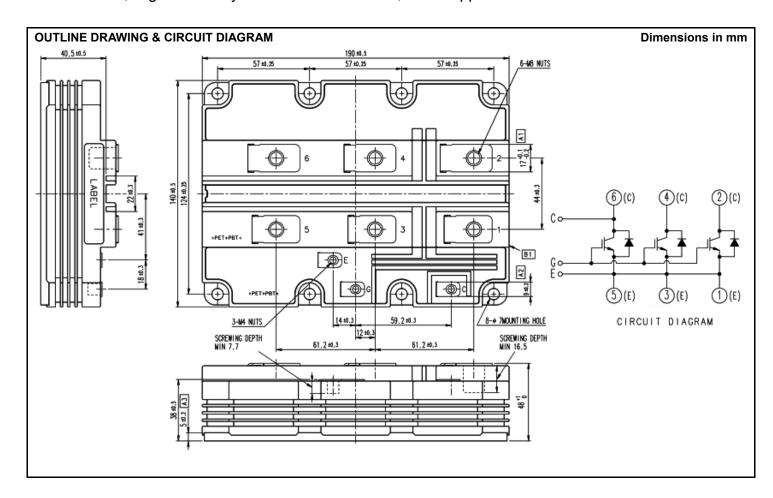
HIGH POWER SWITCHING USE INSULATED TYPE

4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



MAXIMUM RATINGS

Symbol	Item	Conditions		Ratings	Unit
		V _{GE} = 0V, T _j = +125 °C		6500	
V_{CES}	Collector-emitter voltage	$V_{GE} = 0V, T_j = +25^{\circ}C$		6300	V
		$V_{GE} = 0V, T_j = -50^{\circ}C$		5700	
V_{GES}	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25^{\circ}C$		±20	V
Ic	Collector current	DC, $T_c = 95^{\circ}C$		750	Α
I _{CRM}	Collector current	Pulse (I	Note 1)	1500	Α
I _E	Emitter current (4.1.6)	DC		750	Α
I _{ERM}	Emitter current (Note 2)	Pulse (I	Note 1)	1500	Α
P _{tot}	Maximum power dissipation (Note 3)	T _c = 25°C, IGBT part		10400	W
V _{iso}	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1min.		10200	V
V _e	Partial discharge extinction voltage	RMS, sinusoidal, f = 60Hz, Q _{PD} ≤ 10 pC		5100	V
Tj	Junction temperature			− 50 ~ +150	°C
T _{jop}	Operating junction temperature			-50 ~ +125	°C
T _{stg}	Storage temperature			-55 ~ +125	°C
t _{psc}	Short circuit pulse width	$V_{CC} = 4500 \text{ V}, V_{CE} \le V_{CES}, V_{GE} = 15 \text{V}, T_j = 12$	25°C	10	μS

ELECTRICAL CHARACTERISTICS

Symbol	Item	Conditions			Limits		Unit
Symbol	item	Conditions		Min	Тур	Max	Offic
	Collector cutoff current	$V_{CF} = V_{CFS}$, $V_{GF} = 0V$ $T_j = 25^{\circ}C$		_	_	24.0	m A
I _{CES}	Collector cutoff current	VCE - VCES, VGE - UV	T _j = 125°C	1	24.0	_	mA
$V_{GE(th)}$	Gate-emitter threshold voltage	$V_{CE} = 10V, I_{C} = 75mA, T_{j} = 25^{\circ}C$		5.8	6.3	6.8	V
I _{GES}	Gate leakage current	$V_{GE} = V_{GES}$, $V_{CE} = 0V$, $T_j = 25$ °C		-0.5	_	0.5	μΑ
C _{ies}	Input capacitance	V _{CF} = 10V, V _{GF} = 0V, f = 100kHz		_	136.0	_	nF
C _{oes}	Output capacitance	02 , 02 ,		_	8.6	_	nF
C _{res}	Reverse transfer capacitance	$T_j = 25$ °C		_	4.0	_	nF
Q_G	Total gate charge	V_{CC} = 3600V, I_{C} = 750A, V_{GE} = ±15V		_	10.5	_	μС
V	Collector emitter acturation valtage	I _C = 750A (Note 4)	T _j = 25°C	_	3.90	_	V
V _{CEsat}	Collector-emitter saturation voltage	V _{GE} = 15V	T _j = 125°C	_	4.80	5.60	V
_	Turn or deleviting		T _j = 25°C	_	1.20	_	
t _{d(on)}	Turn-on delay time	V _{CC} = 3600V	T _j = 125°C	_	1.15	1.80	μS
	Turn-on rise time	I _C = 750A	T _j = 25°C	_	0.20	_	
t _r	Turn-on rise time	$V_{GE} = \pm 15V$	T _j = 125°C	_	0.22	0.50	μS
_	Turn on quitabing anarque (1) (5)	$R_{G(on)} = 3.9\Omega$	T _j = 25°C	_	3.50	_	_
E _{on(10%)}	Turn-on switching energy (Note 5)	L _s = 150nH	T _j = 125°C	_	4.10	_	J
_	Turn on quitabing onergy (4)	Inductive load	T _j = 25°C	_	3.85	_	
E _{on}	Turn-on switching energy (Note 6)		T _j = 125°C	_	4.80	_	J
	Turn off delay time		T _j = 25°C	_	7.30	_	0
t _{d(off)}	Turn-off delay time	V _{CC} = 3600V	T _j = 125°C	_	7.80	9.00	μS
_	Turn-off fall time	I _C = 750A	T _j = 25°C	_	0.36	_	
t _f	Turn-on fail time	$V_{GE} = \pm 15V$	T _j = 125°C	_	0.44	1.00	μS
_	Turn off awitching aparay	$R_{G(off)} = 33\Omega$	T _j = 25°C	_	3.40		J
E _{off(10%)}	Turn-off switching energy (Note 5)	L _s = 150nH	T _j = 125°C	_	4.60	_	J
	Turn off awitahing aparay (Note a)	Inductive load	T _j = 25°C	-	3.60	_	
E _{off}	Turn-off switching energy (Note 6)		T _j = 125°C	_	4.90	_	J

< HVIGBT MODULES >

CM750HG-130R

HIGH POWER SWITCHING USE INSULATED TYPE

4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

ELECTRICAL CHARACTERISTICS (continuation)

Cumbal	Item	Conditions		Limits			Unit	
Symbol	item	Conditions			Min	Тур	Max	Offic
.,	Emitter-collector voltage	I _E = 750A	(Note 4)	T _j = 25°C	I	3.00	l	V
V _{EC}	(Note 2)	$V_{GE} = 0V$		T _j = 125°C		3.20	3.80	V
	Reverse recovery time			T _j = 25°C	l	0.55	l	_
t _{rr}	(Note 2)		T _j = 125°C	_	0.75	_	μS	
	Reverse recovery current	V _{CC} = 3600V		T _j = 25°C	_	900	_	^
Im	(Note 2)	I _C = 750A		T _j = 125°C	l	1000	l	Α
Q _{rr}	Reverse recovery charge	$V_{GE} = \pm 15V$	T _j = 25°C	_	750	_	C	
	(Note 2)	$\begin{aligned} R_{G(\text{on})} &= 3.9 \Omega \\ L_{\text{s}} &= 150 \text{nH} \\ &\text{Inductive load} \end{aligned}$		T _j = 125°C	_	1100	_	μС
E _{rec(10%)}	Reverse recovery energy			T _j = 25°C	_	1.05	_	
	(Note 2)(Note 5)			T _j = 125°C	I	1.85	l	J
_	Reverse recovery energy			T _j = 25°C	l	1.40	1	
E _{rec}	(Note 2)(Note 6)			T _j = 125°C		2.10	-	J

THERMAL CHARACTERISTICS

Symbol	Item	Conditions		Limits		
Syllibol				Тур	Max	Unit
$R_{th(j-c)Q}$	Thermal resistance	Junction to Case, IGBT part	1		12.0	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to Case, FWDi part	1		22.0	K/kW
R _{th(c-s)}	Contact thermal resistance Case to heat sink, $\lambda_{grease} = 1W/m \cdot k$, $D_{(c-s)} = 100 \mu m$		_	6.0	_	K/kW

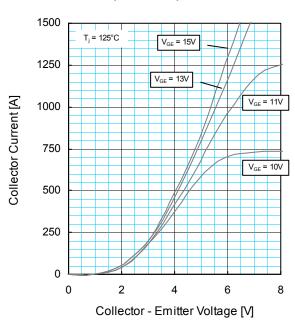
MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Unit		
		Conditions		Тур	Max	Oill
M_t		M : Main terminals screw	7.0	1	22.0	N⋅m
Ms	Mounting torque	M : Mounting screw	3.0	1	6.0	N⋅m
Mt		M : Auxiliary terminals screw	1.0	l	3.0	N⋅m
m	Mass		_	1.4	-	kg
CTI	Comparative tracking index		600	1		_
d _a	Clearance		26.0	-	ı	mm
ds	Creepage distance		56.0	1	-	mm
L _{P CE}	Parasitic stray inductance		_	15.0	-	nΗ
R _{CC'+EE'}	Internal lead resistance	Tc = 25°C	_	0.18	-	mΩ
r _g	Internal gate resistance	Tc = 25°C	_	2.6		Ω

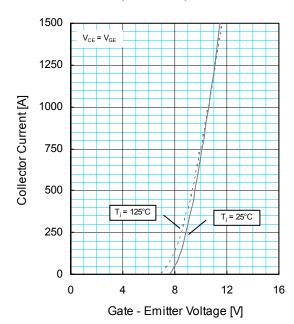
Note1. Pulse width and repetition rate should be such that junction temperature (T_i) does not exceed Topmax rating.

- 2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD_i).
- 3. Junction temperature (T_j) should not exceed T_{jmax} rating (150°C).
- 4. Pulse width and repetition rate should be such as to cause negligible temperature rise.
- 5. $E_{on(10\%)}$ / $E_{off(10\%)}$ / $E_{rec(10\%)}$ are the integral of 0.1 V_{CE} x 0.1 I_C x dt.
- 6. Definition of all items is according to IEC 60747, unless otherwise specified.

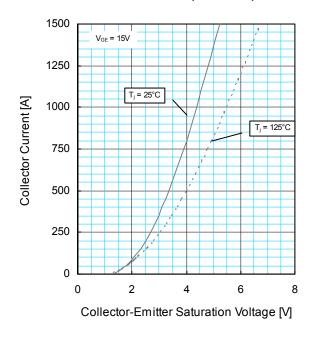
OUTPUT CHARACTERISTICS (TYPICAL)



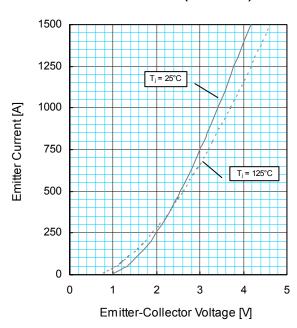
TRANSFER CHARACTERISTICS (TYPICAL)



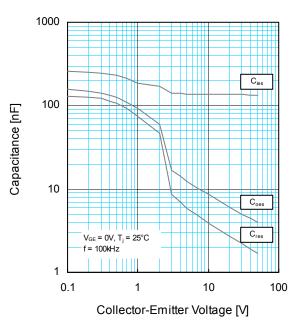
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



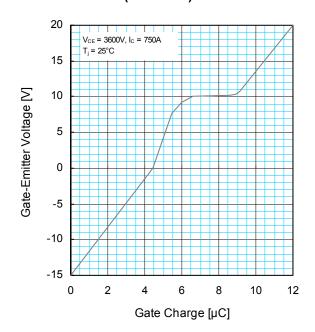
FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



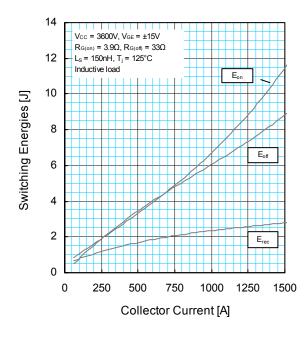
CAPACITANCE CHARACTERISTICS (TYPICAL)



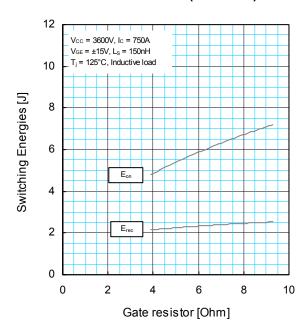
GATE CHARGE CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

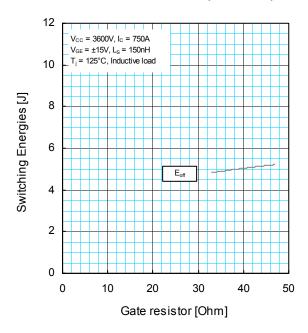


HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

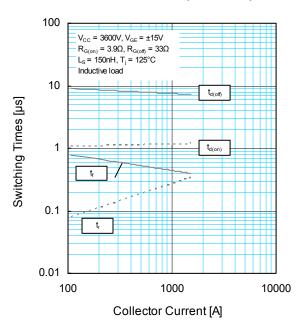


INSULATED TYPE

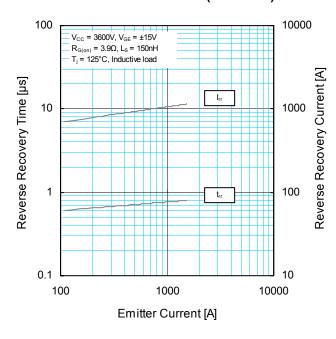
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



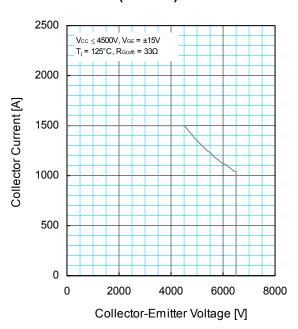
HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



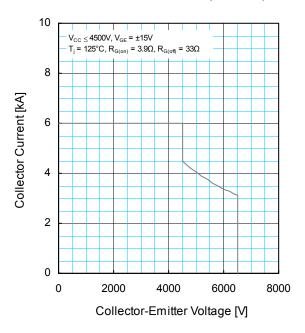
FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



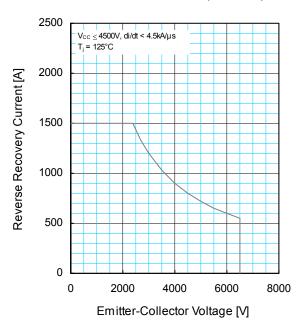
REVERSE BIAS SAFE OPERATING AREA (RBSOA)



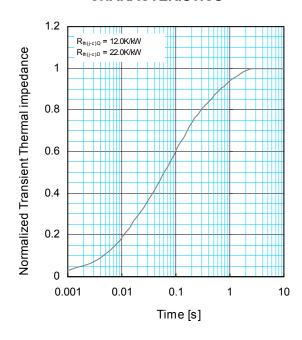
SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - exp^{\left(-\frac{t}{\tau_{i}}\right)} \right\}$$

	1	2	3	4
R _i [K/kW]:	0.0055	0.2360	0.4680	0.2905
t:[sec] ·	0.0001	0.0131	0.0878	0.6247

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