

NPN Silicon Transistor

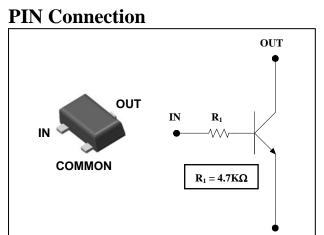
COMMON

Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density



Ordering Information

Type NO.	Marking	Package Code
SRC1210UF	<u>RA</u> ① ②	SOT-323F
	Device Code @ Veer® Week Code	

1) Device Code 2) Year&Week Code

Absolute Maximum Ratings

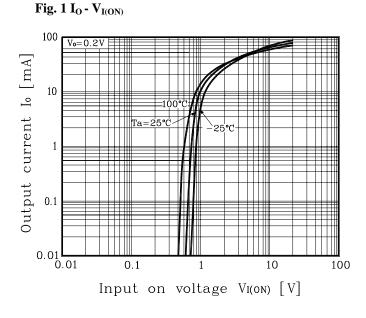
Absolute Maximum Ratings			(Ta=25°C)
Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	VI	20, -5	V
Output current	Ι _Ο	100	mA
Power dissipation	P _D	200	mW
Junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

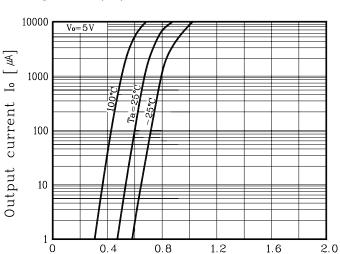
Flectrical Characteristics

Electrical Characteristics					(Ta:	=25°C)
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	$V_0 = 5V, I_0 = 10mA$	120	-	-	-
Output voltage	V _{O(ON)}	I ₀ =10mA, I ₁ =0.5mA	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V$, $I_0 = 5mA$	-	0.8	1.2	V
Input voltage (OFF)	V _{I(OFF)}	$V_0 = 5V$, $I_0 = 0.1mA$	0.3	0.55	-	V
Transition frequency	f_{T}^{*}	$V_0=10V$, $I_0=5mA$, f=1MHz	-	200	-	MHz
Input current	- I _I	$V_1 = 5V, I_0 = 0$	-	-	1.8	mA
Input resistor (Input to base)	R ₁	-	3.3	4.7	6.1	KΩ

* : Characteristic of transistor only

Electrical Characteristic Curves

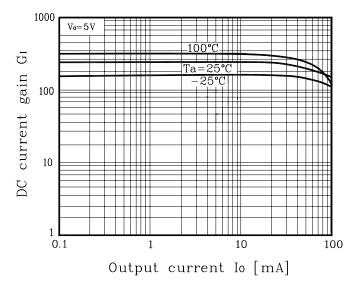




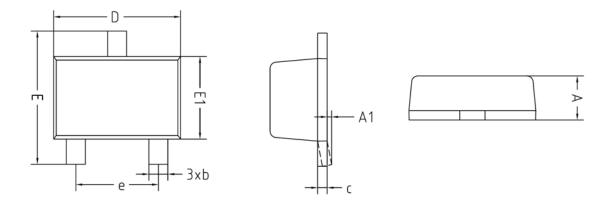
Input off voltage VI(OFF) [V]

Fig. 2 I_O - V_{I(OFF)}



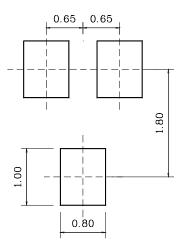


Outline Dimension



SYMBOL	MILLIMETERS			NOTE
STRIDUL	MINIMUM	NOMINAL	MAXIMUM	NUTE
A	0.60	-	0.80	
A1	0.00	-	0.10	
Ь	0.30	-	0.40	
С	0.08	-	0.16	
D	1.90	2.00	2.10	
E	1.95	2.10	2.25	
E1	1.20	1.30	1.40	
e	1.30BSC			

*Recommend PCB solder land [Unit: mm]



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