LED Lamps InGaN and Phosphor

TL21W02-D(T34

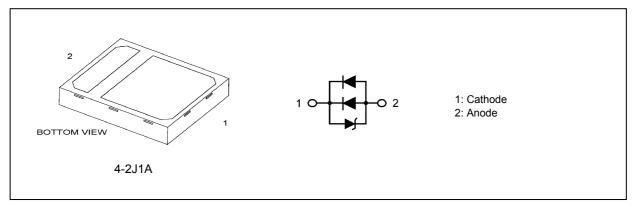
1. Applications

• General Lighting

2. Features

- (1) Size: 2.1 (L) mm \times 2.5 (W) mm \times 0.65 (H) mm
- (2) High luminous flux LED: 48 lm (typ.) $@I_F = 150 \text{ mA}$
- (3) Color: White (color temperature: 6500 K (typ.))
- (4) Reflow-soldering is available.

3. Packaging and Internal Circuit





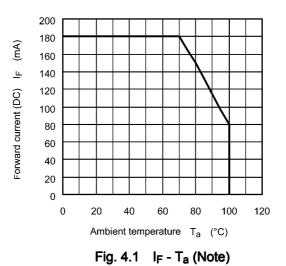
For part availability and ordering information please call Toll Free: 800.984.5337 Website: www.marktechopto.com | Email: info@marktechopto.com

4. Absolute Maximum Ratings (Note) (Unless otherwise specified, $T_a = 25^{\circ}C$)

Characteristics	Symbol	Note	Rating	Unit
Forward current (DC)	١ _F	See Fig. 4.1	180	mA
Forward current (pulsed)	I _{FP}	(Note 1)	300	
Power dissipation	PD		0.71	W
Operating temperature	T _{opr}		-40 to 100	°C
Storage temperature	T _{stg}		-40 to 100	
Junction temperature	Tj		120	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Note: The junction-to-ambient thermal resistance, $R_{th(j-a)}$, should be kept below 70°C/W so that this product is not exposed to a condition beyond the absolute maximum ratings.

 $R_{th(j-a)}$: Thermal resistance from the LED junction to ambient temperature

Note 1: Pulse width 10 ms, duty = 1/10

5. Handling Precautions

- This product is sensitive to electrostatic and care must be fully taken when handling products. Particularly in the case that an overvoltage shall be applied, the overflowed energy may cause damage to or possibly result in destruction of the product. Users shall take absolutely secure countermeasures against electrostatic and surge when handling the product.
- Since this product is intended to be used for display lighting, the measurement standard is based on the spectral sensitivity of the human eye. It is not intended to be used for any applications other than display lighting (e.g., sensors and light communications systems.)
- Please note the handling of products during evaluation.

1. Please do not apply pressure to the upper surface of the product with finger, tweezers, and others. Failure of product to light up may occur due to package deformation, wire deformation and/or disconnection.

- 2. Should tweezers be used in product handling, one with flat surfaces is recommended.
- 3. Please handle the product widthwise.

4. Please do not drop the product. There is a possibility for package transformation etc. to occur when the product is dropped.

5. Please do not stack the printed circuit boards on which the product is mounted to prevent damages to product surface. Also, please note not to damage the surface of the product with cushioning material etc. Surface damage to the product may influence their optical characteristics.

6. Electrical Characteristics (Unless otherwise specified, $T_a = 25^{\circ}C$)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V _F	See Table 6.1	I _F = 150 mA	2.9	3.25	3.8	V
Reverse voltage	V _R		I _R = 1 mA	_	0.75	—	
Thermal resistance (junction-to- soldering point)	R _{th(j-s)}	(Note 1)	I _F = 150 mA		20	_	°C/W

Note 1: $R_{th(j\text{-}s)}\!:$ Thermal resistance from the LED junction to solder point.

Table 6.1 Forward Voltage (Note)

Rank	Test Condition	Forward Voltage (Min)	Forward Voltage (Max)	Unit
1	I_F = 150 mA, T_a = 25°C, Tolerance ±0.1 V	2.9	3.2	V
2		3.2	3.5	
3		3.5	3.8	

Note: This LED lamp is sorted into forward voltage (V_F) ranks shown above. Each reel includes the same rank LEDs. Let the delivery ratio of each rank be unquestioned.

7. Optical Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Chromaticity	C _x	(Note 1)	I _F = 150 mA	—	—	_	—
	Cy	(Note 1)	I _F = 150 mA	_		_	
Luminous flux	φv	(Note 2)	I _F = 150 mA	30.3	48.0	60.5	lm
Color temperature	CCT		I _F = 150 mA	_	6500	—	к
Color rendering index	Ra	(Note 3)	I _F = 150 mA	70	75	_	_

Note 1: See Fig. 7.1 and Table 7.1 for chromaticity rank.

Note 2: See Table 7.2 for luminous flux rank.

Note 3: Tolerance: ± 5

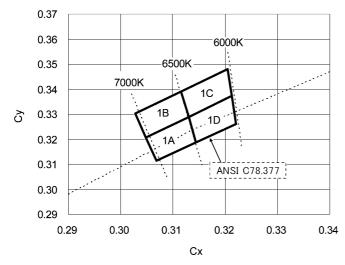


Fig. 7.1 Chromaticity Rank (Note)

Note: Test conditions: I_F = 150 mA, T_a = 25°C, Tolerance: ±0.01

	C _x	Cy		C _x	Cy
1A	0.3048	0.3207	1C	0.3115	0.3391
	0.3130	0.3290		0.3205	0.3481
	0.3144	0.3186		0.3213	0.3373
	0.3068	0.3113		0.3130	0.3290
1B	0.3028	0.3304	1D	0.3130	0.3290
	0.3115	0.3391		0.3213	0.3373
	0.3130	0.3290		0.3221	0.3261
	0.3048	0.3207		0.3144	0.3186

Table 7.1 Chromaticity Rank (Note)

Note: This LED lamp is sorted into chromaticity coordinate groups shown above. Each reel includes the same rank LEDs. Let the delivery ratio of each rank be unquestioned.

Table 7.2	Luminous Flux Rank (Note)
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Rank	Test Condition	Luminous Flux (Min)	Luminous Flux (Max)	Unit
C16	I_F = 150 mA, T_a = 25°C, Tolerance ±10%	30.3	36.0	lm
C17		36.0	42.8	
C18		42.8	51.0	
C19		51.0	60.5	

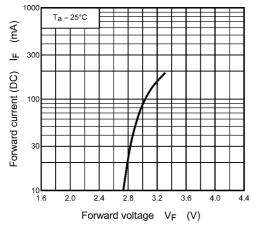
Note: This LED lamp is sorted into luminous flux ranks shown above. Each reel includes the same rank LEDs. Let the delivery ratio of each rank be unquestioned.

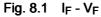
Rank notations: The luminous flux, chromaticity and V_F ranks are printed on labels as shown below:

Example: 1A C18 2 1A: Chromaticity rank C18: Luminous flux rank 2: V_F rank

Relative luminous flux

8. Characteristics Curves (Note)





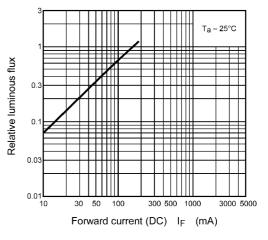


Fig. 8.2 Relative luminous flux - IF

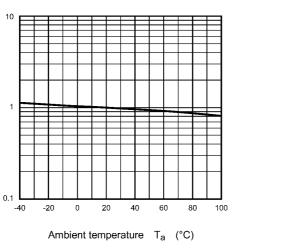


Fig. 8.3 Relative luminous flux - Ta

 $T_a = 25^{\circ}C$

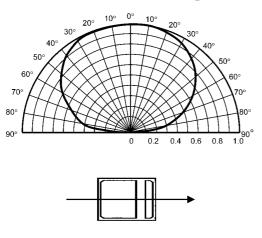


Fig. 8.5 Radiation Pattern

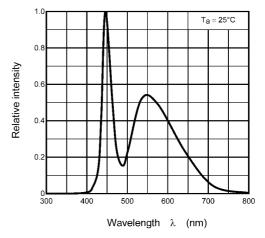
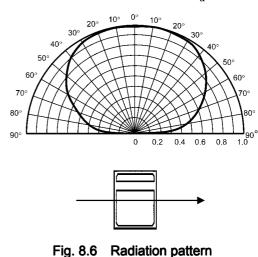


Fig. 8.4 Wavelength characteristic

 $T_a = 25^{\circ}C$



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

9. Packing

9.1. Moisture-Proof Packing

These LED devices are packed in an aluminum envelope with a silica gel and a moisture indicator to avoid moisture absorption. The optical characteristics of the device may be affected by exposure to moisture in the air before soldering and the device should therefore be stored under the following conditions:

- This moisture proof bag may be stored unopened within 12 months at the following conditions. Temperature: 5°C to 30°C Humidity: 90% (max)
- After opening the moisture proof bag, the device should be assembled within 168 hours in an environment of 5°C to 30°C/60% RH or below.
- If upon opening, the moisture indicator card shows humidity 30% or above (Color of indication changes to pink) or the expiration date has passed, the device should be baked in taping with reel.
 After baking, use the baked device within 72 hours, but perform baking only once.
 Baking conditions: 60±5°C, for 12 to 24 hours.

Expiration date: 12 months from sealing date, which is imprinted on the label affixed.

- Repeated baking can cause the peeling strength of the taping to change, then leads to trouble in mounting. Furthermore, prevent the devices from being destructed against static electricity for baking of it.
- If the packing material of laminate would be broken, the hermeticity would deteriorate. Therefore, do not throw or drop the packed devices.

10. Mounting

10.1. Mounting Precautions

- Do not apply mechanical stress to the resin body at high temperature. The time taken for a device to return to the room temperature after reflow soldering depends on the mounting board and environmental conditions.
- The resin body is easily scratched. Avoid friction against hard materials.
- When installing an assembled board into equipment, ensure that the devices on the board do not contact with other components.

10.2. Soldering

- Following show examples of reflow soldering.
- Temperature Profile (see following figures.)

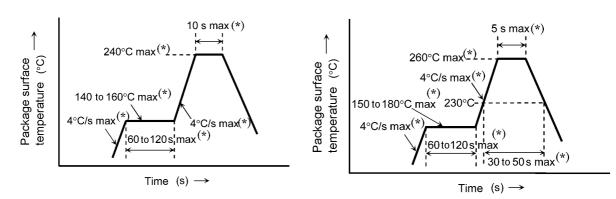


Fig. 10.2.1 Temperature Profile for Pb Soldering Fig. 10.2.2 Temperature profile for Lead(Pb)-free (example) soldering (example)

- The product is evaluated using above reflow soldering conditions. No additional test is performed exceed the condition (i.e. the condition more than (*)MAX values) as a evaluation. Please perform reflow soldering under the above conditions.
- Please perform the first reflow soldering with reference to the above temperature profile and within 168 hours of opening the package.
- If a second reflow process is necessary, reflow soldering should be performed within 168 h of the first reflow under the above conditions. Storage conditions before the second reflow soldering: 30°C, 60% RH (max)
- Do not perform wave soldering.

10.3. Land Pattern Dimensions for Reference Only

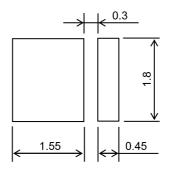


Fig. 10.3.1 Land Pattern Dimensions for Reference Only (Unit: mm)

11. Cleaning

When cleaning is required after soldering, Toshiba recommends the following cleaning solvents. It is confirmed that these solvents have no effect on semiconductor devices in our dipping test (under the recommended conditions). In selecting the one for your actual usage, please perform sufficient review on washing condition, using condition and etc.

Recommended cleaning solvents ASAHI CLEAN AK-225AES: (made by ASAHI GLASS) KAO CLEANTHROUGH 750HS: (made by KAO) PINE ALPHA ST-100S: (made by ARAKAWA CHEMICAL)

12. Tape Specifications

12.1. Product Naming Conventions

The type of package used for shipment is denoted by a symbol suffix after the part number. The method of classification is as below. (this method, however does not apply to products whose electrical characteristics differ from standard Toshiba specifications)

Example: TL21W02-D(T34 Toshiba part number: TL21W02-D Tape type: T34 (4-mm pitch)

12.2. Handling Precautions

(1) The tape is antistatic-coated. However, if the tape is charged with excess static electricity, devices might cling to the tape or waggle in the tape when the cover tape peeled off. Be aware of the following to avoid this:

Use an ionizer to neutralize the ions when utilizing an automatic mounter.

For transport and temporary storage of devices, use containers (boxes, jigs, bags) that are made with antistatic materials or materials that dissipate static electricity.

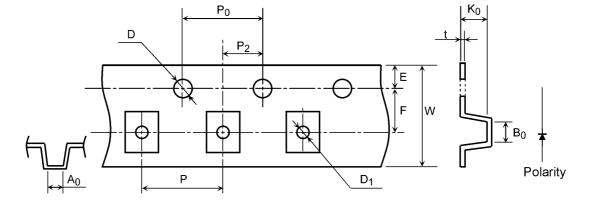
Unit: mm

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12.3. Tape Dimensions

	D	E	P ₀	t	F	D ₁	P ₂	W	Р	A ₀	B ₀	K ₀
Dimensions	1.5	1.75	4.0	0.2	3.5	1.1	2.0	8.0	4.0	2.38	2.78	0.8
Tolerance	+0.1/-0	±0.1	±0.1	±0.05	±0.05	±0.1	±0.05	±0.2	±0.1	±0.1	±0.1	±0.05







12.4. Reel Specification

12.4.1. Reel Dimensions

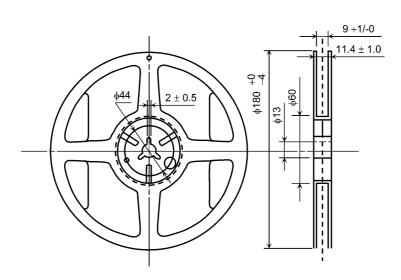


Fig. 12.4.1.1 Reel Dimensions

12.4.2. Tape Leader and Trailer

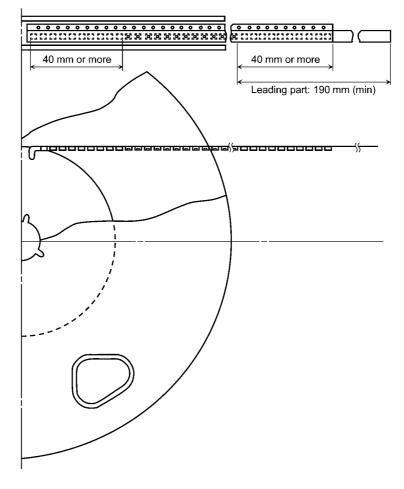


Fig. 12.4.2.1 Tape Leader and Trailer

12.5. Packing Form

Each reel is sealed in an aluminum pack with silica gel. Packing quantity is as shown below.

- Reel: 2,000 pcs
- Carton: 10,000 pcs
- Sealed aluminum pack with silica gel

12.6. Label Format

Label example for TL21W02-D(T34 and label location are as shown below.

12.6.1. Label Example

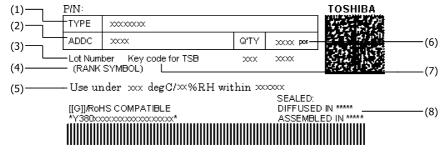
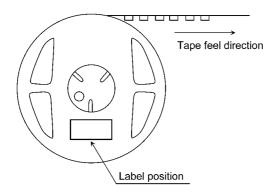


Fig. 12.6.1.1 Label Example

12.6.2. Label Details

No. (# refer to to the above label example)	Information for Customers Use	Information for Toshiba Use
(1)	Part No.	TL21W02-D(T34
(2)	ADD code	Notation explanation (O Assembled in Japan, sales for domestic and overseas customers (J Assembled in overseas factory, sales for domestic customers (T Assembled in overseas factory, sales for overseas customers
(3)	Lot code	Example: 270xxxxx
(4)	Rank symbol	Example: 1AC182
(5)	Storage condition after Opening	Use under 5-30°C/60%RH within 168 h
(6)	Packing quantity	Example: 2,000 pcs
(7)	Key code	Example: 12345
(8)	Country of origin	Example: JAPAN

12.6.3. Label Location



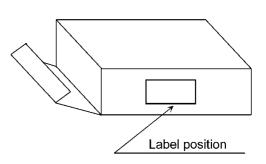


Fig. 12.6.3.1 Label Position on the Reel



Aluminum pack: The aluminum pack in which the reel is supplied also has the label attached to center of one side.

1: Cathode

2: Anode

13. Internal Circuit

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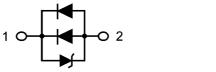
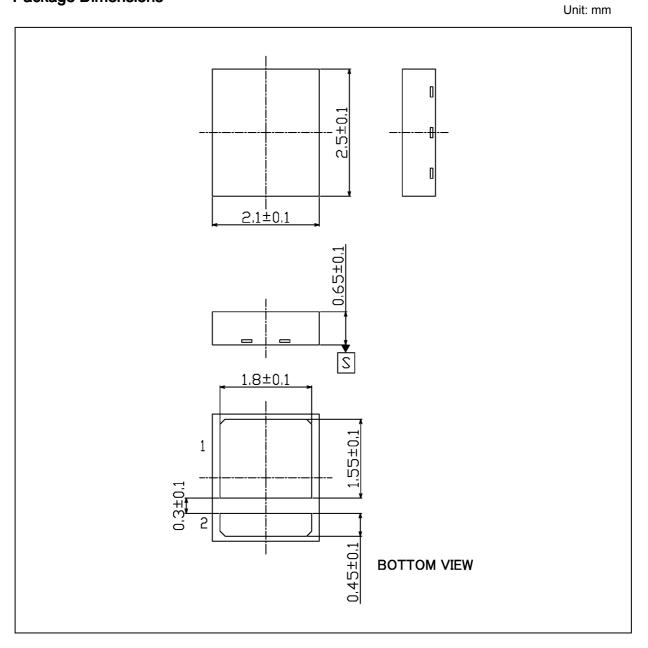
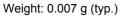


Fig. 13.1 Internal Circuit

Package Dimensions





Package Name(s) TOSHIBA: 4-2J1A

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