

SANYO Semiconductors DATA SHEET

LV7150V— Bi-CMOS IC Switch for the Wideband Video Signal (with LPF)

Overview

The LV7150V is switch for the wideband video signal. It has the two input switches by three channels.

It built in the 6MHz/12MHz/30MHz-LPF. It is the best for the filter to remove the digital clock noise of the Component or RGB Analog video signal before the A/D converter.

It can correspond to the full HD signal because it provides the flat frequency response to 60MHz.

Functions

- Two input switches × three channels
- Component and RGB signal inputs
- Flat frequency response to 60MHz (Bypass filter)
- 6MHz/12MHz/30MHz-LPF

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum Supply voltage	V _{CC} max		6	V
Allowable power dissipation	Pd max	Ta≤75°C Mounted on a specified board *	300	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +125	°C

Note *: Mounted on a specified board: 114.3mm×76.1mm×1.6mm glass epoxy

- Any and all SANYO Semiconductor Co.,Ltd. products described or contained herein are, with regard to "standard application", intended for the use as general electronics equipment (home appliances, AV equipment, communication device, office equipment, industrial equipment etc.). The products mentioned herein shall not be intended for use for any "special application" (medical equipment whose purpose is to sustain life, aerospace instrument, nuclear control device, burning appliances, transportation machine, traffic signal system, safety equipment etc.) that shall require extremely high level of reliability and can directly threaten human lives in case of failure or malfunction of the product or may cause harm to human bodies, nor shall they grant any guarantee thereof. If you should intend to use our products for applications outside the standard applications of our customer who is considering such use and/or outside the scope of our intended standard applications, please consult with us prior to the intended use. If there is no consultation or inquiry before the intended use, our customer shall be solely responsible for the use.
- Specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

LV7150V

Recommended Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended Supply voltage	Vcc		5	V
Operating Supply voltage Range	V _{CC} opg		4.75 to 5.25	V

Electrical Characteristics at $Ta=25^{\circ}C,\,V_{CC}=5.0V$

Doromotor	lanut		Input :	signal		Out	Test Condition		Ratings		mit
Parameter	Input	Point	Signal	Freq	Mag	Point	Test Condition	min	typ	max	unit
V _{CC} Supply Currer	nt & Internal r	eference re	gulator								
V _{CC} Supply Current		V3					At non-signal, The current flows to 3pin		34	42	mA
Regulator voltage value						T16	The voltage of 16pin	2.8	3.0	3.2	V
Voltage Gain		•							l.		
Voltage Gain Py,Pb,Pr,R,G,B	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	100k	300mVpp	T15 T13 T11	Output gain to input, LPF- ON/OFF is selected	-0.5	-0.2	0.0	dB
Frequency respons	se										
Frequency response 1 at LPF-OFF	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	60M 100k	300mVpp	T15 T13 T11	LPF-OFF is selected, Output Gain difference between 100kHz and 60MHz	-1.0	0.0	1.0	dB
Frequency response 2 at 6MHz_LPF	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	6M 100k	300mVpp	T15 T13 T11	LPF-ON is selected, Output Gain difference between 100kHz and 6MHz	-3.0	0.0	1.0	dB
Frequency response 3 at 6MHz _LPF	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	13.5M 100k	300mVpp	T15 T13 T11	LPF-ON is selected, Output Gain difference between 100kHz and 13.5MHz		-30.0	-20.0	dB
Frequency response 4 at 12MHz_LPF	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	12M 100k	300mVpp	T15 T13 T11	LPF-ON is selected, Output Gain difference between 100kHz and 12MHz	-3.0	0.0	1.0	dB
Frequency response 5 at 12MHz_LPF	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	27M 100k	300mVpp	T15 T13 T11	LPF-ON is selected, Output Gain difference between 100kHz and 27MHz		-30.0	-20.0	dB
Frequency response 6 at 30MHz_LPF	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	20M 100k	300mVpp	T15 T13 T11	LPF-ON is selected, Output Gain difference between 100kHz and 20MHz	-1.0	0.0	1.0	dB
Frequency response 7 at 30MHz_LPF	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	30M 100k	300mVpp	T15 T13 T11	LPF-ON is selected, Output Gain difference between 100kHz and 30MHz	-3.5	-1.0	1.5	dB
Frequency response 8 at 30MHz_LPF	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	75M 100k	300mVpp	T15 T13 T11	LPF-ON is selected, Output Gain difference between 100kHz and 75MHz		-45.0	-33.0	dB
S/N											
Compared with S/N at 30MHz_LPF	Py R G B	T1A T2A T4A T5A T7A T8A	SIG1	30M 100k	650mVpp	T15 T13 T11	30MHzLPF-ON is selected, S/N of the band between 100kHz and 30MHz, S/N is shown with dB		-60.0	-50.0	dB
Crosstalk										· ·	
Crosstalk	Py/Pb/Pr R/G/B	T1A T2A T4A T5A T7A T8A	SIG2	4M	700mVpp	T15 T13 T11	LPF-ON/OFF is selected, Ratio at level of non-selection output and selection output		-60.0	-55.0	dB

Continued on next page.

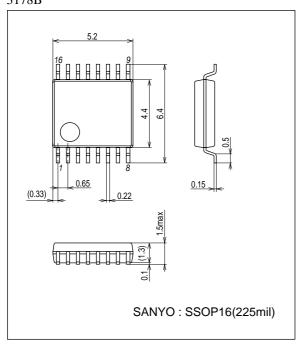
LV7150V

Continued from preceding page.

Description	la-sut		Input signal			Out	Tank On a dikina	Ratings		unit	
Parameter	Input	Point	Signal	Freq	Mag	Point	Test Condition	min	typ	max	unit
Group Delay											
Group Delay 1	Py/Pb/Pr	T1A T2A		60M		T15	LPF-OFF is selected				
at LPF-OFF	R/G/B	T4A T5A	SIG2	100k	300mVpp	T13	f=60MHz/100kHz		0.5	2.0	ns
	R/G/D	T7A T8A		TOOK		T11					
Group Delay 2	Py/Pb/Pr	T1A T2A		6M		T15	6M_LPF-ON is selected				
at LPF-ON	R/G/B	T4A T5A	SIG2	•	300mVpp	T13	f=6MHz/100kHz		40.0	70.0	ns
	R/G/B	T7A T8A		100k		T11					
Group Delay 3	D/Dl- /D-	T1A T2A		4014		T15	12M_LPF-ON is selected				
at LPF-ON	Py/Pb/Pr R/G/B	T4A T5A	SIG2	12M 100k	300mVpp	T13	f=12MHz/100kHz		20.0	40.0	ns
	R/G/B	T7A T8A		TOOK		T11					
Group Delay 4	Py/Pb/Pr	T1A T2A		30M		T15	30MHz_LPF-ON is selected				
at LPF-ON	l ,	T4A T5A	SIG2		300mVpp	T13	f=30MHz/100kHz		10.0	20.0	ns
	R/G/B	T7A T8A		100k		T11					

Package Dimensions

unit : mm (typ) 3178B



Pin Control Table

Pin Control Table

SW No.	Pin No.	SW function name
SW1	Pin12	CLAMP/BIAS_CTL
SW2	Pin10	Filter_CTL1
SW3	Pin9	Filter_CTL2
SW4	Pin14	Input_Select_CTL

Input Control Table

CLAMP/BIAS_CTL Mode selected	
Low(0 to 0.7V)	CLAMP_ON(Component_Mode)
High(2.3V to V _{CC}) BIAS_ON(RGB_Mode)	

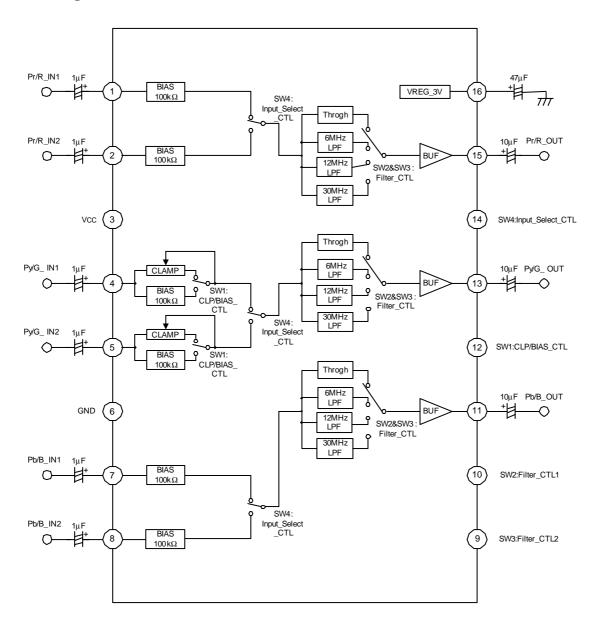
Filter Control Table

Filter_CTL1	Filter_CTL2	Mode selected
Low(0 to 0.7V)	Low(0 to 0.7V)	6M_LPF_ON
Low(0 to 0.7V)	High(2.3V to V _{CC})	12M_LPF_ON
High(2.3V to V _{CC})	Low(0 to 0.7V)	30M_LPF_ON
High(2.3V to V _{CC})	High(2.3V to V _{CC})	LPF_Through_ON

Input Select Control Table

Input_Select_CTL	Mode selected
Low(0 to 0.7V)	CH1_select
High(2.3V to V _{CC})	CH2_select

Block Diagram



- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of July, 2007. Specifications and information herein are subject to change without notice.