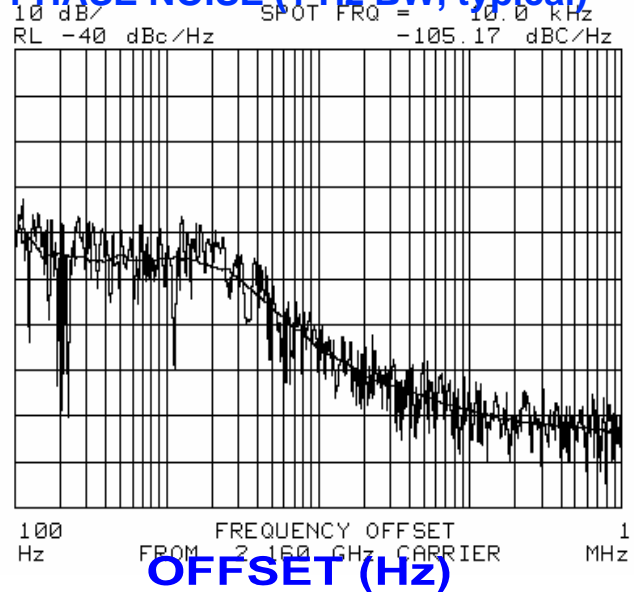




PHASE NOISE (1 Hz BW, typical)



£(f) (dBc/Hz)

FEATURES

- Frequency Range: 2160 - 2280 MHz
- Step Size: 1000 KHz
- PLL-24H - Style Package

APPLICATIONS

- Satellite Communication
- Fixed Wireless

PERFORMANCE SPECIFICATIONS

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	2160 - 2280	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-102	dBc/Hz
Harmonic Suppression (2nd, typ.)	-20	dBc
Sideband Spurs (typ.)	-65	dBc
Power Output	0±3	dBm
Load Impedance	50	Ω
Step Size	1000	kHz
Charge Pump Output Current	3750	μA
Switching Speed (typ., adjacent channel)	5	mSec
Startup Lock Time (typ.)	5	mSec
Operating Temperature Range	0 to 70	°C
Package Style	PLL-24H	

POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	30	mA

All specifications are typical unless otherwise noted and subject to change without notice.

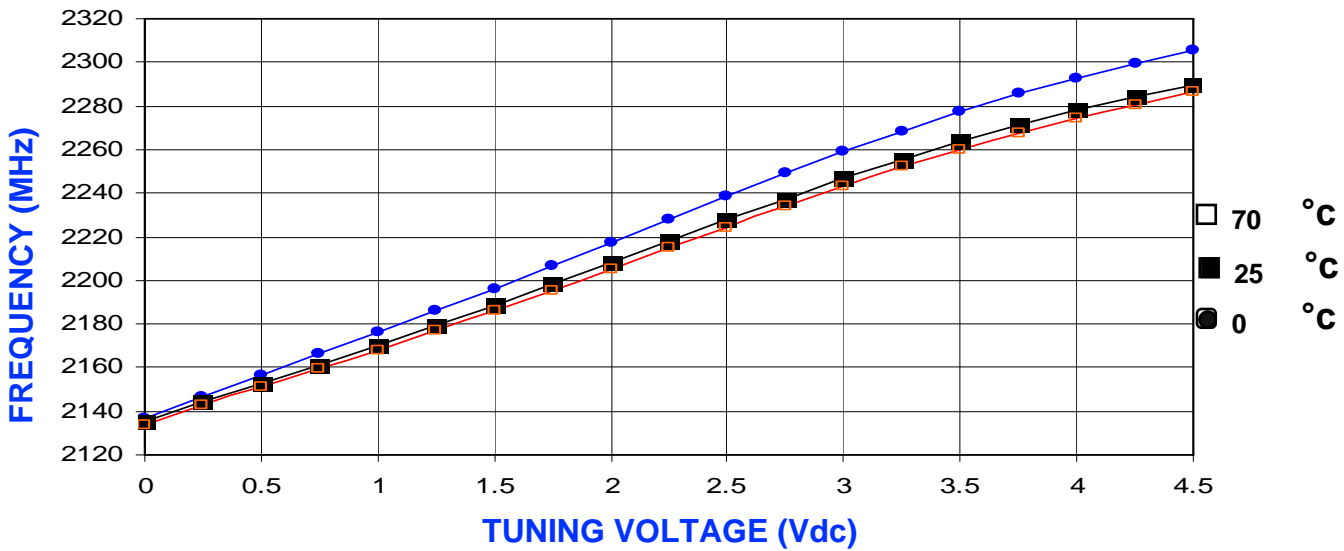
APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-202 : PLL Functional Description

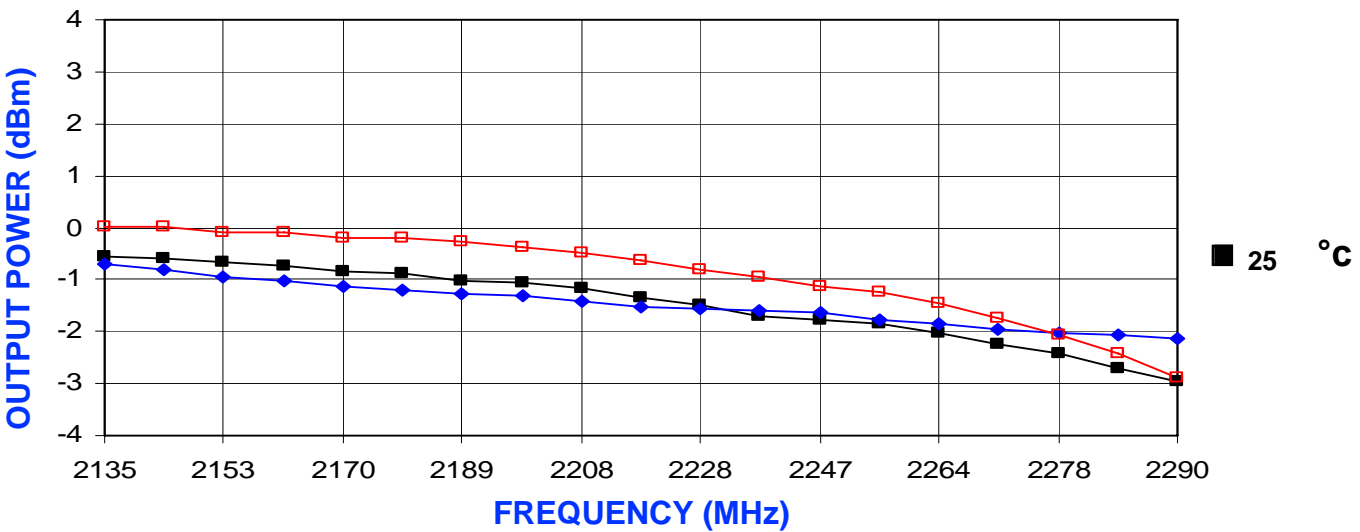
NOTES:

Ref OSC 10MHz. AD chip ADF4106

VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS

