

**NEC**

T-75-01

**TELECOM/ISDN DEVICES****CMOS Combos**

Part No.	Description	Companding Law	Sync/Async Operation	Signaling	Capability	Package
μPD9513AD (Direct replacement for Intel 29C13/2913)	1 master clock; 1.536 or 1.544 or 2.048 MHz	A- or μ-law	Sync only	No	No	20-pin CERDIP
μPD9514AD (Direct replacement for Intel 29C14/2914)	Separate transmit/receive clock	A- or μ-law	Both	Yes	Yes	24-pin CERDIP
μPD9516AD (Direct replacement for Intel 29C16/2916)	2.048 MHz master clock	μ-law	Sync only	No	No	16-pin CERDIP
μPD9517AD (Direct replacement for Intel 29C17/2917)	2.048 MHz master clock	A-law	Sync only	No	No	16-pin CERDIP
μPD9601AD (Compatible replacement for Hitachi 44233C/44237C)	On-chip PLL	A-law	Both	No	Yes	16-pin CERDIP
μPD9602AD (Compatible replacement for Hitachi 44234C/44238C)	On-chip PLL	μ-law	Both	No	Yes	16-pin CERDIP
μPD9604AD	Digital gain setting (Note 1)	μ-law	Both	No	Yes	16-pin CERDIP
μPD9605AD	Digital gain setting (Note 1)	A-law	Both	No	Yes	16-pin CERDIP
μPD9621L	PLCC version of μPD9601AD	A-law	Both	No	Yes	18-pin PLCC
μPD9622L	PLCC version of μPD9602AD	μ-law	Both	No	Yes	18-pin PLCC
μPD9624L	PLCC version of μPD9604AD	μ-law	Both	No	Yes	18-pin PLCC
μPD9625L	PLCC version of μPD9605AD	A-law	Both	No	Yes	18-pin PLCC

## Notes:

(1) From 0 to 15 dB in 0.5 dB steps

**SLICs**

Part No.	Description	Key Features	Package
μPC7059K	-48 V SLIC; constant resistance feed	Battery feed, supervision, 2-wire to 4-wire conversion	28-pin LCC
μPC7062K	-24 V SLIC for key telephone	Battery feed, supervision, 2-wire to 4-wire conversion	28-pin LCC
μPC7069K	-48 V SLIC; constant current feed	Battery feed, supervision, 2-wire to 4-wire conversion	28-pin LCC
μPC7051L	PLCC version of μPC7059K	Battery feed, supervision, 2-wire to 4-wire conversion	32-pin PLCC
μPC7063L	PLCC version of μPC7062K	Battery feed, supervision, 2-wire to 4-wire conversion	32-pin PLCC
μPC7061L	PLCC version of μPC7069K	Battery feed, supervision, 2-wire to 4-wire conversion	32-pin PLCC

