



SOT-23 DIGITAL TRANSISTOR TRANSISTORS(NPN)

FEATURES

- * Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- * Only the on/off conditions need to be set for operation marking device design easy.

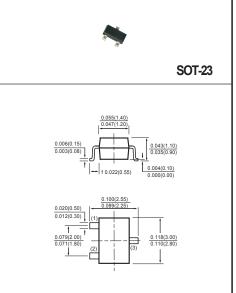
MECHANICAL DATA

- * Case: Molded plastic
- * Epoxy: UL 94V-O rate flame retardant
- * Lead: MIL-STD-202E method 208C guaranteed
- * Mounting position: Any
- * Weight: 0.008 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 $^{\circ}\text{C}$ ambient temperature unless otherwise specified.





Dimensions in inches and (millimeters)

MAXIMUM RATINGES (@ TA = 25°C unless otherwise noted)

RATINGS	SYMBOL	LIMITS	UNITS	
Supply voltage	V _{CC}	50	V	
Input voltage	V _{IN}	-10~30	V	
Output current	lo	100	mA	
	IC(MAX)	100		
Power dissipation	Pd	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 ~150	°C	

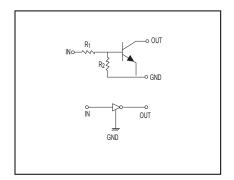
(1) IN (2) GND (3) OUT

ELECTRICAL CHARACTERISTICS (@ TA = 25°C unless otherwise noted)

CHARACTERISTICS	SYMBOL	MIN	TYP	MAX	UNITS
Input voltage (V _{CC} = 5V, I _O = 100μA)	V _{I(off)}	-	-	0.8	v
Input voltage (Vo= 0.3V, I _O = 2mA)	V _{I(on)}	3	-	-	
Output voltage (I _O / I _I = 10mA / 0.5mA)	V _{o(on)}	-	0.1	0.3	V
Input current (V _I = 5V)	ų	-	-	0.88	mA
Output current (V _{CC} = 50V,V _I = 0)	I _{o(off)}	-	-	0.5	μA
DC current gain (V _O = 5V,I _O = 10mA)	GI	24	-	-	-
Input resistance	R ₁	7	10	13	KΩ
Resistance ratio	R ₂ / R ₁	0.37	0.47	0.57	-
Transition frequency (V _O = 10V,I _O = 5mA, f= 100MHz)	fT	-	250	-	MHz

Note : "Fully ROHS compliant", "100% Sn plating (Pb-free)".

EQUIVALENT CIRCUIT (DTC114WCA)





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