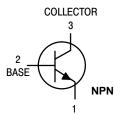
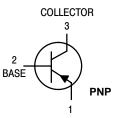
Amplifier Transistors



EMITTER



EMITTER

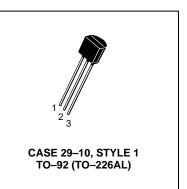
MAXIMUM RATINGS

Rating	Symbol	MPS650 MPS750	MPS651 MPS751	Unit
Collector–Emitter Voltage	VCE	40	60	Vdc
Collector-Base Voltage	V _{CB}	60	80	Vdc
Emitter-Base Voltage	V _{EB}	5.0		Vdc
Collector Current — Continuous	IC	2.0		Adc
Total Power Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0		m₩ mW/°C
Total Power Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12		Watt mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	–55 to +150		°C



Voltage and current are negative for PNP transistors

*ON Semiconductor Preferred Devices



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W	
Thermal Resistance, Junction to Case	$R_{ extsf{ heta}JC}$	83.3	°C/W	

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic Symbol Min Max Unit OFF CHARACTERISTICS Collector-Emitter Breakdown Voltage⁽¹⁾ (I_C = 10 mAdc, I_B = 0) MPS650, MPS750 MPS651 MPS751 V(BR)CEO 40 - Vdc

	MPS651, MPS751		60	—	
Collector–Base Breakdown Voltage ($I_C = 100 \ \mu Adc, I_E = 0$)	MPS650, MPS750 MPS651, MPS751	V(BR)CBO	60 80		Vdc
Emitter–Base Breakdown Voltage (I _C = 0, I _E = 10 μAdc)		V _{(BR)EBO}	5.0	—	Vdc
Collector Cutoff Current ($V_{CB} = 60 \text{ Vdc}, I_E = 0$) ($V_{CB} = 80 \text{ Vdc}, I_E = 0$)	MPS650, MPS750 MPS651, MPS751	ICBO	_	0.1 0.1	μAdc
Emitter Cutoff Current ($V_{EB} = 4.0 \text{ V}, I_C = 0$)		IEBO	—	0.1	μAdc

1. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle = 2.0%.

Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

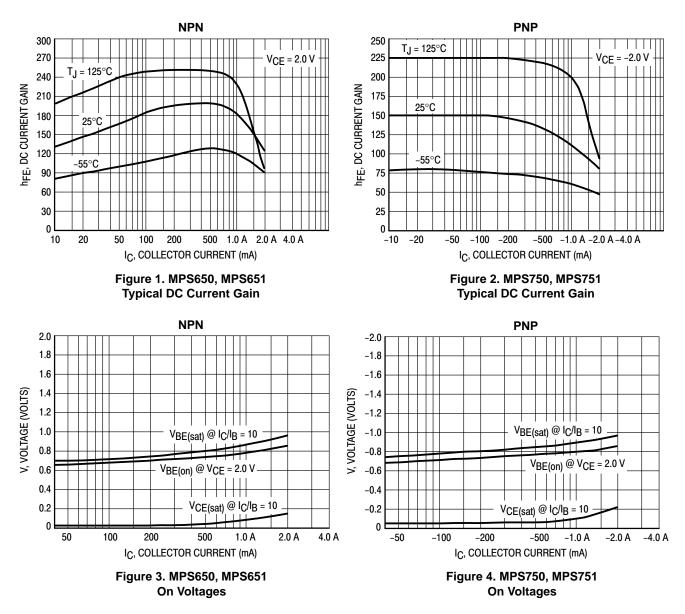
ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted) (Continued)

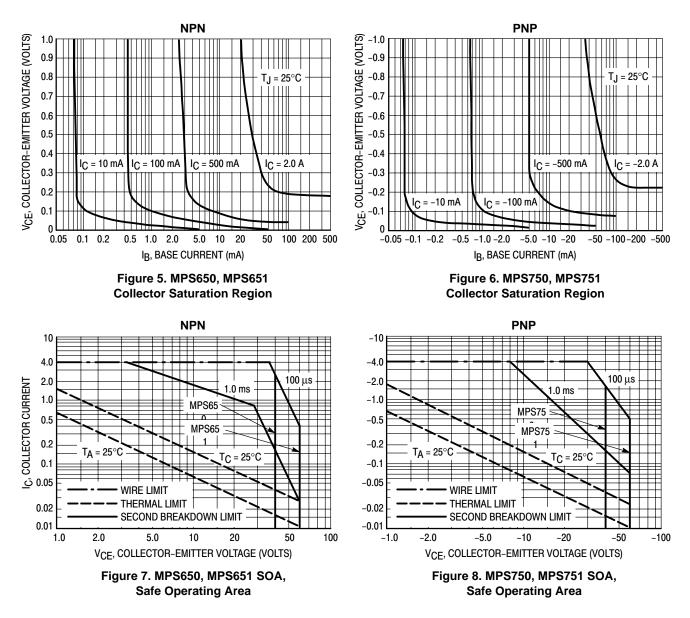
Characteristic		Min	Max	Unit
ON CHARACTERISTICS(1)				
	hfe	75 75 75 40	 	—
Collector-Emitter Saturation Voltage ($I_C = 2.0 \text{ A}, I_B = 200 \text{ mA}$) ($I_C = 1.0 \text{ A}, I_B = 100 \text{ mA}$)	V _{CE(sat)}		0.5 0.3	Vdc
Base–Emitter On Voltage ($I_C = 1.0 \text{ A}$, $V_{CE} = 2.0 \text{ V}$)	V _{BE(on)}	_	1.0	Vdc
Base–Emitter Saturation Voltage (I _C = 1.0 A, I _B = 100 mA) SMALL–SIGNAL CHARACTERISTICS	VBE(sat)	—	1.2	Vdc

Current–Gain — Bandwidth Product(2) f_T 75—MHz $(I_C = 50 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz})$ f_T 75—MHz

1. Pulse Test: Pulse Width $\leq 300~\mu\text{s},$ Duty Cycle = 2.0%.

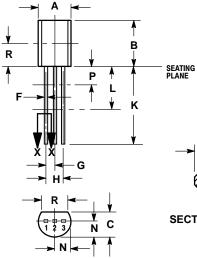
2. f_{T} is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.





PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-10 ISSUE AL







STYLE 1: PIN 1. EMITTER 2. BASE 3. COLLECTOR

NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. DIMENSIONS TAPPLYES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.44	5.21	
В	0.290	0.310	7.37	7.87	
С	0.125	0.165	3.18	4.19	
D	0.018	0.021	0.457	0.533	
F	0.016	0.019	0.407	0.482	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.018	0.024	0.46	0.61	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.135		3.43		

<u>Notes</u>

<u>Notes</u>

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