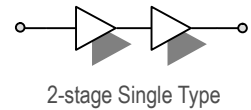


Features

- $S_{21} = 30.1 \text{ dB@806 MHz}$
= 29.9 dB@824 MHz
- NF of 0.75 dB over Frequency
- Unconditionally Stable
- Single 5 V Supply
- High OIP3@Low Current

Description

The plerow™ ALN-series is the compactly designed surface-mount module for the use of the LNA with or without the following gain blocks in the infrastructure equipment of the mobile wireless (CDMA, GSM, PCS, PHS, WCDMA, DMB, WLAN, WiBro, WiMAX), GPS, satellite communication terminals, CATV and so on. It has an exceptional performance of low noise figure, high gain, high OIP3, and low bias current. The stability factor is always kept more than unity over the application band in order to ensure its unconditionally stable implementation to the application system environment. The surface-mount module package including the completed matching circuit and other components necessary just in case allows very simple and convenient implementation onto the system board in mass production level.



Specifications (in Production)

Typ.@T = 25 °C, $V_s = 5 \text{ V}$, Freq. = 815 MHz, $Z_{o.sys} = 50 \text{ ohms}$

Parameter	Unit	Specifications		
		Min	Typ	Max
Frequency Range	MHz	806		824
Gain	dB	29.0	30.0	
Gain Flatness	dB		± 0.1	± 0.2
Noise Figure	dB		0.75	0.8
Output IP3	dBm	38.5	39.5	
S11/S22	dB			-18/-12
Output P1dB	dBm	19	20	
Switching Time ⁽³⁾	μsec		-	
Supply Current	mA		170	200
Supply Voltage	V		5	
Impedance	Ω		50	
Max. RF Input Power	dBm	C.W 29~31 (before fail)		
Package Type & Size	mm	Surface Mount Type, 13Wx13Lx3.8H		

Operating temperature is -40°C to +85°C.

1) OIP3 is measured with two tones at an output power of 4 dBm/tones separated by 1 MHz.

2) S11, S22 (max) is the worst value within the frequency band.

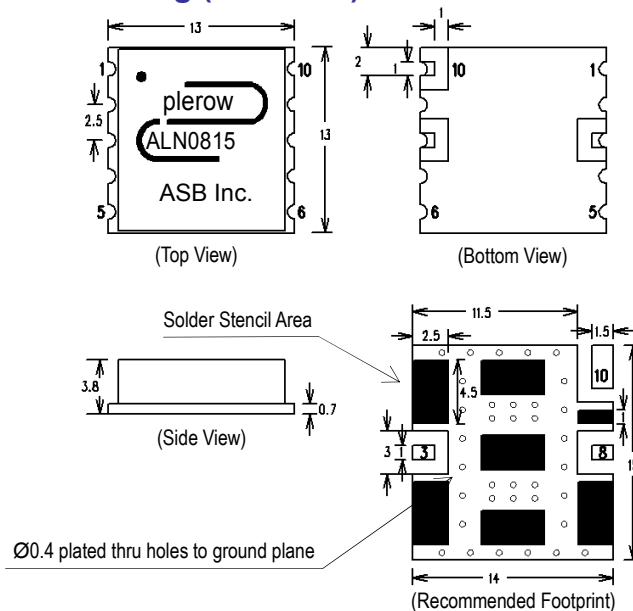
3) Switching time means the time that takes for output power to get stabilized to its final level after switching DC voltage from 0 V to V_s .

More Information

Website: www.asb.co.kr
E-mail: sales@asb.co.kr

Tel: (82) 42-528-7223
Fax: (82) 42-528-7222

Outline Drawing (Unit: mm)



Pin Number	Function
3	RF In
8	RF Out
10	V_s
Others	Ground

Note: 1. The number and size of ground via holes in a circuit board is critical for thermal RF grounding considerations.

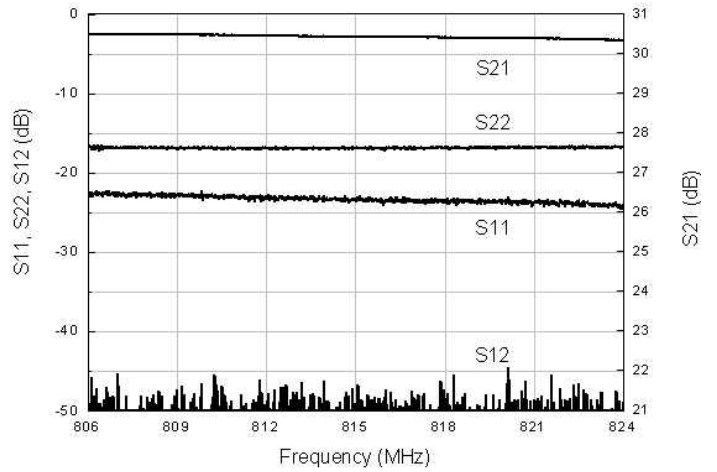
2. We recommend that the ground via holes be placed on the bottom of all ground pins for better RF and thermal performance, as shown in the drawing at the left side.

**Typical Performance
(Measured)**

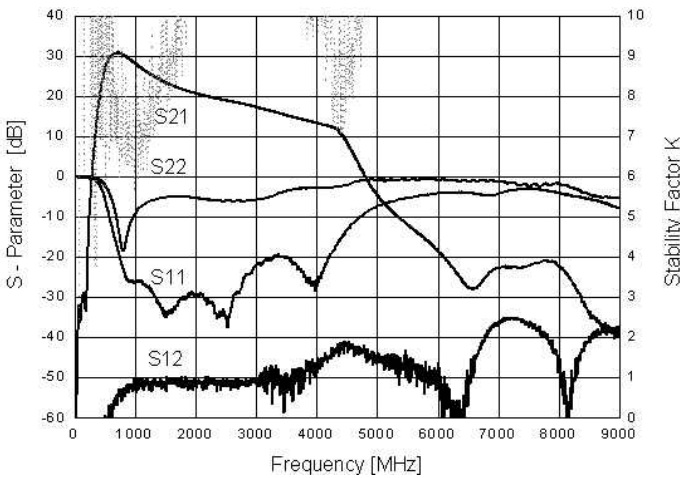
806~824 MHz

+5 V

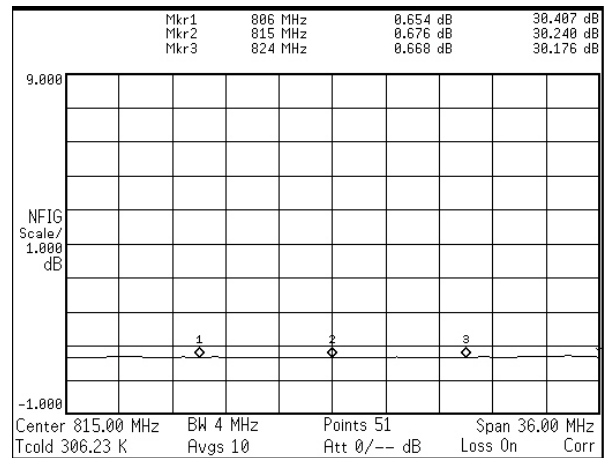
S-parameters



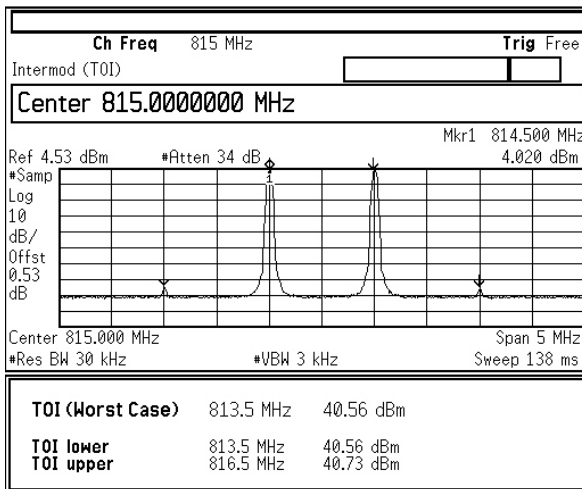
S-parameters & K Factor



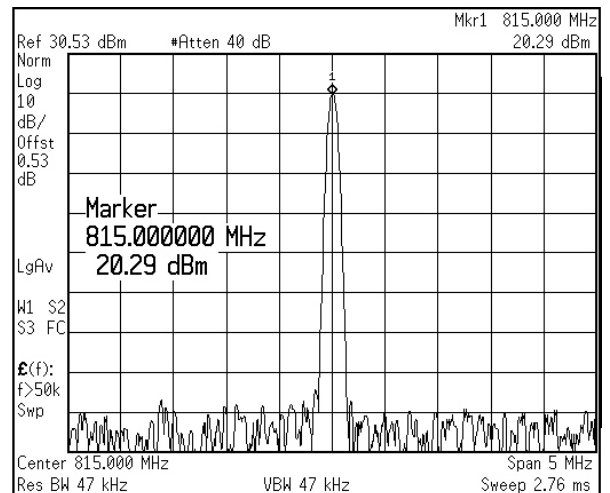
Noise Figure



OIP3



P1dB



RF Performance with Voltage Change

1. S-parameter

	806 MHz			815 MHz				824 MHz		
	S21 (dB)	S11 (dB)	S22 (dB)	S21 (dB)	G/F (dB)	S11 (dB)	S22 (dB)	S21 (dB)	S11 (dB)	S22 (dB)
4.50 V	30.08	-23.62	-16.44	30.02	0.14	-24.62	-16.41	29.94	-25.52	-16.29
4.75 V	30.21	-23.70	-16.50	30.14	0.15	-24.70	-16.49	30.06	-25.68	-16.37
5.00 V	30.32	-23.61	-16.57	30.25	0.15	-24.64	-16.56	30.17	-25.57	-16.47
5.25 V	30.40	-23.70	-16.62	30.33	0.15	-24.68	-16.62	30.25	-25.73	-16.48
5.50 V	30.48	-23.65	-16.66	30.41	0.15	-24.71	-16.62	30.33	-25.66	-16.47

2. OIP3, P1dB & NF

	806 MHz			815 MHz			824 MHz		
	OIP3 (dBm)	P1dB (dBm)	NF (dB)	OIP3 (dBm)	P1dB (dBm)	NF (dB)	OIP3 (dBm)	P1dB (dBm)	NF (dB)
4.50 V	41.32	19.48	0.713	41.02	19.37	0.702	40.79	19.21	0.717
4.75 V	41.78	20.00	0.730	41.44	19.93	0.713	41.46	19.84	0.730
5.00 V	40.75	20.47	0.747	40.90	20.48	0.751	41.00	20.26	0.743
5.25 V	39.20	20.92	0.767	39.41	20.89	0.780	39.47	20.70	0.766
5.50 V	37.95	21.34	0.789	38.20	21.27	0.798	38.45	21.15	0.796

Note: tested at room temperature.

RF Performance with Operating Temperature

1. S-parameter

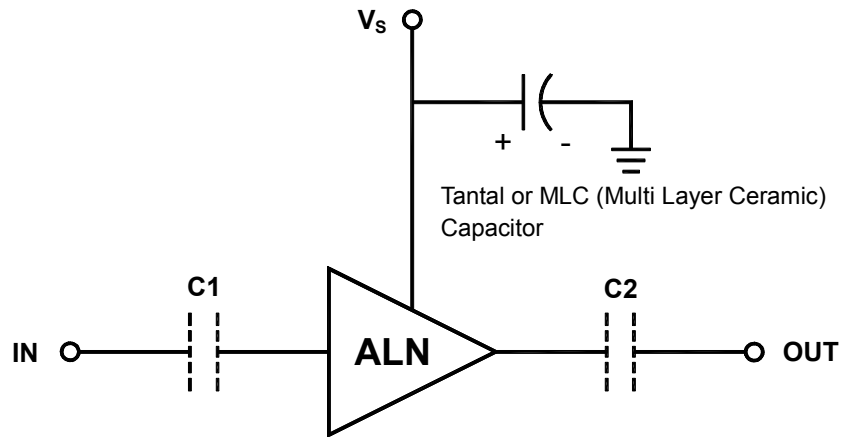
	806 MHz			815 MHz				824 MHz		
	S21 (dB)	S11 (dB)	S22 (dB)	S21 (dB)	G/F (dB)	S11 (dB)	S22 (dB)	S21 (dB)	S11 (dB)	S22 (dB)
-45 °C	30.65	-18.83	-16.43	30.58	0.15	-19.40	-16.95	30.50	-19.99	-17.29
-10 °C	30.55	-21.14	-16.65	30.47	0.16	-21.88	-16.90	30.39	-22.82	-17.00
25 °C	30.46	-23.33	-16.54	30.39	0.17	-24.20	-16.90	30.29	-25.02	-16.69
60 °C	30.31	-25.00	-16.48	30.23	0.16	-26.03	-16.27	30.15	-27.17	-15.84
85 °C	30.20	-25.04	-16.20	30.12	0.18	-26.09	-15.93	30.02	-27.25	-15.41

2. OIP3, P1dB & NF

	806 MHz			815 MHz			824 MHz		
	OIP3 (dBm)	P1dB (dBm)	NF (dB)	OIP3 (dBm)	P1dB (dBm)	NF (dB)	OIP3 (dBm)	P1dB (dBm)	NF (dB)
-45 °C	39.25	20.14	0.504	39.42	20.28	0.511	39.50	20.39	0.501
-10 °C	40.02	20.20	0.623	40.23	20.32	0.625	40.40	20.46	0.620
25 °C	40.74	20.35	0.751	40.81	20.50	0.747	40.99	20.65	0.750
60 °C	40.30	20.38	0.889	40.12	20.51	0.891	40.05	20.68	0.874
85 °C	39.95	20.24	0.977	39.60	20.40	0.983	39.58	20.50	0.980

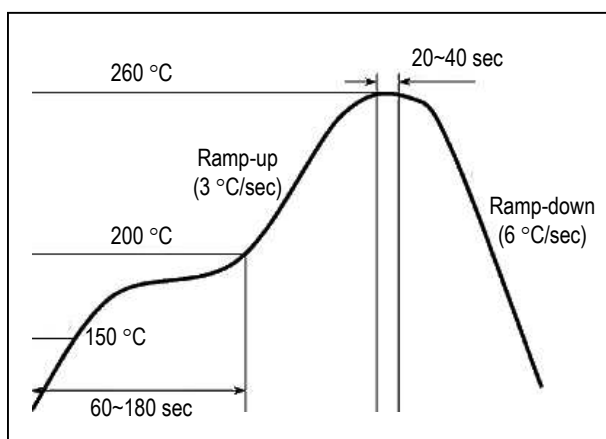
Note: tested at $V_s = 5V$.

Application Circuit



- 1) The tantal or MLC (Multi Layer Ceramic) capacitor is optional and for bypassing the AC noise introduced from the DC supply. The capacitance value may be determined by customer's DC supply status. The capacitor should be placed as close as possible to V_s pin and be connected directly to the ground plane for the best electrical performance.
- 2) DC blocking capacitors are always necessarily placed at the input and output port for allowing only the RF signal to pass and blocking the DC component in the signal. The DC blocking capacitors are included inside the ALN module. Therefore, C1 & C2 capacitors may not be necessary, but can be added just in case that the customer wants. The value of C1 & C2 is determined by considering the application frequency.

Recommended Soldering Reflow Process



Evaluation Board Layout

