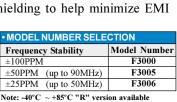
HCMOS/TTL HEAVY LOAD OSCILLATOR F3000

The F3000 Clock Oscillator is capable of driving heavy HCMOS loads. This oscillator has a tri-state enable/disable function on pin 1 to facilitate testing with ATE. The package is all metal with pin 7 as case ground which provides shielding to help minimize EMI radiation.

FEATURES

- 50pF HCMOS Load to 80 MHz
- 10TTL Fanout
- Tri-State E apre/1 sable



(ex: F3000R) to 90 MHz

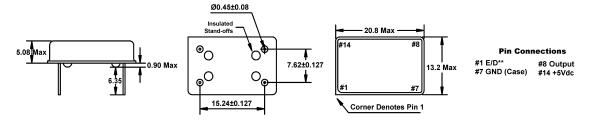


• ELECT UCA_CLA	VAC IL RIS I	S/ DI	= 5	V GL	= <mark>0</mark> pl			
PARAMETERS	FREQUENCY I	RANGE	CON	OITIO	NS	MIN	MAX	

PARAMETER	S	FREQUENCY RANGE	CONDITIONS	MIN	MAX	UNITS
Frequency Range	(Fo)			1.544	120.000	MHz
Frequency Stabili	ty	1.544 ~ 100.000	All Conditions*	-100	+100	PPM
		100.000+ ~ 120.000		-200	+200	
Temperature Rang	ge	1.544 ~ 120.000				
Operating	(TOPR)			-10	+70	°C
Storage	(Tstg)			-55	+125	
Supply Voltage	(Vdd)	1.544 ~ 120.000		+4.5	+5.5	V
Input Current	(Idd)	1.544 ~ 25.000			25	mA
		25.000+ ~ 50.000			40	
		50.000+ ~ 80.000			77	
		80.000+ ~ 120.000			82	
Output Symmetry		1.544 ~ 80.000	2.5V	45	55	%
		80.000+ ~ 120.000		40	60	
Rise Time	(TR)	$1.544 \sim 120.000$	$0.5V\sim4.5V$		5	nS
Fall Time	(TF)	$1.544 \sim 120.000$	$4.5V\sim0.5V$		5	
Output Voltage	(Vol)	$1.544 \sim 120.000$	IOL = 16 mA		0.5	V
	(Voh)		IOH = -16 mA	4.5		
Output Current	(IOL)	$1.544 \sim 120.000$	$V_{OL} = 0.5 V$		16	mA
	(IOH)		$V_{OH} = 4.5 \text{ V}$		- 16	
Output Load		1.544 ~ 120.000	TTL		10	TTL
		1.544 ~ 80.000	HCMOS		50	pF
		80.000+ ~ 100.000	HCMOS		30	pF
		100.000+ ~ 120.000	HCMOS		15	pF
Start-up Time	(Ts)	1.544 ~ 120.000			10	mS

^{*} Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

• ENABLE/DISABLE FUNCTION**					
INH (Pin 1)	OUTPUT (Pin 8)				
OPEN ***	ACTIVE				
'1' Level VIH ≥ 2.2 V	ACTIVE				
'0' Level VIL ≤ 0.8 V	High Z				



All dimensions are in millimeters.

^{***}An internal pullup resistor from pin 1 to pin 14 allows active output if pin 1 is left open.

See page 44 for mechanical specifications, test circuits, and output waveform. All specifications subject to change without notice. Rev. 03/02/00