

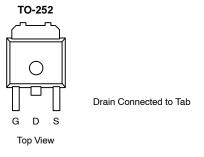
PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(\Omega)$	I _D (A) ^a	
30	0.0043 @ V _{GS} = 10 V	33	
	0.0065 @ V _{GS} = 4.5 V	27	

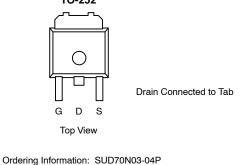
FEATURES

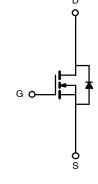
- TrenchFET® Power MOSFET
- 175°C Junction Temperature
- Optimized for Low-Side Synchronous Rectifier Operation
- 100% R_g Tested

APPLICATIONS

- DC/DC Converters
- Synchronous Rectifiers







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	.,	
Gate-Source Voltage		V _{GS}	±20	v	
	T _A = 25°C		33		
Continuous Drain Current ^a	T _C = 25°C	- I _D	70 ^b		
Pulsed Drain Current		I _{DM}	100	A	
Continuous Source Current (Diode Conduction) ^a		Is	8.3 ^a		
	T _C = 25°C	_	88		
Maximum Power Dissipation	T _A = 25°C	P _D	8.3 ^a	W	
Operating Junction and Storage Temperature Range		T _J , T _{sta}	-55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 sec		15	18		
Maximum Junction-to-Ambient ^a	Steady State	R _{thJA}	40	50	°C/W	
Maximum Junction-to-Case		R _{thJC}	1.2	1.5		

Notes

Surface Mounted on FR4 Board, $t \le 10$ sec.

Limited by package.

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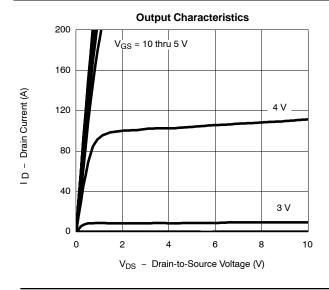


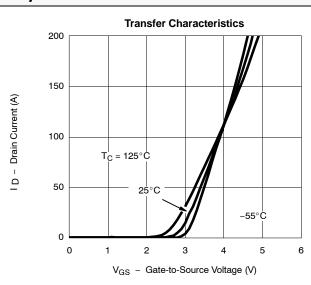
Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit
Static	1			I		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 250 μA	30			v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0		3.0	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current		V _{DS} = 30 V, V _{GS} = 0 V			1	_
	DSS	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}$	50			μΑ
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α
		$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		0.0035	0.0043	
Drain-Source On-State Resistance ^b	r _{DS(on)}	V_{GS} = 10 V, I_{D} = 20 A, T_{J} = 125°C			0.007	Ω
	, ,	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		0.0051	0.0065	
Forward Transconductanceb	9fs	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$	20			S
Dynamic ^a						
Input Capacitance	C _{iss}			5100		
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		860		pF
Reverse Transfer Capacitance	C _{rss}			430		
Gate Resistance	R _g	f = 1 MHz	0.5	1.0	1.5	Ω
Total Gate Charge ^c	Qg			90	135	
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 15 \text{ V}, \ V_{GS} = 10 \text{ V}, \ I_{D} = 50 \text{ A}$		18		nC
Gate-Drain Charge ^c	Q_{gd}			16		
Turn-On Delay Time ^c	t _{d(on)}			12	20	
Rise Time ^c	t _r	$V_{DD} = 15 \text{ V}, R_L = 0.3 \Omega$		12	20	Ī
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		40	60	ns
Fall Time ^c	t _f			10	15	7
Source-Drain Diode Ratings ar	d Characteristic	c (T _C = 25°C)	•			
Pulsed Current	I _{SM}				100	Α
Diode Forward Voltage ^b	V _{SD}	$I_F = 100 \text{ A}, V_{GS} = 0 \text{ V}$		1.2	1.5	V
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 50 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$	1	40	80	ns

Notes

- Guaranteed by design, not subject to production testing.
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2%.
- a. Independent of operating temperature.

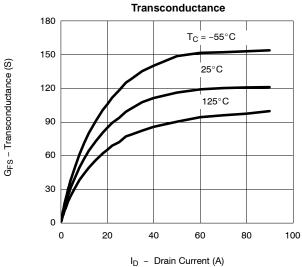
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



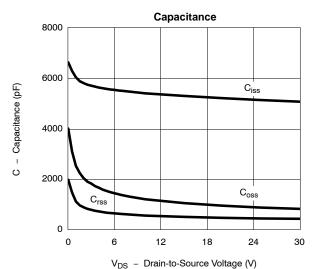


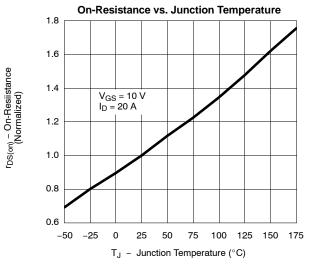


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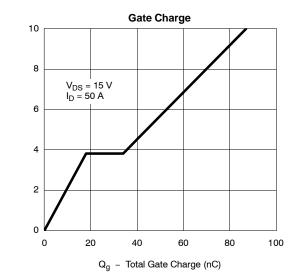




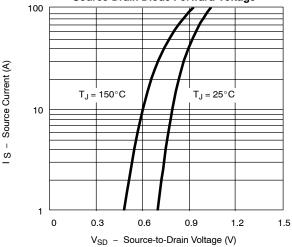


On-Resistance vs. Drain Current 0.010 0.008 R_{DS(on)} – On-Resistance (Ω) $V_{GS} = 4.5 V$ 0.006 V_{GS} = 10 V 0.004 0.002 0.000 80 100 20 40 60

I_D - Drain Current (A)



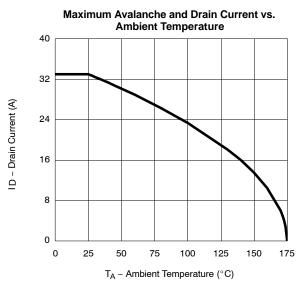
Source-Drain Diode Forward Voltage

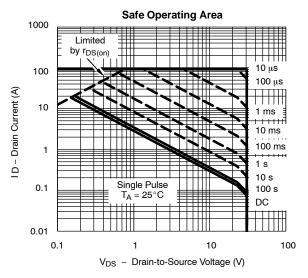


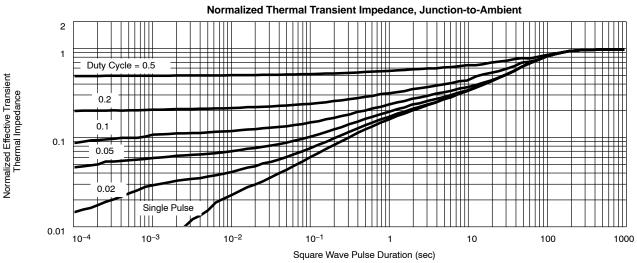
V GS - Gate-to-Source Voltage (V)

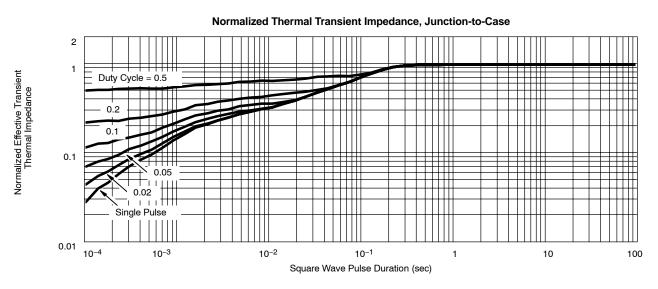


THERMAL RATINGS











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