



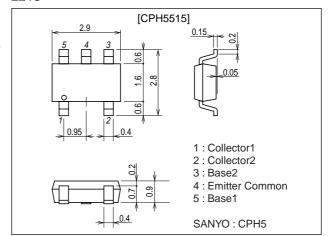
Switching Applications with Bias Resistance

Features

- On-chip bias resistance (R1=10k Ω , R2=10k Ω).
- Composite type with 2 transistors contained in the CPH package currently in use, improving the mounting efficiency greatly.
- The CPH5515 is formed with two chips, being equivalent to the 2SC3402, placed in one package.
- · Excellent in thermal equilibrium and pair capability.

Package Dimensions

unit : mm 2218



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		50	٧
Collector-to-Emitter Voltage	VCEO		50	٧
Emitter-to-Base Voltage	VEBO		10	٧
Collector Current	IC		100	mA
Collector Current (Pulse)	ICP		200	mA
Collector Dissipation	PC	1unit	350	mW
Total Power Dissipation	PT		500	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =40V, I _E =0			0.1	μΑ
	ICEO	V _{CE} =40V, I _E =0			0.5	μΑ
Emitter Cutoff Current	I _{EBO}	V _{EB} =5V, I _C =0	170	250	360	μΑ
DC Current Gain	hFE	VCE=5V, IC=10mA	50			
Gain-Bandwidth Product	fT	V _{CE} =10V, I _C =5mA		250		MHz
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		3.3		pF

Note: The specifications shown above are for each individual transistor.

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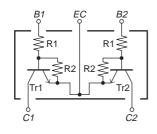
Marking: 3V

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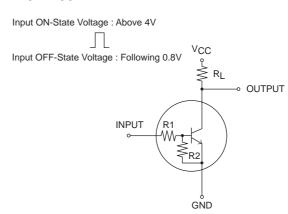
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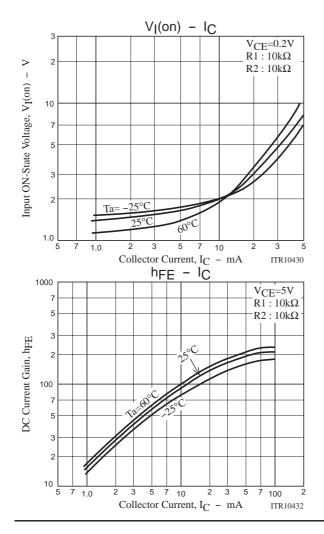
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =10mA, I _B =0.5mA		1.0	0.3	٧
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =10μA, I _E =0	50			٧
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=100μA, RBE=∞	50			>
Input OFF-State Voltage	V _I (off)	V _{CE} =5V, I _C =100μA	0.8	1.1	1.5	>
Input ON-State Voltage	V _I (on)	V _{CE} =0.2V, I _C =10mA	1.0	2.0	4.0	٧
Input Resistance	R1		7.0	10	13	kΩ
Resistance Ratio	R1 / R2		0.9	1.0	1.1	

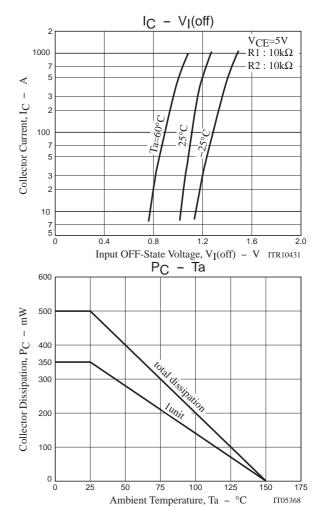
Electrical Connection



Sample Application Circuit







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