



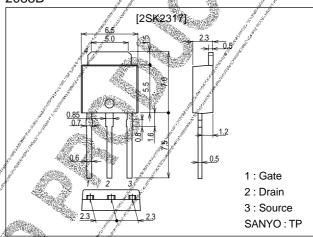
## **Ultrahigh-Speed Switching Applications**

#### **Features**

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

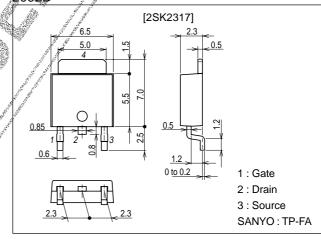
# Package Dimensions

2083B



unit:mm

2092B



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## **Specifications**

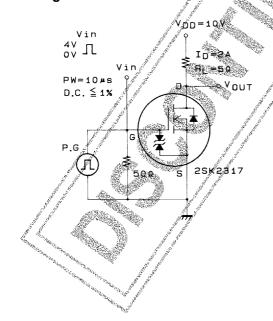
### Absolute Maximum Ratings at Ta = 25°C

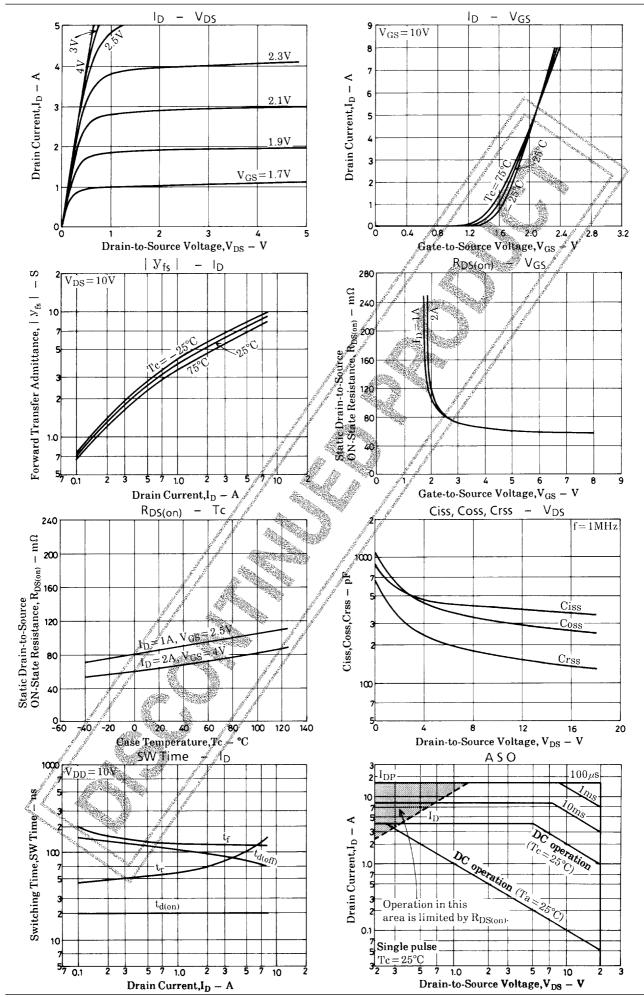
Parameter	Symbol	Conditions		Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>			20	V
Gate-to-Source Voltage	V <sub>GSS</sub>		į.	±10	V
Drain Current (DC)	I <sub>D</sub>		Service C	4	Α
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	100	16	Α
Allowable Power Dissipation	D-	J.	Jan San San San San San San San San San S		W
	PD	Tc=25°C	S.	20	W
Channel Temperature	Tch	, j. j. j.		, 150,	/C
Storage Temperature	Tstg	A A		_55 to +150	°°C

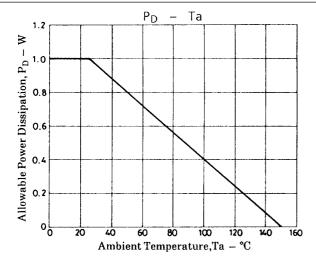
#### Electrical Characteristics at Ta = 25°C

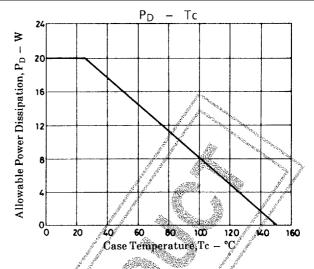
			Age. 100 (1982)	1.40.		
Parameter	Symbol	Conditions	min	Ratings/	max	Unit
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	20			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =16V, V <sub>GS</sub> =0		A September	100	μA
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0	2	g g g g	±10	μA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	<sub>3</sub> 0.5		1.5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =2A	<b>3</b> .5	5		S
Ctatis Danie to Course ON Ctate Danietone	R <sub>DS(on)</sub>	I <sub>D</sub> =2A, V <sub>GS</sub> =4V	See See	65	85	mΩ
Static Drain-to-Source ON-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =1A, V <sub>GS</sub> =2.5V	r de la companya della companya della companya de la companya della companya dell	85	125	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =10V, f=1MHz		400		pF
Output Capacitance	Coss	V <sub>DS</sub> =10V, #=1MHz		300		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =10V, f±1MHz		160		pF
Turn-ON Delay Time	td(on)	See specified Test Circuit		20		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		70		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		100		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		120		ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =4A, <b>V</b> <sub>GS</sub> =0		1.0	1.2	V

### **Switching Time Test Circuit**









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