

# UNISONIC TECHNOLOGIES CO., LTD

### UP9972

Preliminary

## N-CHANNEL ENHANCEMENT MODE

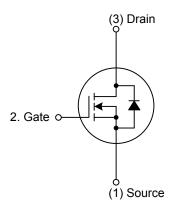
#### DESCRIPTION

The UTC **UP9972** is an N-ch enhancement mode Power MOS Field Effect Transistor using advanced technology to provide fast speed switching, low on-resistance and perfect cost-effectiveness.

The UTC **UP9972** is ideal for commercial-industrial surface mount applications applied to DC/DC converters or other low voltage applications.

#### FEATURES

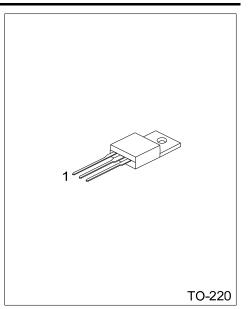
- \* Single drive required
- \* Fast switching capability
- \* Ultra low gate charge
- \* Halogen Free
- SYMBOL



#### ORDERING INFORMATION

Ordering Number		Dockogo	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UP9972L-TA3-T	UP9972G-TA3-T	TO-220	G	D	S	Tube	

UP9972 <u>L-TA3-T</u> (1)Packing Type (2)Package Type (3) Lead Plating	(1) T: Tube (2) TA3: TO-220 (3) G: Halogen Free, L: Lead Free
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#### ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	V <sub>DS</sub>	60	V	
Gate-Source Voltage	V <sub>GS</sub>	±25	V	
Continuous Drain Current (V <sub>GS</sub> =10V, T <sub>C</sub> =25°C)	ID	60	А	
Pulsed Drain Current (Note 2)	I <sub>DM</sub>	230	А	
Avalanche Current (Note 3)	I <sub>AR</sub>	30	А	
Single Pulse Avalanche Energy (Note 3)	E <sub>AS</sub>	45	mJ	
Power Dissipation (T <sub>C</sub> =25°C)	PD	89	W	
Junction Temperature	TJ	+150	°C	
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Pulse width limited by T<sub>J(MAX)</sub>

3. L = 100 $\mu$ H, V<sub>DD</sub> =30V, I<sub>AS</sub>=30A, R<sub>G</sub> =25 $\Omega$ , Starting T<sub>J</sub> =25°C

#### THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to ambient	$\theta_{JA}$	62	°C/W
Junction to case	θ <sub>JC</sub>	1.4	°C/W

#### ■ ELECTRICAL CHARACTERISTICS (TJ=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	
			IVIIIN		WAX	
	D\/	V -0V L -250WA	60			V
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA	60			V
Breakdown Voltage Temperature Coefficient	$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Reference to 25°C, $I_D$ =1mA		0.06		V/°C
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			10	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{GS}=\pm 25V, V_{DS}=0V$			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	1		3	V
Drain to Source On state Desistance (Nate)	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =35A			18	mΩ
Drain to Source On-state Resistance (Note)		V <sub>GS</sub> =4.5V, I <sub>D</sub> =25A			22	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	CISS			3170	5070	рF
Output Capacitance	C <sub>OSS</sub>	V <sub>GS</sub> =0V,V <sub>DS</sub> =25V, f=1.0MHz		280		рF
Reverse Transfer Capacitance	C <sub>RSS</sub>			230		рF
Gate Resistance	R <sub>G</sub>	f=1.0MHz		1.7		Ω
SWITCHING PARAMETERS						
Turn-ON Delay Time (Note)	t <sub>D(ON)</sub>			11		ns
Turn-ON Rise Time	t <sub>R</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V , I <sub>D</sub> =35A		58		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	R <sub>G</sub> =3.3Ω, R <sub>D</sub> =0.86Ω		45		ns
Turn-OFF Fall-Time	t⊧			80		ns
Total Gate Charge (Note)	$Q_{G}$			32	51	nC
Gate Source Charge	Q <sub>GS</sub>	I <sub>D</sub> =35A, V <sub>DS</sub> =48V, V <sub>GS</sub> =4.5V		8		nC
Gate Drain Charge	Q <sub>GD</sub>	]		20		nC
SOURCE- DRAIN DIODE RATINGS AND CHA	RACTERIS	STICS				
Drain-Source Diode Forward Voltage (Note)	V <sub>SD</sub>	I <sub>S</sub> =35A, V <sub>GS</sub> =0V			1.2	V
Reverse Recovery Time (Note)	t <sub>RR</sub>	I <sub>S</sub> =35A, V <sub>GS</sub> =0V		50		ns
Reverse Recovery Charge	$Q_{RR}$	dl/dt=100A/µs		48		nC
Noto: Dulas test						

Note: Pulse test



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