

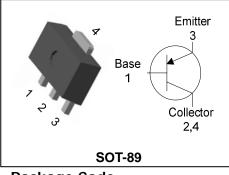
DP500F

PNP Silicon Transistor

Description

- Suitable for low voltage large current drivers
- Excellent h_{FE} Linearity
- Complementary pair with DN500
- Switching Application

PIN Connection



Ordering Information

6		
Type NO.	Marking	Package Code
DP500F	P5 □YWW	SOT-89
P5. DEV/I	CECODE 🗌 · hrs. rank VWW(V · Vear	r code WW · Weekly code)

P5: DEVICE CODE, □ : h_{FE} rank, YWW(Y : Year code, WW : Weekly code)

Absolute maximum ratings

Absolute maximum ratings			(Ta=25°C)
Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	V _{CBO}	-15	V
Collector-Emitter voltage	V _{CEO}	-12	V
Emitter-Base voltage	V _{EBO}	-5	V
Collector current	Ι _C	-5	А
Collector power dissipation	P _C	0.5	W
Collector power dissipation	P _c *	1	vv
Junction temperature	TJ	150	°C
Storage temperature	T _{stg}	-55~150	°C

* : When mounted on ceramic substrate(250 mm² \times 0.8t)

Electrical Characteristics

Electrical Characteristics (Ta=25°C)						=25°C)
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	I_{c} =-50 μ , I_{E} =0	-15	-	-	V
Collector-Emitter breakdown voltage	BV_{CEO}	$I_{C}=-1 \text{ mA}, I_{B}=0$	-12	-	-	V
Emitter-Base breakdown voltage	BV _{EBO}	I _E =-50 μA, I _C =0	-5	-	-	V
Collector cut-off current	I _{CBO}	V_{CB} =-12V, I_{E} =0	-	-	-1	μA
Emitter cut-off current	I _{EBO}	$V_{EB} = -5V, I_{C} = 0$	-	-	-1	μA
DC current gain	h _{FE1} *	V_{CE} =-1V, I _C =-100 mA	120	-	700	-
	h _{FE2}	V_{CE} =-1V, I_{C} =-3A	40	-	-	-
Collector-Emitter on voltage	V _{CE(sat1)}	I_{C} =-3A, I_{B} =-150 mA	-	-	-0.5	V
Base-Emitter on voltage	V _{BE(sat)}	I_{C} =-3A, I_{B} =-150 mA	-	-	-1.2	V
Transition frequency	f _T	V_{CB} =-5V, I_{C} =-500 mA	-	150	-	MHz
Collector output capacitance	C _{ob}	V_{CB} =-10V, I_{E} =0, f=1 MHz	-	-	50	pF
* : h _{FE} rank / O : 120 ~ 240, Y : 200 ~ 400, G : 350 ~ 700						

KSD-T5B020-000

DP500F

Electrical Characteristic Curves

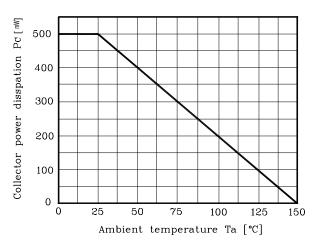


Fig. 1 Pc - Ta

Fig. 3 h_{FE} - I_C

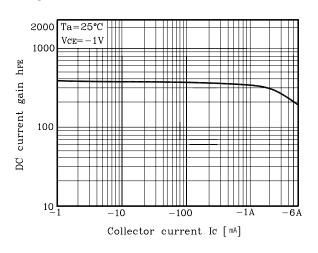
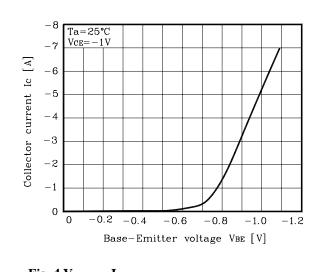
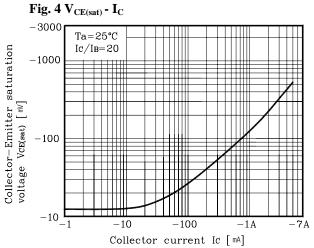
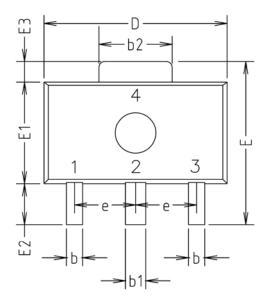


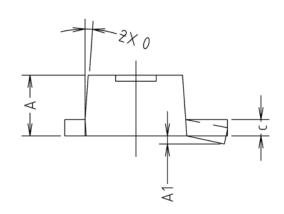
Fig. 2 Ic - V_{BE}





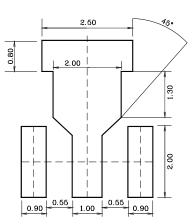
Outline Dimension(mm)





	MILLIMETERS			NOTE
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	
Α	1.40	1.50	1.60	
A1	0.00	—	0.10	
b	0.38	0.42	0.48	
b1	0.48	0.52	0.58	
b2	1.79	1.82	1.87	
С	0.40	0.42	0.46	
D	4.40	4.50	4.70	
E	3.70	4.00	4.30	
E1	2.40	2.50	2.70	
E2	0.80	1.00	1.20	
E3	0.40	0.50	0.60	
е		1.50 TYP.		
θ		4° TYP.		

*Recommend PCB solder land [Unit: mm]



DP500F

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