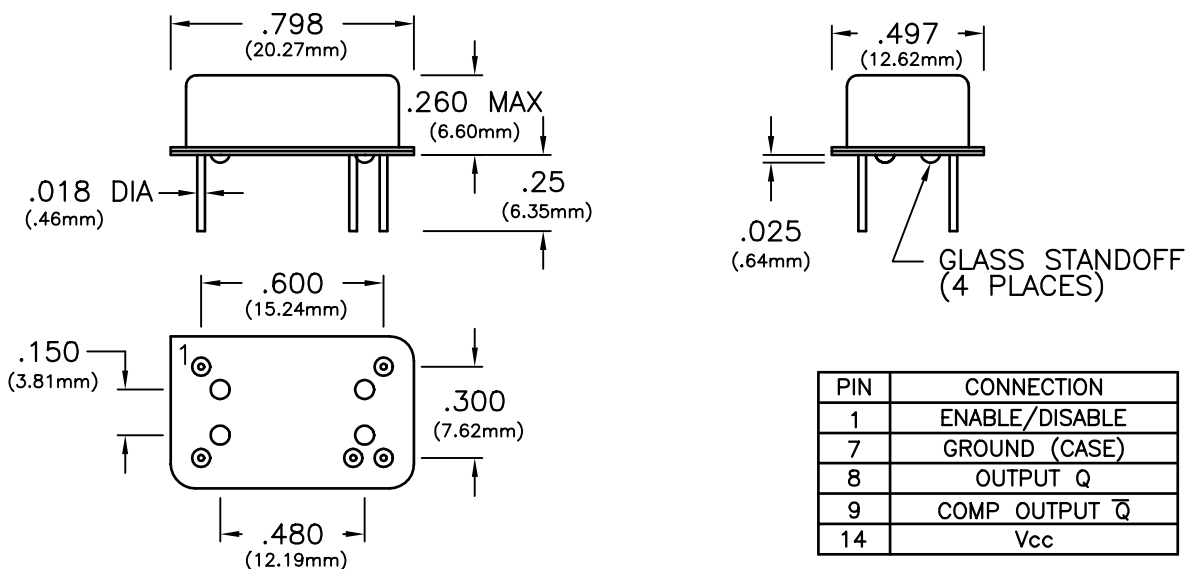
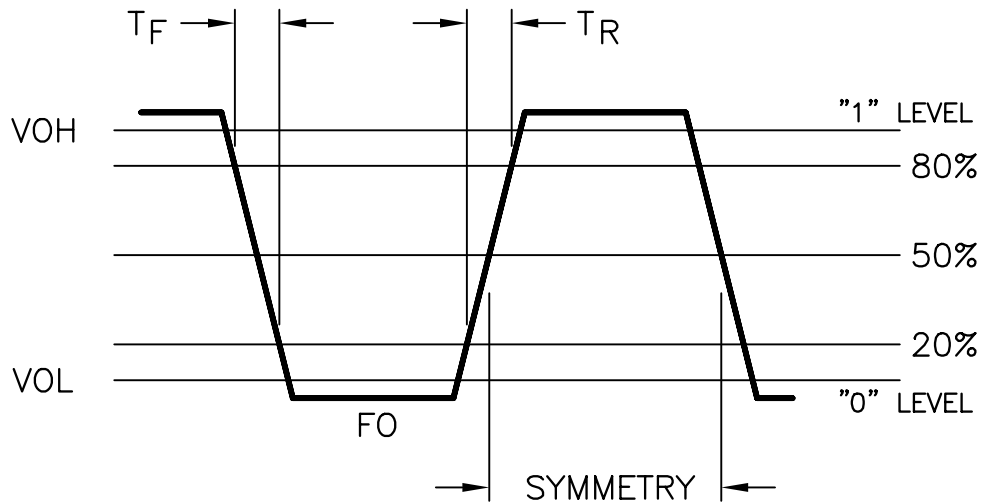


14 PIN DIP 3.3V PECL

SPECIFICATIONS	EE24-511		
Center Frequency (Fo)	300 MHz		
Frequency Stability	±25ppm		
	(Inclusive of calibration tolerance at 25°C, operating temperature range, input voltage change, load change, aging, shock and vibration)		
Temperature Range	0°C to +70°C		
Output	Waveform	Low Voltage, PECL Squarewave	
	Load	Output must be terminated into 50 ohms to Vcc-2V	
	Voltage	Voh	2.275 Minimum
		Vol	1.68 Maximum
	Duty Cycle	45/55 Maximum measured at 1.97V	
	Rise/Fall Time	1nS Maximum measured from 20% to 80%	
	Jitter	10pS RMS Maximum	
Enable/Disable (Pin 1)	Enable; Low Level (Vil) , Disable; High Level (Vih) NOTE: When oscillator is disabled the output Q is in a low state (Vol), Output \bar{Q} is in a high state (Voh). Outputs are enabled with Pin 1 floating		
Supply Voltage (Vcc)	+3.3Vdc ±5%		
Supply Current	60mA Maximum		
Package	All metal, hermetically sealed, welded package		



OUTPUT WAVEFORM



TEST CIRCUIT

