

FMV19N60E

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.0±0.5V) High avalanche durability

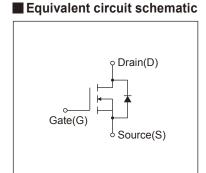
Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

■ Outline Drawings [mm] TO-220F(SLS) 0.5 %3



Description	Symbol	Characteristics	Unit	Remarks
Dunin Sauraa Valtana	V _{DS}	600	V	
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} = -30V
Continuous Drain Current	ID	±19	Α	
Pulsed Drain Current	IDP	±76	Α	
Gate-Source Voltage	V _G s	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	Iar	19	Α	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	799	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	13	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	6.5	kV/μs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Martin and Branch and Control	Po	2.16	14/	Ta=25°C
Maximum Power Dissipation		130	W	Tc=25°C
	Tch	150	°C	
Operating and Storage Temperature range	T _{stg}	-55 to + 150	°C	
Isolation Voltage	Viso	2	kVrms	t = 60sec, f = 60Hz

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions	Conditions		typ.	max.	Unit	
Drain-Source Breakdown Voltage	BVDSS	I _D =250µA, V _{GS} =0V		600	-	-	V	
Gate Threshold Voltage	V _{GS} (th)	In=250µA, Vns=Vgs	I _D =250μA, V _{DS} =V _{GS}		3.0	3.5	V	
Zero Gate Voltage Drain Current		V _{DS} =600V, V _{GS} =0V	T _{ch} =25°C	-	-	25		
	IDSS	V _{DS} =480V, V _{GS} =0V	T _{ch} =125°C	-	-	250	μA	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V	V _{GS} =±30V, V _{DS} =0V		10	100	nA	
Drain-Source On-State Resistance	Ros (on)	I _D =9.5A, V _{GS} =10V	I _D =9.5A, V _{GS} =10V		0.31	0.365	Ω	
Forward Transconductance	g _{fs}	I _D =9.5A, V _{DS} =25V		13	26	-	S	
Input Capacitance	Ciss	V _{DS} =25V		-	3600	5400	pF	
Output Capacitance	Coss	V _{GS} =0V	V _{GS} =0V		310	465		
Reverse Transfer Capacitance	Crss	f=1MHz		-	23	35		
Turn-On Time	td(on)	V_{cs} =300V V_{ds} =10V I_{D} =9.5A R_{cs} =8.2 Ω		-	26	39	ns	
	tr			-	13	20		
Turn-Off Time	td(off)			-	150	225		
	tf			-	20	30		
Total Gate Charge	Q _G	Vcc=300V	Vcc=300V		105	160	nC	
Gate-Source Charge	Q _{GS}	I _D =19A V _{GS} =10V		-	23	35		
Gate-Drain Charge	Q _{GD}			-	30	45		
Avalanche Capability	lav	L=1.71mH, Tch=25°C		19	-	-	А	
Diode Forward On-Voltage	V _{SD}	I _F =19A, V _{GS} =0V, T _{ch} =25°	I _F =19A, V _{GS} =0V, T _{ch} =25°C		0.90	1.35	V	
Reverse Recovery Time	trr	I _F =19A, V _{GS} =0V	I _F =19A, V _{GS} =0V		0.6	-	μs	
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	10	-	μC	

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			0.96	°C/W
	Rth (ch-a)	Channel to ambient			58.0	°C/W

Note *1 : Tch≤150°C

Note *2: Stating Tch=25°C, Ias=8A, L=22.9mH, Vcc=60V, R_G=50Ω

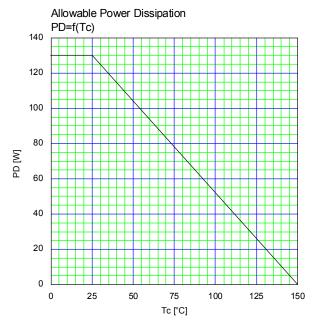
Eas limited by maximum channel temperature and avalanche current.

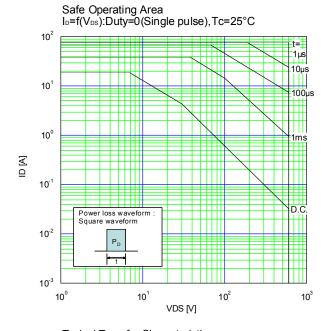
See to 'Avalanche Energy' graph.

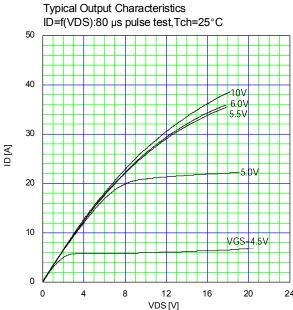
Note $^{\star}3$: Repetitive rating : Pulse width limited by maximum channel temperature

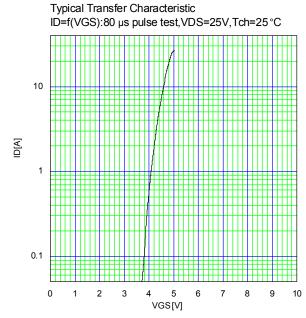
See to the 'Transient Themal impeadance' graph.

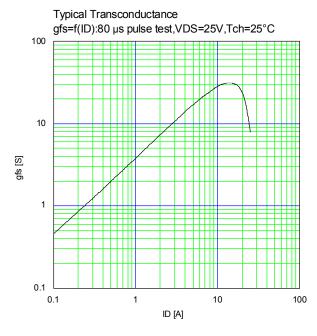
Note *4 : Irs-Ib, -di/dt=100A/µs, VccsBVbss, Tchs150°C.
Note *5 : Irs-Ib, dv/dt=5.0kV/µs, VccsBVbss, Tchs150°C.

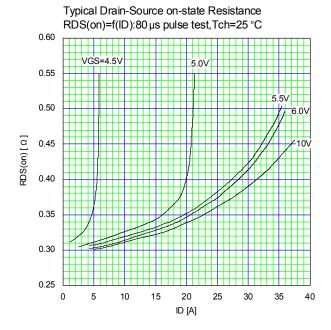


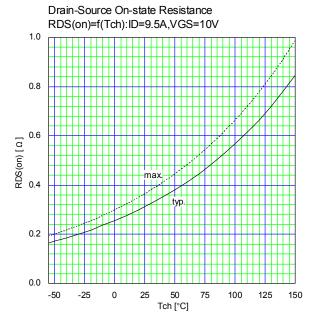


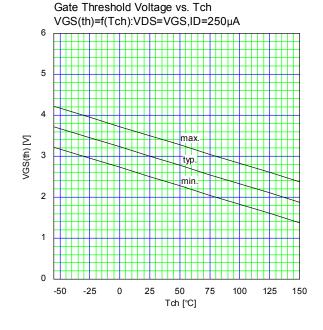


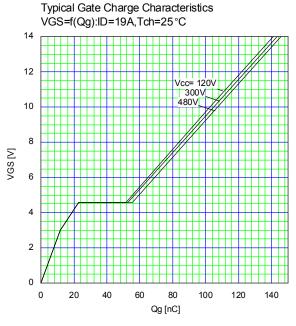


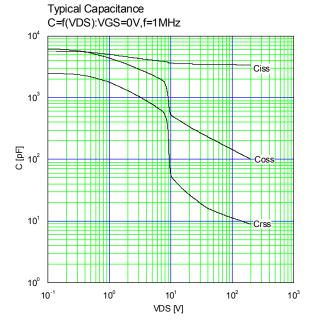


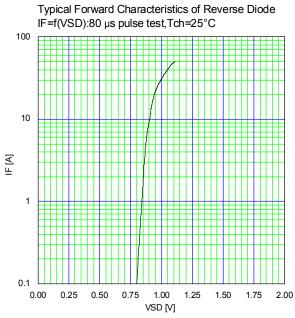


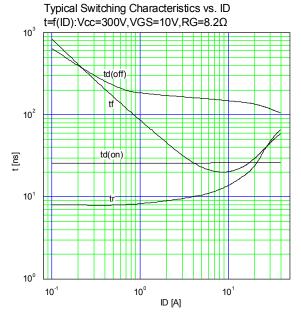


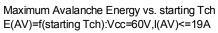


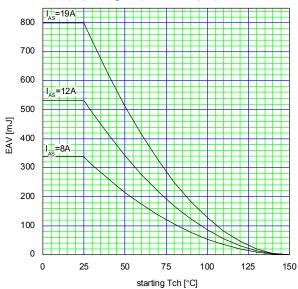


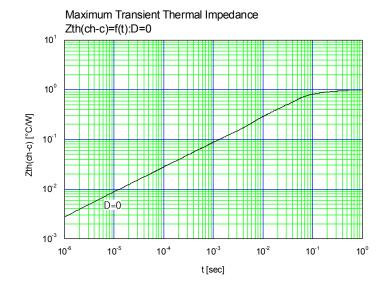












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