MEH Series

8 pin DIP, 5.0 Volt, ECL, PECL, Clock Oscillators

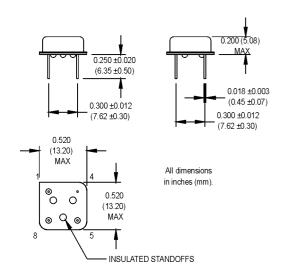


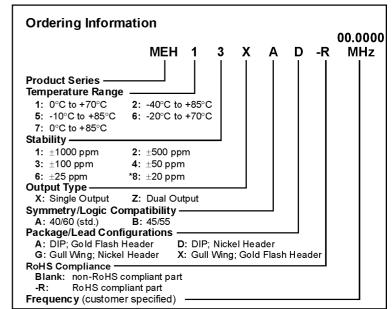






MEH Series ECL/PECL Half-Size Clock Oscillators, 10 KH Compatible with Optional Complementary Outputs





^{*}Contact factory for availability.

Pin Connections

| PIN | FUNCTION(S) (Model Dependent) | | | |
|-----|-------------------------------|--|--|--|
| 1 | N/C, Output #2 | | | |
| 4 | -Vee, Ground | | | |
| 5 | Output #1 | | | |
| 8 | +Vcc | | | |

| | PARAMETER | Symbol | Min. | Тур. | Max. | Units | Condition |
|---------------------------|------------------------|--|---|------|-----------|--------|------------------|
| Electrical Specifications | Frequency Range | F | 40 | | 133 | MHz | |
| | Frequency Stability | ∆F/F | (See Ordering Information) | | | | |
| | Operating Temperature | TA | (See Ordering Information) | | | | |
| | Storage Temperature | Ts | -55 | | +125 | °C | |
| | Input Voltage | Vcc | 4.75 | 5.0 | 5.25 | ٧ | |
| | Input Current | lee/lcc | | 35 | 60 | mA | |
| | Symmetry (Duty Cycle) | | (See Ordering Information) | | | | Vcc -1.3 V level |
| | Load | | 130 Ω to Vcc -2V or Thevenin Equivalent | | | | See Note 1 |
| | Rise/Fall Time | Tr/Tf | | | 2.5 | ns | See Note 2 |
| | Logic "1" Level | Voh | Vcc -0.98 | | | ٧ | |
| | Logic "0" Level | Vol | | | Vcc -1.63 | V | |
| | Cycle to Cycle Jitter | | | 11 | 25 | ps RMS | 1 Sigma |
| Environmental | Mechanical Shock | Per MIL-STD-202, Method 213, Condition C | | | | | |
| | Vibration | Per MIL-STD-202, Method 201 & 204 | | | | | |
| | Wave Solder Conditions | See page 147 | | | | | |
| | Hermeticity | Per MIL-STD-202, Method 112 (1 x 10 [®] atm.cc/s of helium) | | | | | |
| ᇤ | Solderability | Per EIAJ-STD-002 | | | | | |

- 1. Internally terminated outputs. See load circuit diagram #4.
- 2. Rise/Fall times are measured between Vcc -0.98 $\dot{\text{V}}$ and Vcc -1.63 $\dot{\text{V}}$.

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MtronPTI Lead Free Solder Profile

