TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

## TC74LCX32F,TC74LCX32FT,TC74LCX32FK

Low-Voltage Quad 2-Input OR Gate with 5-V Tolerant Inputs and Outputs

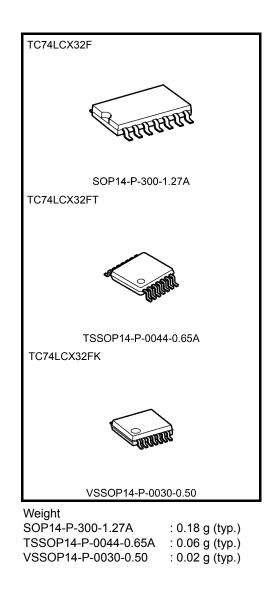
The TC74LCX32 is a high-performance CMOS 2-input OR gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low-power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

All inputs are equipped with protection circuits against static discharge.

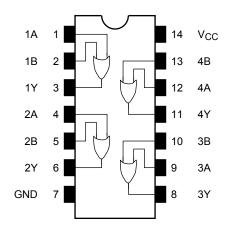
## Features

- Low-voltage operation: VCC = 1.65 to 3.6 V
- High-speed operation:  $t_{pd} = 5.5 \text{ ns} (\text{max}) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Output current:  $|I_{OH}|/I_{OL} = 24 \text{ mA} (\text{min}) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance:  $>\pm500$  mA
- Available in JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection is provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 32 type

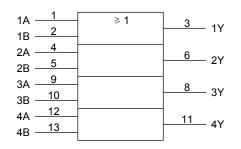


Note: The Electrical Characteristics of V<sub>CC</sub>=1.8 $\pm$ 0.15V is only applicable for products which manufactured from January 2009 onward.

## Pin Assignment (top view)



#### **IEC Logic Symbol**



## **Truth Table**

Inp	uts	Outputs
А	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

## Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	–0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	Vout	-0.5 to V <sub>CC</sub> + 0.5 (Note 3)	V
Input diode current	I <sub>IK</sub>	-50	mA
Output diode current	IOK	±50 (Note 4)	mA
DC output current	IOUT	±50	mA
Power dissipation	PD	180	mW
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	T <sub>stg</sub>	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND, V_{OUT} > V_{CC}$ 

## **Operating Ranges (Note 1)**

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	1.65 to 3.6	V
Tower supply voltage	vcc	1.5 to 3.6 (Note 2)	v
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Output voltage	Vout	0 to 5.5 (Note 3)	V
Output voltage	VOUT	0 to V <sub>CC</sub> (Note 4)	v
Output current	IOH/IOL	±24 (Note 5)	mA
Output current	'OH/'OL	±12 (Note 6)	IIIA
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

- Note 2: Data retention only
- Note 3:  $V_{CC} = 0 V$
- Note 4: High or low state
- Note 5:  $V_{CC} = 3.0$  to 3.6 V
- Note 6:  $V_{CC} = 2.7$  to 3.0 V
- Note 7:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V

## **Electrical Characteristics**

DC Characteristics (Ta = -40 to 85°C)

Characterist	Characteristics Symbol Test Condition		Min Max		Unit			
		0,			V <sub>CC</sub> (V)		Max	Onic
					1.65 to 2.3	$V_{CC} \times 0.9$		
	H-level	VIH	—		2.3 to 2.7	1.7	_	
					2.7 to 3.6	2.0		V
Input voltage					1.65 to 2.3		V <sub>CC</sub> × 0.1	v
	L-level	VIL	—		2.3 to 2.7		0.7	
					2.7 to 3.6	_	0.8	
				$I_{OH} = -100 \ \mu A$	1.65 to 3.6	V <sub>CC</sub> -0.2		
				I <sub>OH</sub> = -4 mA	1.65	1.05	_	
	H-level	V <sub>OH</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OH</sub> = -8 mA	2.3	1.7		· · · ·
	n-level			I <sub>OH</sub> = -12 mA	2.7	2.2	_	
				I <sub>OH</sub> = -18 mA	3.0	2.4	_	
Output voltage				I <sub>OH</sub> = -24 mA	3.0	2.2	_	
Output voltage				$I_{OL} = 100 \ \mu A$	1.65 to 3.6	_	0.2	
				$I_{OL} = 4 \text{ mA}$	1.65	_	0.45	
	I. I. averal			I <sub>OL</sub> = 8 mA	2.3	_	0.7	
	L-level	V <sub>OL</sub>	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 12 mA	2.7	_	0.4	
				I <sub>OL</sub> = 16 mA	3.0		0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μA
Power-off leakage curr	r-off leakage current $I_{OFF}$ $V_{IN}/V_{OUT} = 5.5$ V 0		$V_{IN}/V_{OUT} = 5.5 V$		0		10.0	μA
Quiescent supply current			$V_{IN} = V_{CC}$ or GND		1.65 to 3.6		10.0	
	ли 	Icc	V <sub>IN</sub> = 3.6 to 5.5 V		1.65 to 3.6		±10.0	μA
Increase in Icc per inpu	ncrease in Icc per input		$V_{IH} = V_{CC} - 0.6 \ V$		2.7 to 3.6		500	

#### AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition		Min	Max	Unit
	t <sub>pLH</sub> t <sub>pHL</sub>		$1.8\pm0.15$	_	20.0	
Propagation delay time		Figure 1, Figure 2	$2.5\pm0.2$	_	7.2	
			2.7	_	6.2	ns
			$\textbf{3.3}\pm\textbf{0.3}$	1.5	5.5	
Output to output skew	t <sub>osLH</sub>	()	2.7			ns
	t <sub>osHL</sub>	(Note)	$3.3\pm 0.3$		1.0	115

Note: Parameter guaranteed by design.

 $(t_{\text{OSLH}} = |t_{\text{pLHm}} - t_{\text{pLHn}}|, t_{\text{OSHL}} = |t_{\text{pHLm}} - t_{\text{pHLn}}|)$ 

#### Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5 \text{ ns}$ , $C_L = 50 \text{ pF}$ , $R_L = 500 \Omega$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic $V_{OL}$	V <sub>OLP</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V
Quiet output minimum dynamic $V_{OL}$	V <sub>OLV</sub>	$V_{IH} = 3.3 V, V_{IL} = 0 V$	3.3	0.8	V

#### Capacitive Characteristics ( $Ta = 25^{\circ}C$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	—	3.3	7	pF
Output capacitance	C <sub>OUT</sub>	—	0	8	pF
Power dissipation capacitance	C <sub>PD</sub>	f <sub>IN</sub> = 10 MHz (Note	) 3.3	25	pF

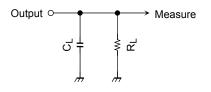
Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$  (per gate)

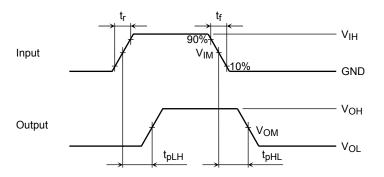
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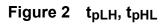
## **AC Test Circuit**





## AC Waveform





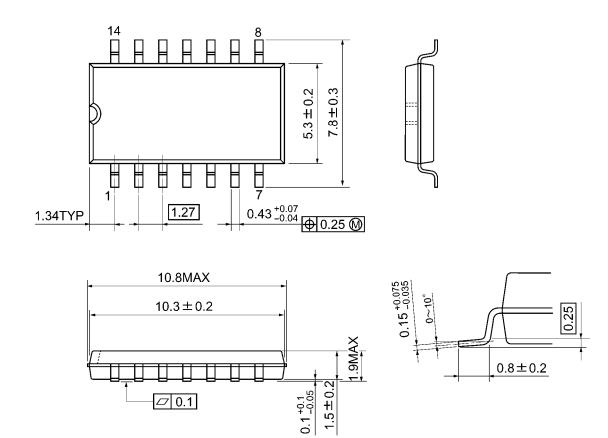
		Vcc					
	Symbol	3.3 ± 0.3 V 2.7V	$2.5\pm0.2~V$	$1.8\pm0.15~V$			
Input	VIH	2.7V	V <sub>CC</sub>	V <sub>CC</sub>			
	VIM	1.5V	V <sub>CC</sub> /2	V <sub>CC</sub> /2			
	tr,tf	2.5ns	2.0ns	2.0ns			
Output	V <sub>OM</sub>	1.5V	V <sub>OH</sub> /2	V <sub>OH</sub> /2			
Load	CL	50pF	30pF	30pF			
	RL	500 Ω	500 Ω	1kΩ			



## **Package Dimensions**

SOP14-P-300-1.27A

Unit: mm

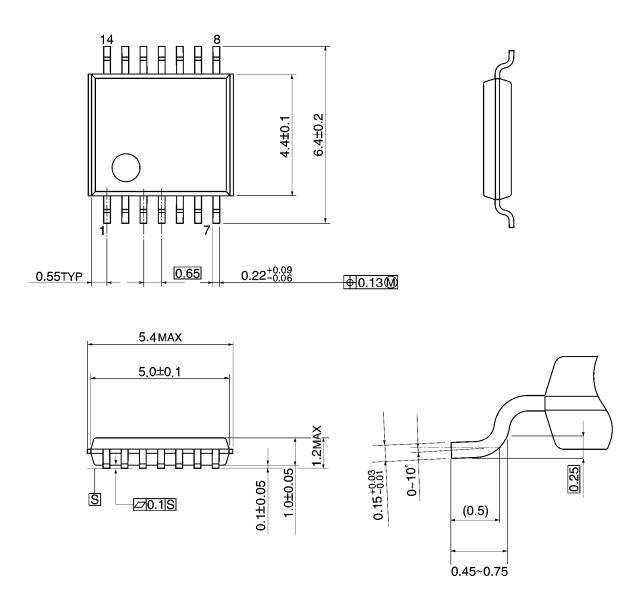


Weight: 0.18 g (typ.)

## Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



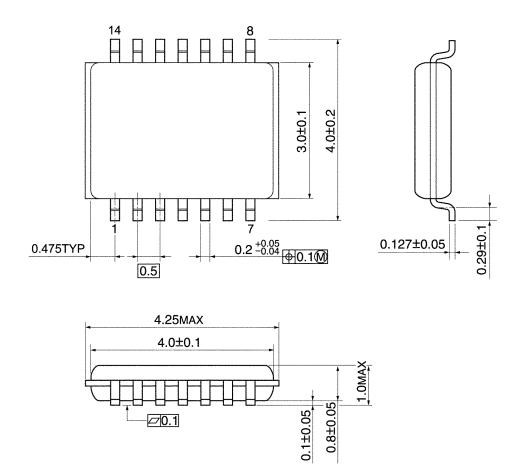
Weight: 0.06 g (typ.)



## **Package Dimensions**

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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