SPEC

Spec No.	TQ3C-8EAF0-E1DDP62-00
Date	November 5, 2008

#### TYPE: TCG057VGLBC-G00

< 5.7 inch VGA transmissive color TFT with LED backlight, and touch panel >

#### **CONTENTS**

- 1. Application
- 2. Construction and outline
- 3. Mechanical specifications
- 4. Absolute maximum ratings
- 5. Electrical characteristics
- 6. Optical characteristics
- 7. Interface signals
- 8. Input timing characteristics
- 9. Backlight characteristics
- 10. Design guidance for analog touch panel
- 11. Lot number identification
- 12. Warranty
- 13. Precautions for use
- 14. Reliability test data
- 15. Outline drawing

Issued
Date: Nov.14,2008
KYDEERA
Hayato LCD Division

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by: Engineering dept.			Confirmed by: QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
November 5, 2008	S. Kofima	H.Tokumer)	G Matremoto	J. Sakaguchi	Ho , Suf

Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	-

# Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

#### Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	-

# Revision record

Date		Designe	ed by:	Engineering of	lept.	Confirmed by	: QA dept.
		Prepa	red	Checked	Approved	Checked	Approved
D	D :				D		
Rev.No.	Date	Page			Descripti	ons	



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	1

# 1. Application

This document defines the specification of TCG057VGLBC-G00. (RoHS Compliant)

#### 2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(without constant current circuit for LED backlight)

Touch panel : Analog type(Glass/Glass), Glare Anti-reflection treatment

#### 3. Mechanical specifications

#### 3-1. LCD

Item	Specification	
Outline dimensions	127.2 (W)× 100.4 (H) × 6.9 (D)	mm
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	
Dot format	640×(B,G,R) (W) × 480 (H)	
Dot pitch	0.06 (W) × 0.18 (H)	
Base color 1)	Normally White	
Mass	(TBD)	g

1) Due to the characteristics of the LCD material, the color varies with environmental temperature.

# 3-2. Touch panel

Item	Specification	Unit
Input	Radius-0.8 stylus or Finger	
Actuation Force	0.1 ~ 2.0	N
Transmittance	Typ. 85	%
Surface hardness	Pencil hardness 2H or more according	-



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	2

#### 4. Absolute maximum ratings

#### 4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	0	4.0	V
Input signal voltage 1)	$V_{\rm IN}$	-0.3	6.0	V
LED forward current 2)	IF	-	30	mA
Reversed voltage 2)	VR	-	5	V
Supply voltage for touch panel	$V_{\mathrm{TP}}$	0	6.0	V
Input current of touch panel	$I_{\mathrm{TP}}$	0	0.5	mA

- 1) Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, Hsync, Vsync, ENAB, R/L, U/D
- 2) For each "AN1-CA1", "AN2-CA2" and "AN3-CA3" Temp. = 25°C

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	Тор	-20	70	°C
Storage temperature	2)	Tsto	-30	80	°C
Operating humidity	3)	$H_{\mathrm{OP}}$	10	4)	%RH
Storage humidity	3)	Нѕто	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. = -30°C < 48h , Temp. = 80°C < 168h

Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

- 3) Non-condensing
- 4) Temp. 40°C, 85%RH Max.

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

Frequency	10 ~ 55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total)

**EIAJ ED-2531** 

6) Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

**EIAJ ED-2531** 



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	3

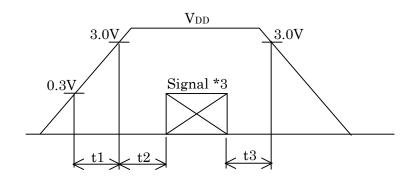
### 5. Electrical characteristics

#### 5-1. LCD

Temp. =  $-20 \sim 70$ °C

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic 1)	$ m V_{DD}$	-	3.0	3.3	3.6	V
Current consumption for logic	${ m I}_{ m DD}$	2)	-	170	220	mA
Permissive input ripple voltage	$V_{\mathrm{RP}}$	-	1	1	100	mVp-p
Invest signal valtage 2)	$ m V_{IL}$	"Low" level	0	-	$0.3V_{\mathrm{DD}}$	V
Input signal voltage 3)	$V_{\mathrm{IH}}$	"High" level	$0.7V_{\mathrm{DD}}$	-	$V_{ m DD}$	V

#### 1) V<sub>DD</sub>-turn-on conditions



0 < t1 20ms

0 < t2 50 ms

0 < t3 1s

2) Display pattern:

$$V_{DD}$$
 = 3.3V, Temp. = 25°C

 $\frac{1}{2}$ 

2

3

479

480

(dot)

3) Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, Hsync, Vsync, ENAB, R/L, U/D

# 5-2. Touch panel

Item	Specification			
Supply voltage for touch panel	$5.0\mathrm{V}$			
The control of the co	$xL \sim xR : (200\Omega \sim 1,000\Omega)$			
Terminal resistance	$yU \sim yL : (200\Omega \sim 1,000\Omega)$			
Linearity	less than $\pm (2.5)\%$			
Insulation resistance	$50 \mathrm{M}\Omega$ or more at DC25V			



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	4

# 6. Optical characteristics

Measuring spot = 6.0mm, Temp. = 25°C

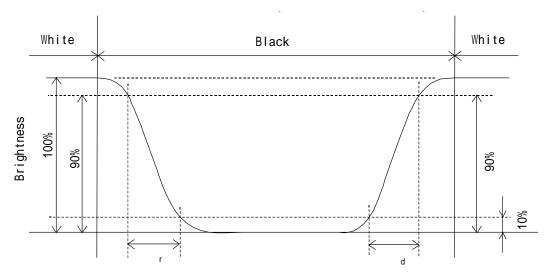
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D	Rise	τr	= =0°	-	10	-	ms
Response time	Down	τd	= =0°	-	25	-	ms
T7 1		UPPER		-	80	-	1
Viewing angle View direction	_	LOWER	CR 5	-	80	-	deg.
: 6 o'cloc		LEFT	CK 5	-	80	-	1
(Gray in	version)	$\phi$ right		-	80	-	deg.
Contrast ratio		CR	= =0°	300	500	-	-
Brightness	Brightness		IF=15mA/Line	145	210	-	cd/m²
	Red	X	= =0°	0.56	0.61	0.66	
		У	0	0.32	0.37	0.42	
	C	X	= =0°	0.29	0.34	0.39	
Chromaticity	Green	У	0	0.52	0.57	0.62	
coordinates	Dl	X	= =0°	0.09	0.14	0.19	-
	Blue	У	0	0.06	0.11	0.16	
	W/h:40	X	= =0°	0.28	0.33	0.38	
	White	У	0	0.30	0.35	0.40	

### 6-1. Definition of contrast ratio

CR(Contrast ratio) = Brightness with all pixels "White"

Brightness with all pixels "Black"

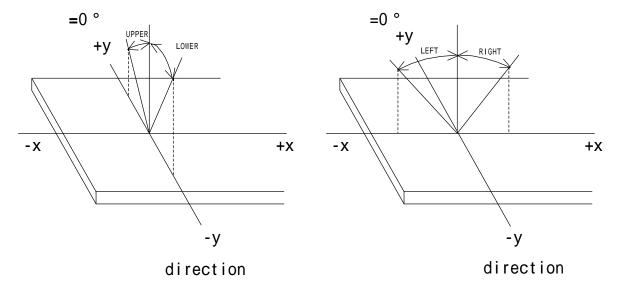
# 6-2. Definition of response time



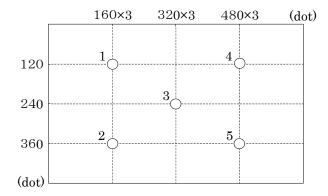


Spec No		Part No.	Page
TQ3C-8	8EAF0-E1DDP62-00	TCG057VGLBC-G00	5

# 6-3. Definition of viewing angle



### 6-4. Brightness measuring points



- 1) Rating is defined on the average in the viewing area.
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	6

# 7. Interface signals

# 7-1. LCD panel and LED

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	Hsync	Horizontal synchronous signal (negative)	I	
4	$V_{ m SYNC}$	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	1)
28	$V_{ m DD}$	3.3V power supply	-	
29	$V_{ m DD}$	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L: Normal, H: Left / Right reverse mode	I	2)
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	I	2)
32	NC	No connect	-	
33	CA1	Cathode 1	-	
34	CA2	Cathode 2	-	
35	CA3	Cathode 3	-	
36	NC	No connect	-	
37	AN1	Anode 1	-	
38	AN2	Anode 2	-	
39	AN3	Anode 3	-	
40	NC	No connect	-	

LCD connector : IMSA-9681S-40A-GF (IRISO)

Recommended matching FFC or FPC : 0.5mm pitch



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	7

1) The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined. Don't keep ENAB "High" during operation.

2)



R/L = LU/D = H



R/L = HU/D = H



R/L = LU/D = L



R/L = HU/D = L

### 7-2. Touch panel

No.	Symbol	Description
1	уL	y-Lower terminal
2	хL	x-Left terminal
3	уU	y-Upper terminal
4	xR	x-Right terminal

Touch panel side connector : 1.25mm pitch

Recommended matching connector : 04FFS-SP-GB-TF(LF)(SN) (JST)

: 00-8370-049-000-888+ (ELCO)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	8

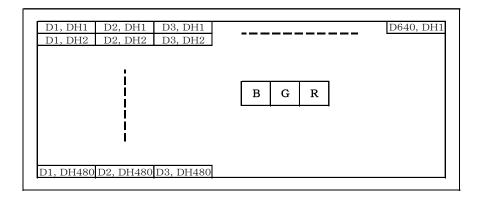
# 8. Input timing characteristics

#### 8-1. Timing characteristics

	Item	Symbol	Min	Тур	Max	Unit	Note
Cll-	Frequency	1/Tc	22.66	25.18	27.69	MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
D. A	Set up time	Tds	10	-	-	ns	
Data	Hold time	Tdh	10	-	-	ns	
	Cools	WH.	30.0	31.8	-	μs	
	Cycle	TH	770	800	850	clock	
Horizontal sync. signal	Pulse width	THp	5	30	-	clock	
orginar	Set up time	THs	10	-	-	ns	
	Hold time	THh	10	-	-	ns	
	Cycle	TV	515	525	535	line	ENAB=L
TT 1		1 V	515	525	560	line	With ENAB
Vertical sync. signal	Pulse width	TVp	1	3	5	line	
- 0	Set up time	TVs	10	-	-	ns	
	Hold time	TVh	10	-	-	ns	
	Pulse width	ТЕр		640		clock	
Enable signal (ENAB)	Set up time	TEs	10	-	-	ns	
(BITTE)	Hold time	TEh	10	-	-	ns	
H <sub>SYNC</sub> - Enable s	ignal phase difference	THE	112	144	175	clock	
Harnes - Varnes aim	mal phase difference	THV	0	-	4	clock	ENAB=L
$H_{SYNC}$ - $V_{SYNC}$ signal phase difference		1ПV	10	-	-	ns	With ENAB
Vertical sync. signal start position		TVE	2	35	76	line	
Horizontal displa	THd		640		clock		
Vertical display	TVd	480			line		

- 1) When ENAB is fixed at "Low", the horizontal display starts from the data of C144 (clock) as shown in 8-3.
- 2) When ENAB is fixed at "Low", the vertical sync. signal start position is 35 (line) as shown in 8-3.
- 3) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

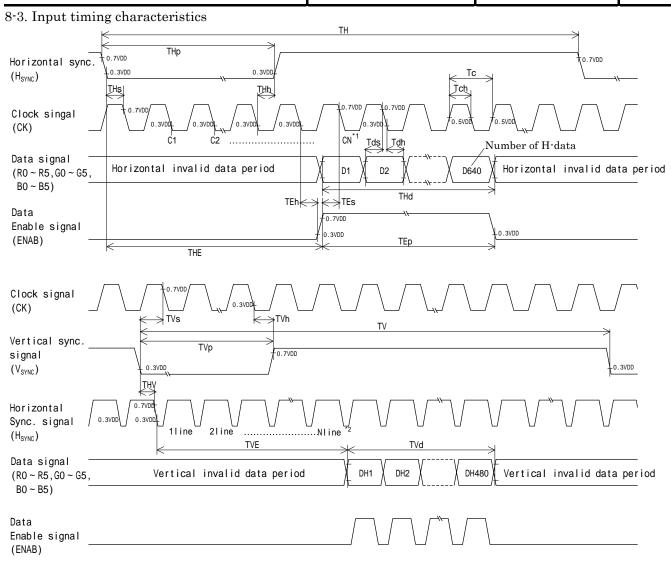
### 8-2. Input Data Signals and Display position on the screen





 Spec No.
 Part No.
 Page

 TQ3C-8EAF0-E1DDP62-00
 TCG057VGLBC-G00
 9



- 1) When ENAB is fixed at "Low", the horizontal display starts from the data of C144 (clock).
- 2) When ENAB is fixed at "Low", the vertical sync. signal start position is 35 (line).



Ī	Spec No.	Part No.	Page
	TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	10

#### 9. Backlight characteristics

#### LED ratings

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	1)	IF	•	15	-	mA	Ta=-20 ~ 70°C
			•	22.1	25.0	V	IF=15mA, Ta=-20
Forward voltage	1)	VF	-	21.7	24.5	V	IF=15mA, Ta=25
			-	21.3	24.1	V	IF=15mA, Ta=70
Operating life time	2), 3)	Т	•	40,000	-	h	IF=15mA, Ta=25

- 1) For each "AN1-CA1", "AN2-CA2" and "AN3-CA3"
- 2) When brightness decrease 50% of initial brightness.
- 3) Life time is estimated data. (Condition: IF=15mA, Ta=25 in chamber).
- 4) An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.

#### 10. Design guidance for analog touch panel

- 10-1. Electrical (In customer's design, please remember the following considerations.)
  - 1) Do not use the current regulated circuit.
  - 2) Keep the current limit with top and bottom layer. (Please refer to "Electrical absolute maximum ratings" for details.)
  - 3) Analog touch panel can not sense two points touching separately.
  - 4) A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read of the touch panel position data.
  - 5) Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

#### 10-2. Software

- 1) Do the "User Calibration".
- 2) "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3) When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

#### 10-3. Mounting on display and housing bezel

- 1) Do not use an adhesive tape to bond it on the front of touch panel and hang it to the housing
- 2) This touch panel has an airtight but not watertight structure. Please not to use it for the applications requiring watertight or under the environments occurred condensation. If it is expected to be exposed to the environments that vapor, moisture or other liquids may seep inside a bezel, please be sure to take some measurements for drip-proof or waterproof by using sealing materials on the bezel.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	11

#### 11. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2008	2009	2010	2011	2012	2013
Code	8	9	0	1	2	3

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

#### 12. Warranty

#### 12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 12-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	12

#### 13. Precautions for use

#### 13-1. Installation of the LCD

- 1) The LCD shall be installed so that there is no pressure on the LSI chips.
- 2) The LCD shall be installed flat, without twisting or bending.
- 3) Must maintain a gap between inside of bezel and touch panel to avoid malfunction or electrode damage of touch panel.
- 4) A transparent protection sheet is attached to the touch panel. Please remove the protection film slowly before use, paying attention to static electricity.

#### 13-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

#### 13-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

#### 13-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 13-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3) When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4) Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1DDP62-00	TCG057VGLBC-G00	13

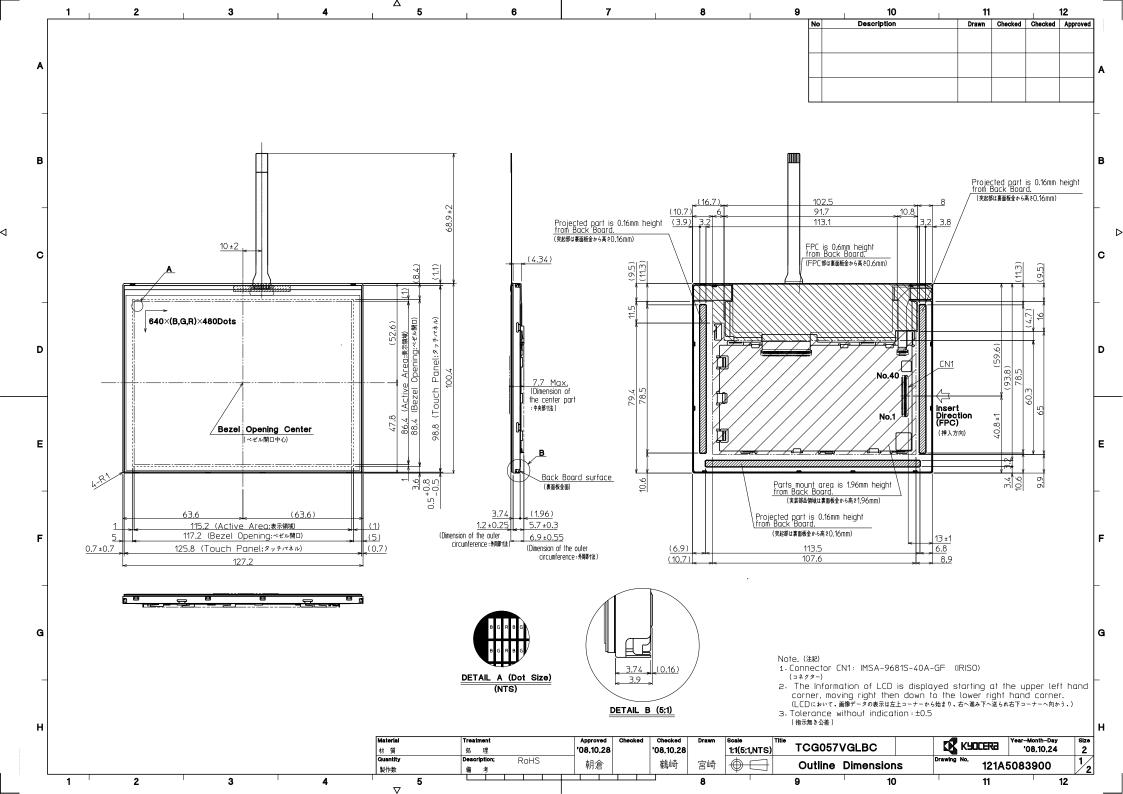
# 14. Reliability test data

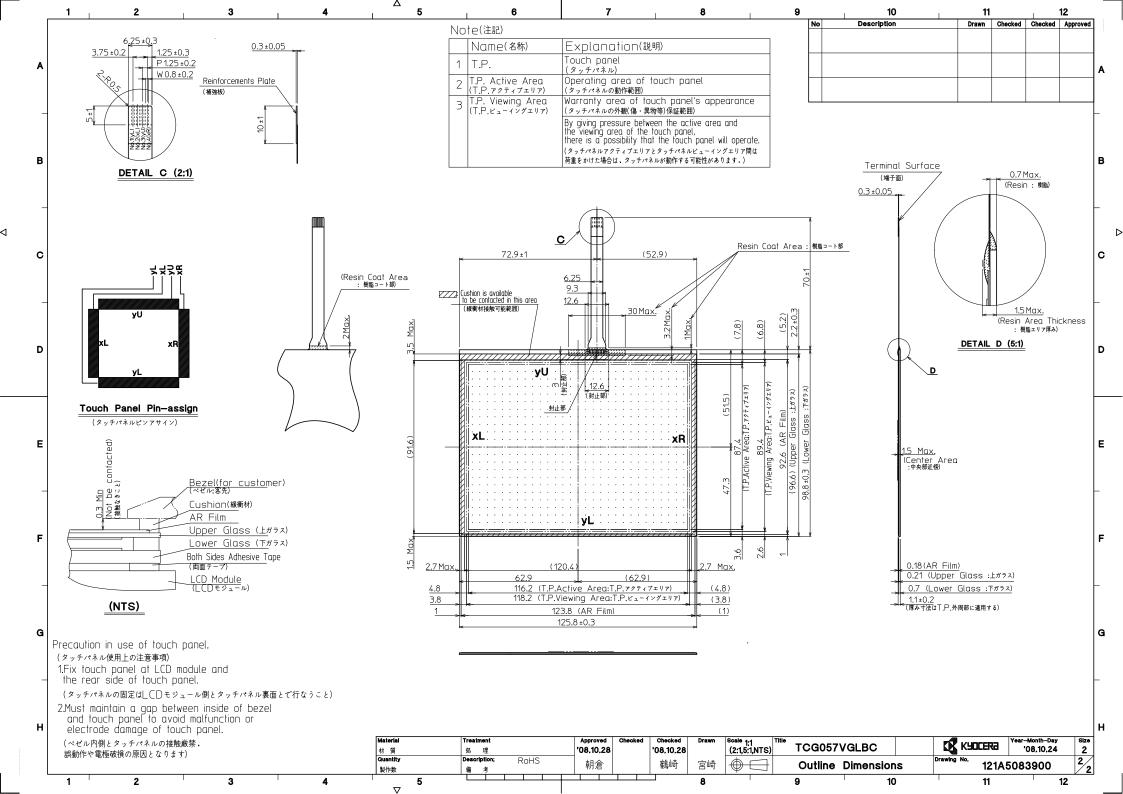
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>
High temp. operation	70°C	500h	Display function Display quality Current consumption	<ul><li>No defect</li><li>No defect</li><li>No defect</li></ul>
Point Activation life	Polyacetal stylus R4, Hardness 60° Hitting force 2.9N Hitting speed 5 time/s	one million times	Terminal resistance Insulation resistance Linearity Actuation Force	<ul><li>: No defect</li><li>: No defect</li><li>: No defect</li><li>: No defect</li></ul>

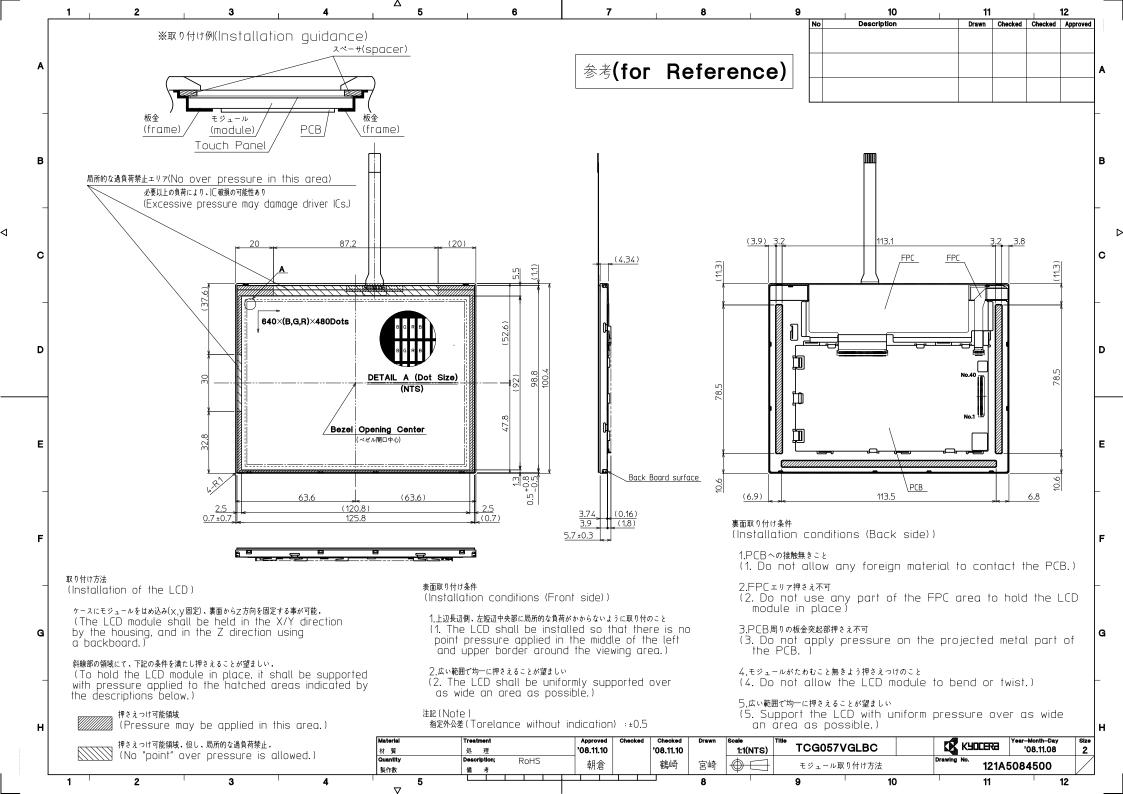
- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

  The reliability test is conducted only to examine the LCD's capability.









Spec No.	TQ3C-8EAF0-E2DDP62-00
Date	November 5, 2008

# KYOCERA INSPECTION STANDARD

TYPE: TCG057VGLBC-G00

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Engineering de	pt.	Confirmed by : QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved
November 5, 2008	S. Kojima	H.Tokumer)	G Marsemoto	J. Sakaguchi	To , Suf



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDP62-00	TCG057VGLBC-G00	-

# Revision record

Date		Designed by :		Engineering dept.		Confirmed by : QA dept.	
		Prepa	red	Checked	Approved	Checked	Approved
Rev.No.	Date	Page			Description	ons	



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDP62-00	TCG057VGLBC-G00	1

# Visuals specification 1) Note

1) Note	1								
			Note						
General	<ol> <li>Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent.</li> </ol>								
	2. This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area.								
	3. Inspection conditions								
	Lumina	ance	: 500 Lux min.						
	Inspect	ion distance	: 300 mm.						
	Temper	rature	: 25 ± 5						
	Direction	on	: Directly above						
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the						
inspection item			LCD, even when all "Black" data sent to the screen.						
			Inspection tool: 5% Transparency neutral density filter.						
			Count dot: If the dot is visible through the filter.						
			Don't count dot: If the dot is not visible through the						
			filter.  RGBRGBRGB  RGBRGBRGB  dot defect						
		Black dot defect	The dot is constantly "off" when power applied to the						
			LCD, even when all "White" data sent to the screen.						
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot						
			defects or black dot defects.						
			R G B R G B R G B R G B R G B R G B R G B R G B R G B						
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non						
	inspection	Foreign particle (Polarizer, Cell,	operating.						
		Backlight)	December 1 and 1 and 1 and 1						
		Appearance inspection	Does not satisfy the value at the spec.						
	Others	LED wire	Damaged to the LED wires, connector, pin, functional						
	Others	DED WIFE	failure or appearance failure.						
	Definition	Definition of							
	of size								
		d = (a +	h)/2						



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDP62-00	TCG057VGLBC-G00	2

#### 2) Standard

2) Standa		1 -						
Classification		Inspection item		Judgement standard				
Defect	Dot	Bright dot defect		Acceptable number	: 4			
(in LCD			Bright dot spacing			or more		
glass)		Black dot defect		Acceptable number : 5				
				Bright dot spacing	or more			
			Bright dot	Acceptable number : 2				
			defect	Acceptable number · 2				
			Black dot defect	Acceptable number		: 3		
		3 or more	dots join	Acceptable number		: 0		
		Total dot d	lefects	Acceptable number				
	Others	White dot,	Dark dot	•				
		(Circle)		Size (mm	n)	Ac	ceptable number	
				d 0.2		110	(Neglected)	
				0.2 < d	0.4		5	
				0.4 < d			3	
				0.5 < d	d		0	
E-+1	:4:	Dalasias (	(C4-1-)					
(Defect or	inspection	Polarizer (Scratch)		Width (mm)	I are orthogonal		A	
Polarizer				Width (mm) W 0.1	Length (mm)		Acceptable number (Neglected)	
					L 5.0		(Neglected)	
between I				0.1 < W 0.3	5.0 < L	0.0	0	
and LCD glass)				0.3 < W	-		0	
		D.1 /	D 111.1.)					
		Polarizer (Bubble)		G: . (	.\	Α.		
				Size (mm) d 0.2		Acceptable number (Neglected)		
				0.2 < d 0.3		(Neglected)		
				0.3 < d 0.5		3		
				0.5 < d		0		
		Foncian no	untiala				-	
		Foreign particle ( Circular shape )		Size (mm) Ac			aantahla numban	
				d 0.2		Acceptable number (Neglected)		
				0.2 < d 0.4		(Neglecteu)		
ļ				0.4 < d 0.5		3		
<u> </u>				0.5 < d		0		
		T						
		Foreign particle (Linear shape)		***************************************				
				Width (mm)	Length (mm)		Acceptable number	
		Scratch		W 0.03		9.0	(Neglected)	
				0.03 < W 0.1	$\frac{L}{2.0} < \frac{2.0}{4.0}$		(Neglected)	
				0.00 , 11 0.1	$\frac{2.0 < L}{4.0 < L}$	4.0	0	
				0.1 < W	4.0 \ L		(According to	
							circular shape)	
							circular shape)	



Spec No.	Part No.	Page
TQ3C-8EAF0-E2DDP62-00	TCG057VGLBC-G00	3

Inspection item	Judgement standard								
Scratch,	( W = Width, L = Length, D = Diameter = (major axis + minor axis)/ 2)								
Foreign particle	Item	Width(mm)	T .			eceptable number			
(Touch screen		d 0.03 L		20	_				
portion)		0.03 < d 0.05 L		10 2pc		ces within φ20mm			
	Scratch			6	2pces within φ20mm				
		0.08 < d 0.1	L	4	1pces within φ30mm				
	Foreign	W 0.05	Negle	cted		Neglected			
	( line like )	0.05 < W 0.1	L	5	2pc	es within 30mm			
	Foreign	D	0.2			Neglected			
	( circle like )	0.2 < D	0.3		2pc	es within 30mm			
	Above are applie	ed to the visible area.			1				
	Unless there a	re foreign particle and o	damage a	affected	seriou	sly to the electrical			
	performance out of the active area, we approve of this product.								
Glass crack	_	a. (	`			Acceptable			
(Touch screen	Item	Size (mm)				number			
portion)			ı	X	3				
		/	2 /	Λ	ა				
	Conner crack	X Y Y Y			3	2 pcs			
		XXX				/panel			
			1	Z	< t				
		<b>\</b>		X	5				
	Crack in	× * * * * * * * * * * * * * * * * * * *	$\langle \cdot \rangle$	Λ					
	other area			Y	1.5	2 pcs			
	than in		Z			/side			
	corner	2			< t				
		•		<u> </u>					
	D	\\/				0			
	Progressive crack					0 pcs (NG even 1pcs)			
	crack					(NG even 1pcs)			
	Above are applied to the visible area.								
	Unless there are foreign particle and damage affected seriously to the electrical								
performance out of the active area, we approve of this product.									
Newton's ring	All Newton Rings	s in the center of the screen	must be	rejected					
(Touch screen	Border around the screen are permitted.								
portion)	छ। प्राप्त वार्याम् शार अराज्याम् प्राप्त अराज्याम् अराज्याम्यस्याम्यस्यस्य								
						-			
						Ta			
	NG OK								
	Į.								

