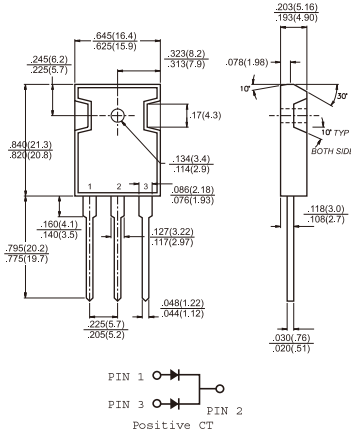


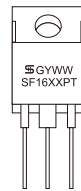


TO-3P/TO-247AD



Dimensions in inches and (millimeters)

Marking Diagram



SF16XXPT = Specific Device Code
 G = Green Compound
 Y = Year
 WW = Work Week

Features

- ◇ UL Recognized File # E-326243
- ◇ Dual rectifier construction, positive center-tap
- ◇ Plastic package has Underwriters Laboratory Flammability Classifications 94V-0
- ◇ Glass passivated chip junctions
- ◇ Superfast recovery time, high voltage
- ◇ Low forward voltage, high current capability
- ◇ Low thermal resistance
- ◇ Low power loss, high efficiency
- ◇ High temperature soldering guaranteed: 260°C / 10 seconds, 0.16"(4.06mm) lead lengths at 5 lbs.(2.3kg) tesion
- ◇ Green compound with suffix "G" on packing code & prefix "G" on datecode.

Mechanical Data

- ◇ Cases: JEDEC TO-3P/TO-247AD molded plastic
- ◇ Terminals: Pure tin plated, lead free, solderable per MIL-STD-750, Method 2026
- ◇ Polarity: As marked
- ◇ Mounting position: Any
- ◇ Weight: 5.6 grams

Maximum Ratings and Electrical Characteristics

Rating at 25 °C ambient temperature unless otherwise specified.
 Single phase, half wave, 60 Hz, resistive or inductive load.
 For capacitive load, derate current by 20%

Type Number	Symbol	SF 1601 PT	SF 1602 PT	SF 1603 PT	SF 1604 PT	SF 1605 PT	SF 1606 PT	SF 1607 PT	SF 1608 PT	Units
Maximum Recurrent Peak Reverse Voltage	VRRM	50	100	150	200	300	400	500	600	V
Maximum RMS Voltage	VRMS	35	70	105	140	210	280	350	420	V
Maximum DC Blocking Voltage	VDC	50	100	150	200	300	400	500	600	V
Maximum Average Forward Rectified Current at Tc=100 °C	IF(AV)	16								A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	IFSM	150								A
Maximum Instantaneous Forward Voltage @8.0A	VF	0.95		1.3		1.7				V
Maximum DC Reverse Current at @ TA=25 °C Rated DC Blocking Voltage(Note 1)@ TA=125 °C	IR			10		500				uA
Maximum Reverse Recovery Time(Note 2)Tj=25 °C	Trr	35								nS
Typical Junction Capacitance (Note 4)	Cj	85.0								pF
Typical Thermal Resistance (Note 3)	RθJC	2								°C/W
Operating Junction Temperature Range	Tj	-55 to +150								°C
Storage Temperature Range	TSTG	-55 to +150								°C

- Notes: 1. Pulse Test with PW=300 usec,1% Duty Cycle
 2. Measured at 1 MHz and Applied Reverse Voltage of 4.0V D.C.
 3. Reverse Recovery Test Conditions: I_F=0.5A, I_R=1.0A, Recover to 0.25A.
 4. Mounted on Heatsink Size of 3" x 5" x 0.25" Al-Plate.

RATINGS AND CHARACTERISTIC CURVES (SF1601PT THRU SF1608PT)

FIG.1- MAXIMUM FORWARD CURRENT DERATING CURVE

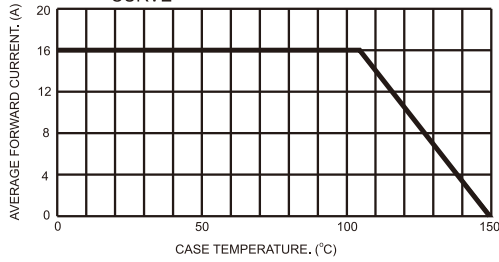


FIG.2- TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS PER LEG

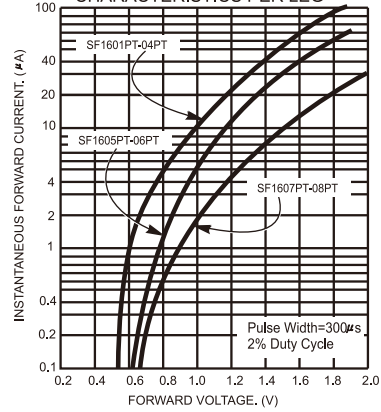


FIG.3- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT PER LEG

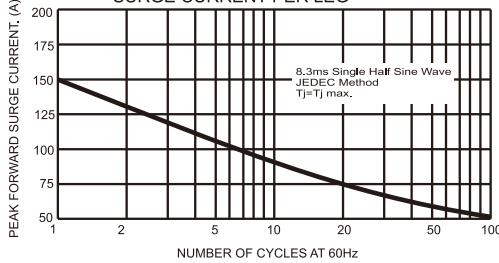


FIG.5- TYPICAL REVERSE CHARACTERISTICS

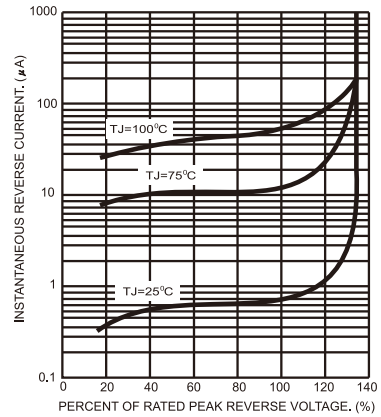


FIG.4- TYPICAL JUNCTION CAPACITANCE PER LEG

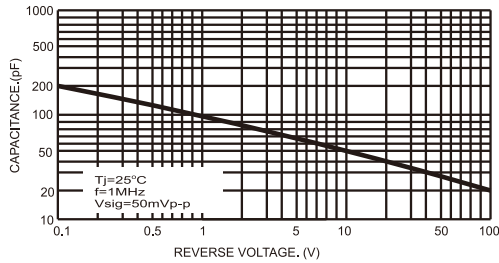
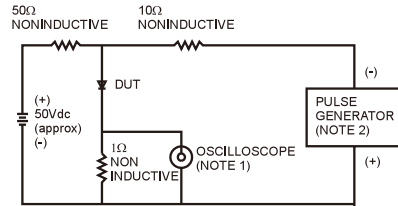


FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



NOTES: 1. Rise Time=7ns max. Input Impedance= 1 megohm 22pf
2. Rise Time=10ns max. Source Impedance= 50 ohms

