

# DIM1600FSS12-L000

# **Single Switch IGBT Module**

DS5968-1 August 2010 (LN27436)

## **FEATURES**

- 10µs Short Circuit Withstand
- Isolated Cu Base with Al<sub>2</sub>O<sub>3</sub> Substrates
- Optimised for high frequency operation
- Lead Free construction

#### **APPLICATIONS**

- High Power Inverters
- Motor Controllers
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM1600FSS12-L000 is a single switch 1200V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for high frequency operation.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

#### **ORDERING INFORMATION**

Order As:

## DIM1600FSS12-L000

Note: When ordering, please use the complete part number

#### **KEY PARAMETERS**

$V_{CES}$		1200V
V <sub>CE(sat)</sub>	* (typ)	3.1V
I <sub>C</sub>	(max)	1600A
I <sub>C(PK)</sub>	(max)	3200A

<sup>\*</sup> Measured at the power busbars, not the auxiliary terminals

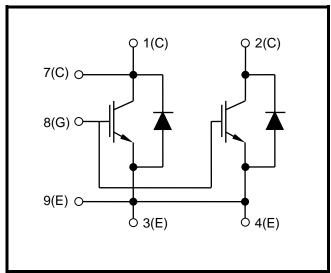


Fig. 1 Circuit configuration



Fig. 2 Package



## **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	$V_{GE} = 0V$	1200	V
$V_{GES}$	Gate-emitter voltage		±20	V
I <sub>C</sub>	Continuous collector current	T <sub>case</sub> = 80°C	1600	Α
I <sub>C(PK)</sub>	Peak collector current	1ms, T <sub>case</sub> = 110°C	3200	Α
P <sub>max</sub>	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	13890	W
l <sup>2</sup> t	Diode I <sup>2</sup> t value	$V_R = 0$ , $t_p = 10$ ms, $T_j = 125$ °C	400	kA <sup>2</sup> s
V <sub>isol</sub>	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2500	V

#### THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R <sub>th(j-c)</sub>	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	9	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	20	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
_	Junction temperature	Transistor	-	-	150	°C
T <sub>j</sub>		Diode	-	-	125	°C
T <sub>stg</sub>	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	ı	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



## **ELECTRICAL CHARACTERISTICS**

 $T_{case}$  = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
	Collector cut-off current	$V_{GE} = 0V$ , $V_{CE} = V_{CES}$			2	mA
I <sub>CES</sub>		$V_{GE} = 0V$ , $V_{CE} = V_{CES}$ , $T_{case} = 125$ °C			50	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			8	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_C = 80$ mA, $V_{GE} = V_{CE}$	4.5	5.5	6.5	V
\ \ \ †	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 1600A		3.1	3.9	V
V <sub>CE(sat)</sub> †		V <sub>GE</sub> = 15V, I <sub>C</sub> = 1600A, T <sub>j</sub> = 125°C		3.3	4.1	V
I <sub>F</sub>	Diode forward current	DC			1600	Α
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms			3200	Α
v, t	Diode forward voltage	I <sub>F</sub> = 1600A		2.1	2.4	V
$V_F^{\dagger}$		I <sub>F</sub> = 1600A, T <sub>j</sub> = 125°C		2.1	2.4	V
C <sub>ies</sub>	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		180		nF
Qg	Gate charge	±15V		18		μC
C <sub>res</sub>	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$				nF
L <sub>M</sub>	Module inductance			15		nΗ
R <sub>INT</sub>	Internal transistor resistance			135		μΩ
SC <sub>Data</sub>	Short circuit current, I <sub>SC</sub>	$T_{j} = 125^{\circ}C, V_{CC} = 900V$ $t_{p} \le 10\mu s, V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*}x dI/dt$ IEC 60747-9		9000		A

 $<sup>^{\</sup>dagger}$  Measured at the power busbars, not the auxiliary terminals  $^{\star}$  L is the circuit inductance +  $L_{\rm M}$ 



## **ELECTRICAL CHARACTERISTICS**

T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_{C} = 1600A$ $V_{GF} = \pm 15V$		370		ns
t <sub>f</sub>	Fall time			260		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 600V$		330		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = 1.5\Omega$		280		ns
t <sub>r</sub>	Rise time	$R_{G(OFF)} = 1.5\Omega$ $L_S \sim 100$ nH		270		ns
E <sub>ON</sub>	Turn-on energy loss			60		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	I <sub>F</sub> = 1600A		150		μC
I <sub>rr</sub>	Diode reverse recovery current	V <sub>CE</sub> = 600V		750		Α
E <sub>rec</sub>	Diode reverse recovery energy	$dI_F/dt = 8200A/\mu s$		90		mJ

## $T_{case}$ = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time			1320		ns
t <sub>f</sub>	Fall time	$I_{C} = 1600A$ $V_{GF} = \pm 15V$		350		ns
E <sub>OFF</sub>	Turn-off energy loss	$V_{CE} = 600V$		360		mJ
t <sub>d(on)</sub>	Turn-on delay time	$R_{G(ON)} = 1.5\Omega$ $R_{G(OFF)} = 1.5\Omega$ $L_S \sim 100 \text{nH}$		570		ns
t <sub>r</sub>	Rise time			310		ns
E <sub>ON</sub>	Turn-on energy loss			115		mJ
Q <sub>rr</sub>	Diode reverse recovery charge	$I_F = 1600A$ $V_{CE} = 600V$ $dI_F/dt = 7500A/\mu s$		350		μC
I <sub>rr</sub>	Diode reverse recovery current			900		Α
E <sub>rec</sub>	Diode reverse recovery energy			160		mJ



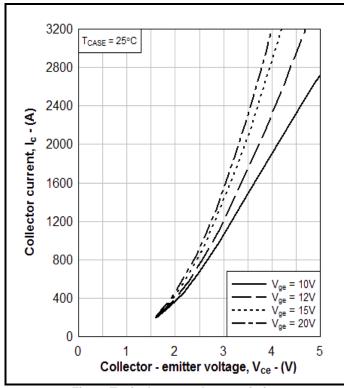


Fig. 3 Typical output characteristics

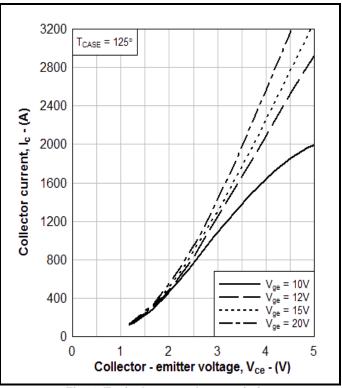


Fig. 4 Typical output characteristics

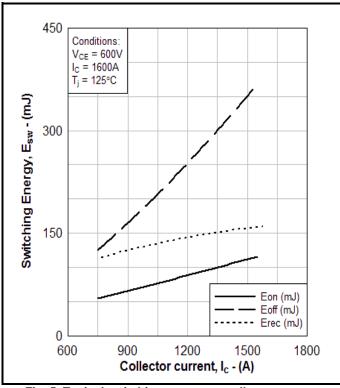


Fig. 5 Typical switching energy vs collector current

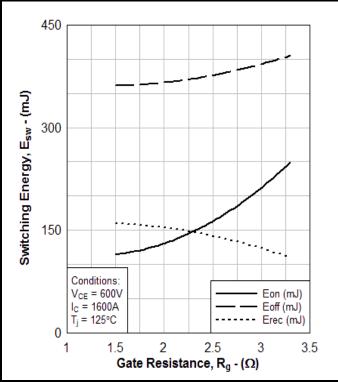


Fig. 6 Typical switching energy vs gate resistance



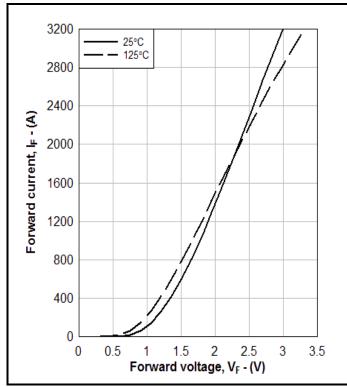


Fig. 7 Diode typical forward characteristics

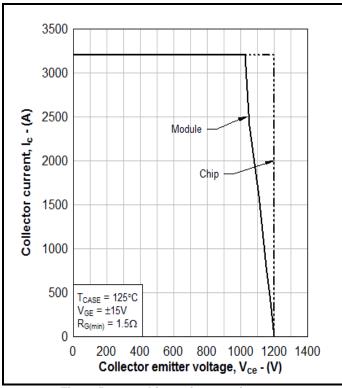


Fig. 8 Reverse bias safe operating area

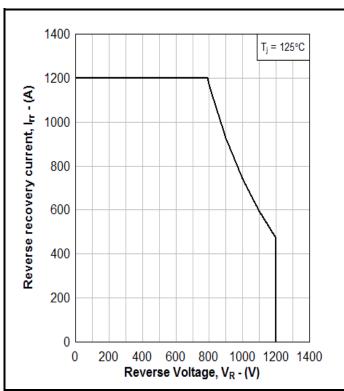


Fig. 9 Diode reverse bias safe operating area

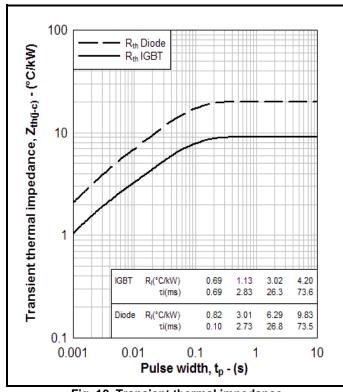


Fig. 10 Transient thermal impedance



## **PACKAGE DETAILS**

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

## DO NOT SCALE.

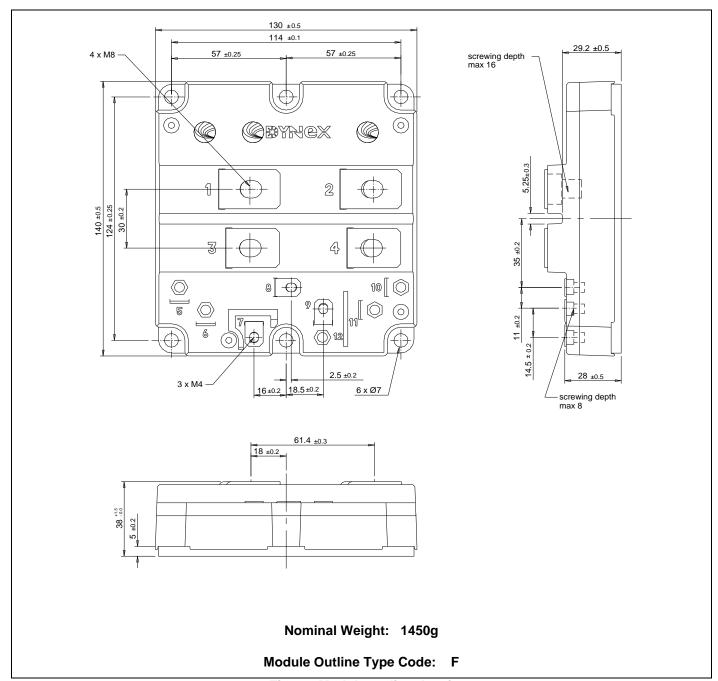


Fig. 11 Module outline drawing



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