

# RJK0660DPA

## Silicon N Channel Power MOS FET Power Switching

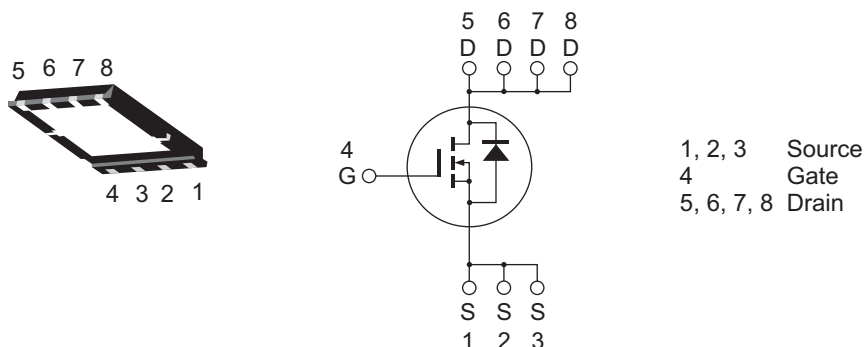
R07DS0346EJ0100  
Rev.1.00  
Apr 06, 2011

### Features

- High speed switching
- Low drive current
- High density mounting
- Low on-resistance  
 $R_{DS(on)} = 4.2 \text{ m}\Omega$  typ. (at  $V_{GS} = 10 \text{ V}$ )
- Pb-free
- Halogen-free

### Outline

RENESAS Package code: PWSN0008DC-B  
(Package name: WPAK(3))



### Absolute Maximum Ratings

( $T_a = 25^\circ\text{C}$ )

| Item                                   | Symbol                            | Ratings     | Unit                      |
|--|-----------------------------------|-------------|---------------------------|
| Drain to source voltage                | $V_{DSS}$                         | 60          | V                         |
| Gate to source voltage                 | $V_{GSS}$                         | $\pm 20$    | V                         |
| Drain current                          | $I_D$                             | 40          | A                         |
| Drain peak current                     | $I_{D(pulse)}$ <sup>Note 1</sup>  | 160         | A                         |
| Body-drain diode reverse drain current | $I_{DR}$                          | 40          | A                         |
| Avalanche current                      | $I_{AP}$ <sup>Note 2</sup>        | 20          | A                         |
| Avalanche energy                       | $E_{AR}$ <sup>Note 2</sup>        | 30          | mJ                        |
| Channel dissipation                    | $P_{ch}$ <sup>Note 3</sup>        | 65          | W                         |
| Channel to case thermal impedance      | $\theta_{ch-c}$ <sup>Note 3</sup> | 1.93        | $^\circ\text{C}/\text{W}$ |
| Channel temperature                    | $T_{ch}$                          | 150         | $^\circ\text{C}$          |
| Storage temperature                    | $T_{stg}$                         | -55 to +150 | $^\circ\text{C}$          |

Notes: 1.  $PW \leq 10 \mu\text{s}$ , duty cycle  $\leq 1\%$   
2. Value at  $T_{ch} = 25^\circ\text{C}$ ,  $R_g \geq 50 \Omega$   
3.  $T_c = 25^\circ\text{C}$

## Electrical Characteristics

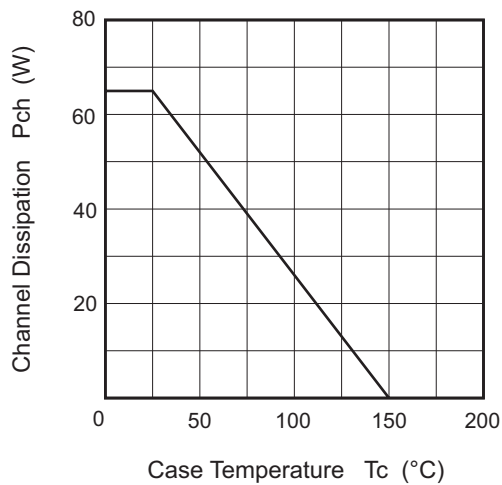
(Ta = 25°C)

| Item                                       | Symbol        | Min | Typ  | Max       | Unit             | Test Conditions  |
|--|---------------|-----|------|-----------|------------------|--|
| Drain to source breakdown voltage          | $V_{(BR)DSS}$ | 60  | —    | —         | V                | $I_D = 10 \text{ mA}$ , $V_{GS} = 0 \text{ V}$   |
| Gate to source leak current                | $I_{GSS}$     | —   | —    | $\pm 0.1$ | $\mu\text{A}$    | $V_{GS} = \pm 20 \text{ V}$ , $V_{DS} = 0 \text{ V}$   |
| Zero gate voltage drain current            | $I_{DSS}$     | —   | —    | 1         | $\mu\text{A}$    | $V_{DS} = 60 \text{ V}$ , $V_{GS} = 0 \text{ V}$   |
| Gate to source cutoff voltage              | $V_{GS(off)}$ | 2.0 | —    | 4.0       | V                | $V_{DS} = 10 \text{ V}$ , $I_D = 1 \text{ mA}$   |
| Static drain to source on state resistance | $R_{DS(on)}$  | —   | 4.2  | 5.1       | $\text{m}\Omega$ | $I_D = 20 \text{ A}$ , $V_{GS} = 10 \text{ V}$ <sup>Note4</sup>  |
| Forward transfer admittance                | $ y_{fs} $    | —   | 60   | —         | S                | $I_D = 20 \text{ A}$ , $V_{DS} = 10 \text{ V}$ <sup>Note4</sup>  |
| Input capacitance                          | $C_{iss}$     | —   | 3600 | —         | pF               | $V_{DS} = 10 \text{ V}$ , $V_{GS} = 0 \text{ V}$ ,<br>$f = 1 \text{ MHz}$  |
| Output capacitance                         | $C_{oss}$     | —   | 830  | —         | pF               |  |
| Reverse transfer capacitance               | $C_{rss}$     | —   | 230  | —         | pF               |  |
| Gate Resistance                            | $R_g$         | —   | 1.1  | —         | $\Omega$         |  |
| Total gate charge                          | $Q_g$         | —   | 45   | —         | nC               | $V_{DD} = 25 \text{ V}$ , $V_{GS} = 10 \text{ V}$ ,<br>$I_D = 40 \text{ A}$  |
| Gate to source charge                      | $Q_{gs}$      | —   | 20   | —         | nC               |  |
| Gate to drain charge                       | $Q_{gd}$      | —   | 7.5  | —         | nC               |  |
| Turn-on delay time                         | $t_{d(on)}$   | —   | 18   | —         | ns               | $V_{GS} = 10 \text{ V}$ , $I_D = 20 \text{ A}$ ,<br>$V_{DD} \cong 30 \text{ V}$ , $R_L = 1.5 \Omega$ ,<br>$R_g = 4.7 \Omega$ |
| Rise time                                  | $t_r$         | —   | 14   | —         | ns               |  |
| Turn-off delay time                        | $t_{d(off)}$  | —   | 43   | —         | ns               |  |
| Fall time                                  | $t_f$         | —   | 11   | —         | ns               |  |
| Body-drain diode forward voltage           | $V_{DF}$      | —   | 0.8  | 1.1       | V                | $I_F = 40 \text{ A}$ , $V_{GS} = 0 \text{ V}$ <sup>Note4</sup>   |
| Body-drain diode reverse recovery time     | $t_{rr}$      | —   | 47   | —         | ns               | $I_F = 40 \text{ A}$ , $V_{GS} = 0 \text{ V}$<br>$di_F/dt = 100 \text{ A}/\mu\text{s}$                                       |

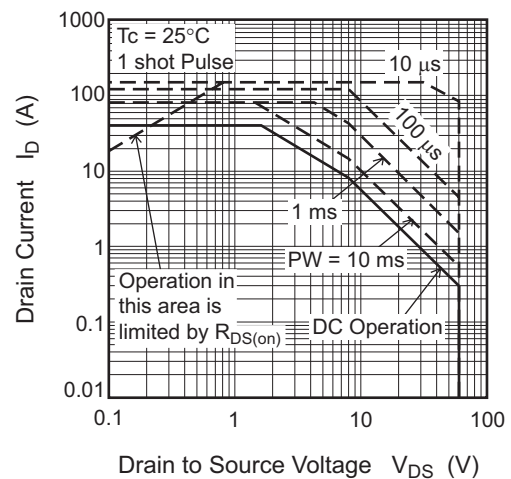
Notes: 4. Pulse test

## Main Characteristics

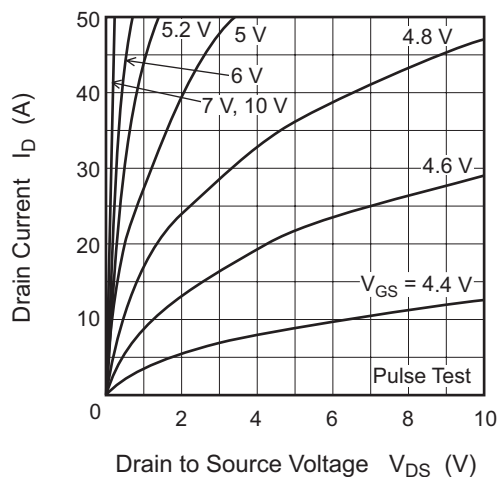
Power vs. Temperature Derating



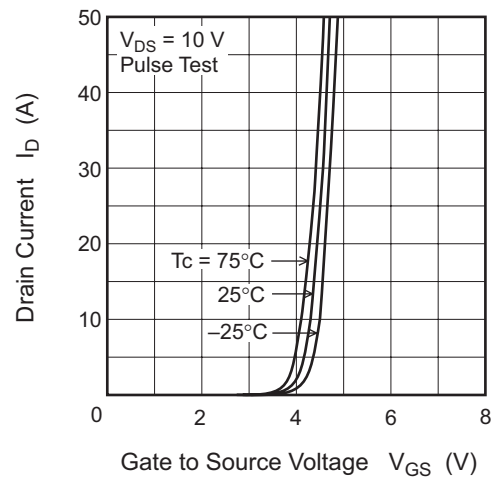
Maximum Safe Operation Area



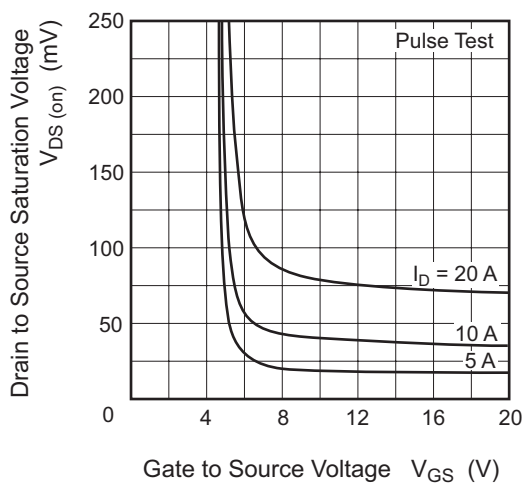
Typical Output Characteristics



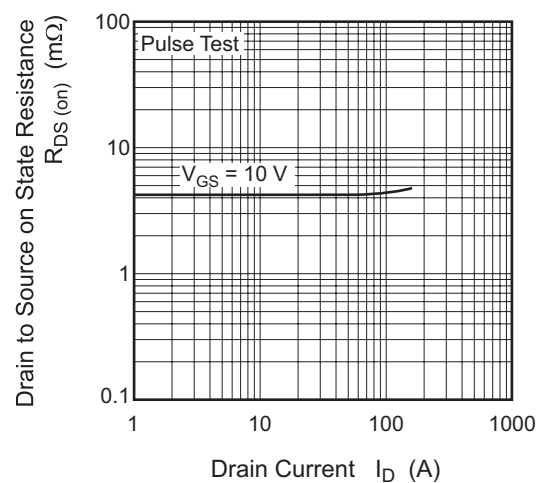
Typical Transfer Characteristics



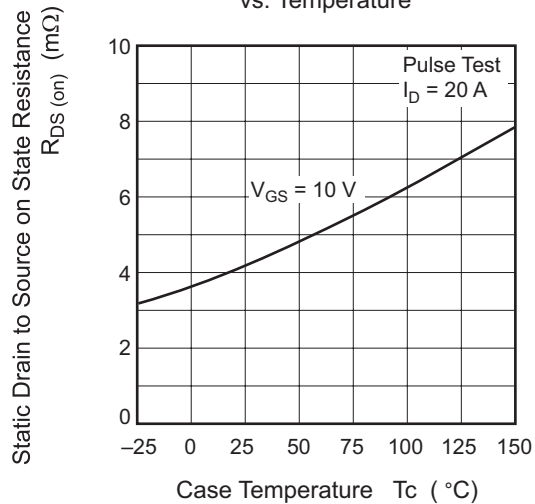
Drain to Source Saturation Voltage vs. Gate to Source Voltage



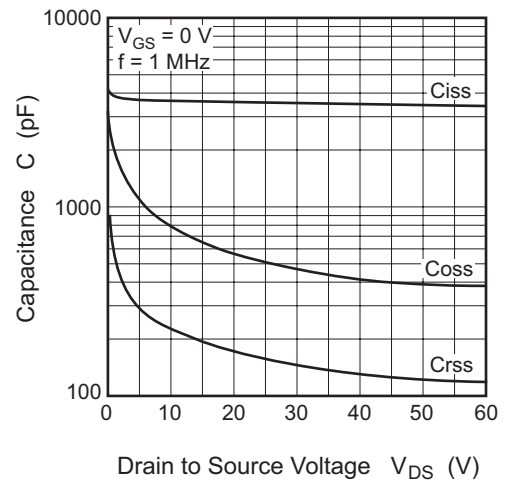
Static Drain to Source on State Resistance vs. Drain Current



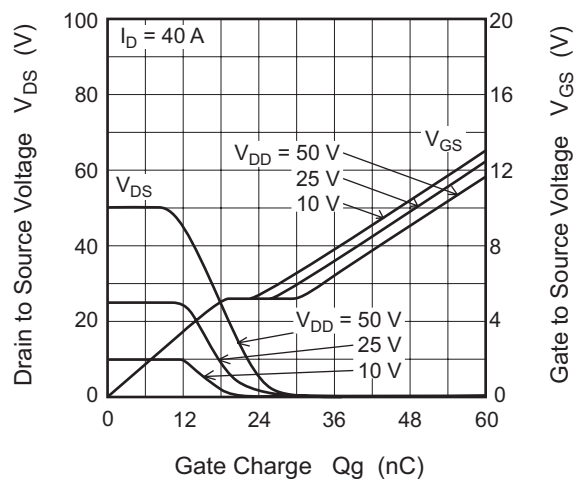
Static Drain to Source on State Resistance vs. Temperature



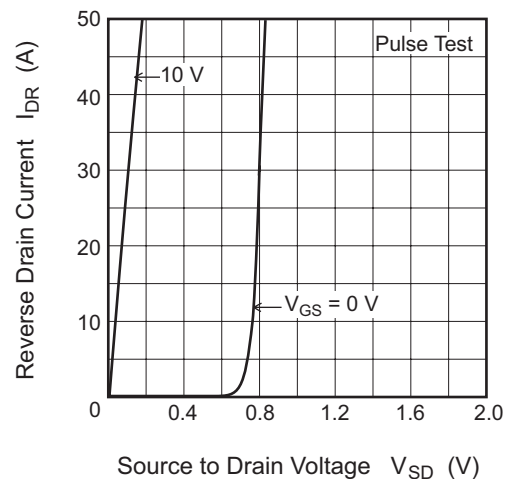
Typical Capacitance vs. Drain to Source Voltage



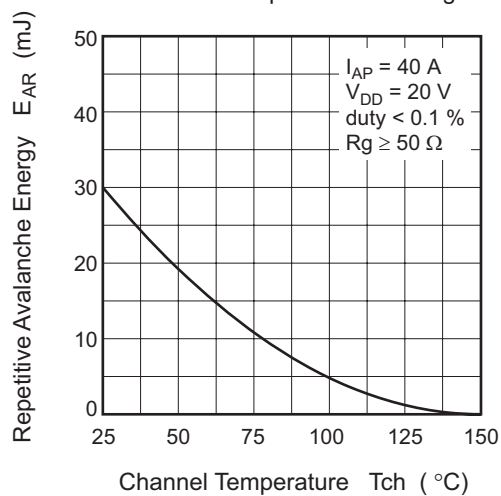
Dynamic Input Characteristics

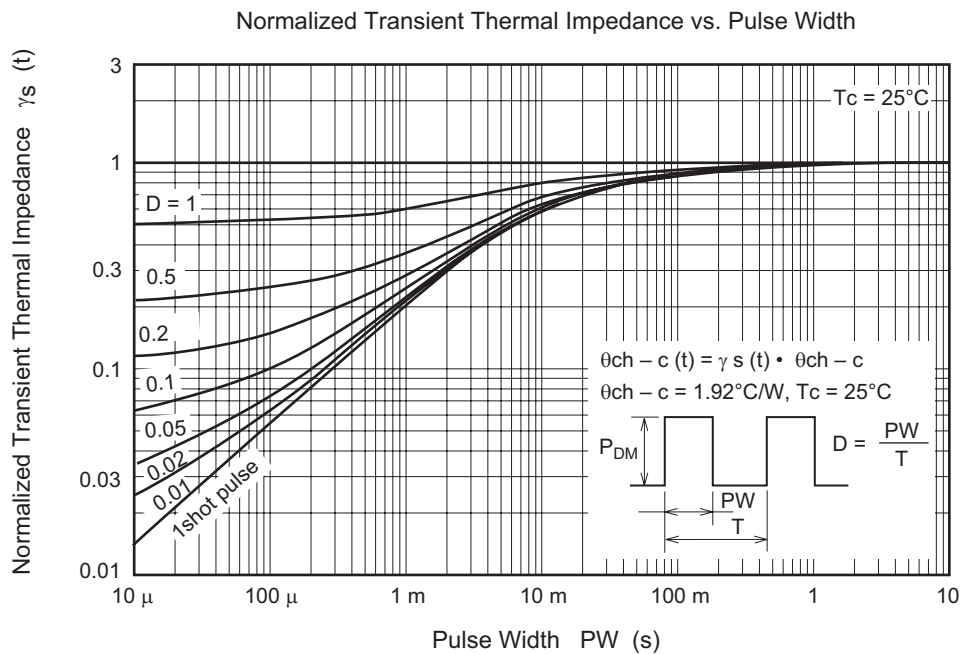


Reverse Drain Current vs. Source to Drain Voltage

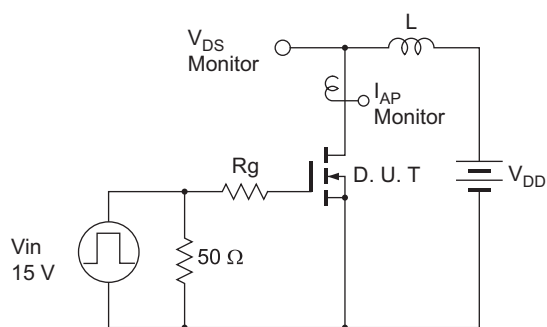


Maximum Avalanche Energy vs. Channel Temperature Derating

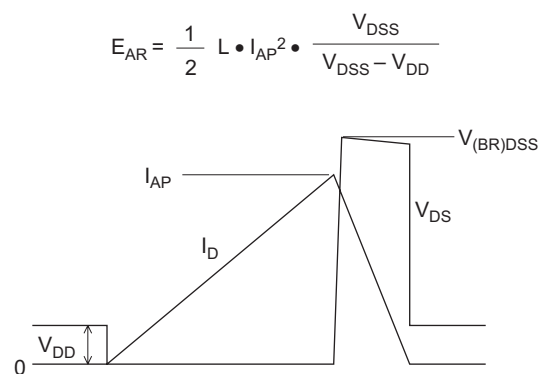




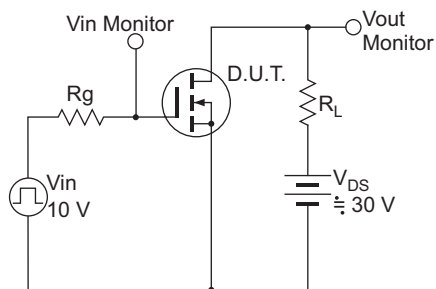
Avalanche Test Circuit



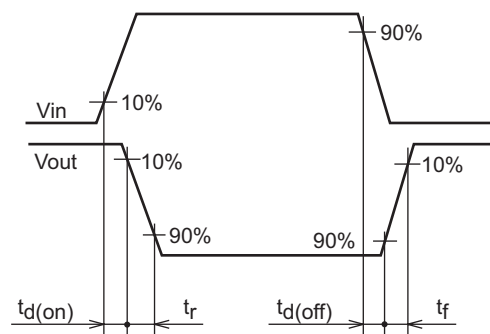
Avalanche Waveform



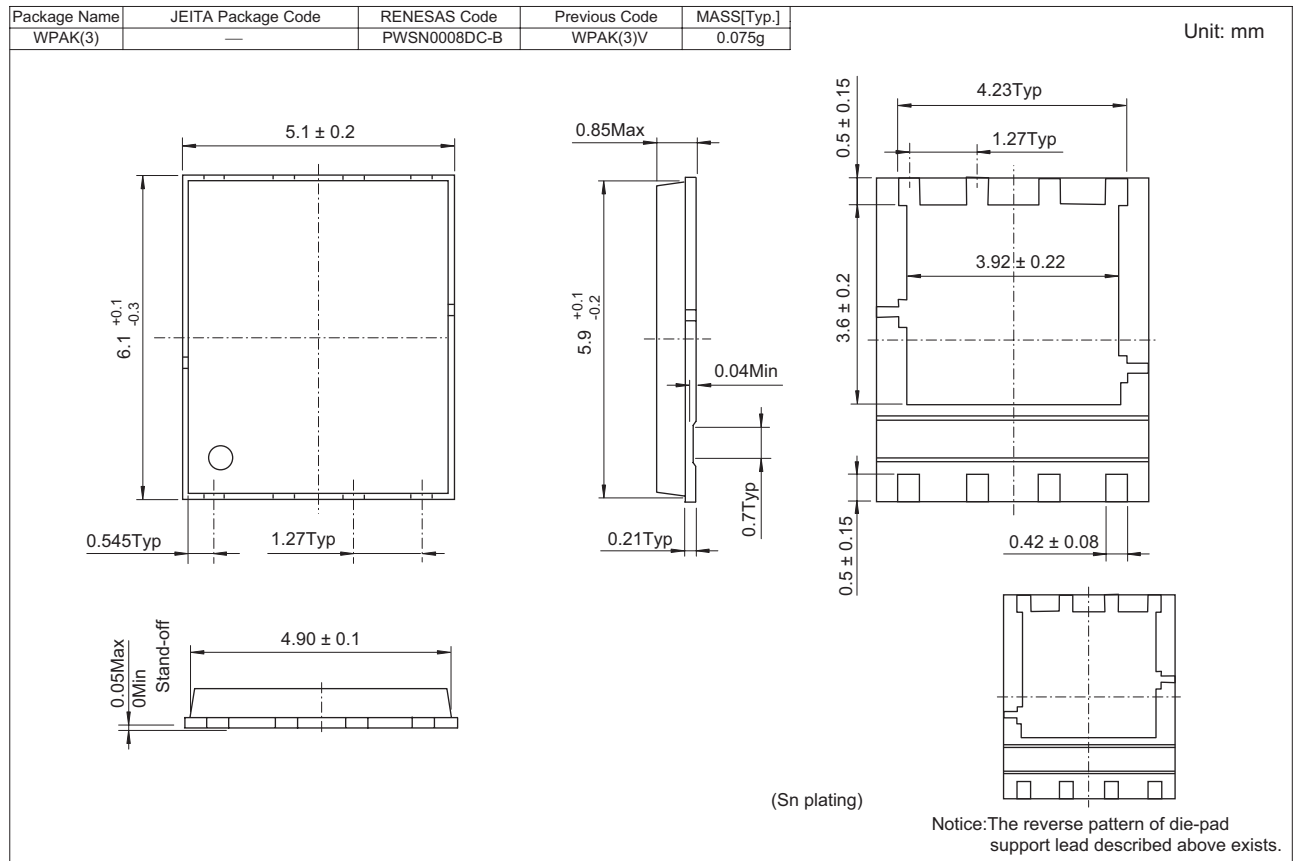
Switching Time Test Circuit



Switching Time Waveform



## Package Dimensions



## Ordering Information

| Orderable Part No. | Quantity | Shipping Container |
|--------------------|----------|--------------------|
| RJK0660DPA-00-J5A  | 3000 pcs | Taping             |

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