



N-Channel Silicon MOSFET

ATP206 — General-Purpose Switching Device Applications

Features

- Low ON-resistance
- 4.5V drive
- Halogen free compliance
- Large current
- Slim package
- Protection diode in

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		40	V
Gate-to-Source Voltage	V _{GSS}		±20	V
Drain Current (DC)	I _D		40	A
Drain Current (PW≤10μs)	I _{DP}	PW≤10μs, duty cycle≤1%	120	A
Allowable Power Dissipation	P _D	Tc=25°C	40	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	E _{AS}		26	mJ
Avalanche Current *2	I _{AV}		20	A

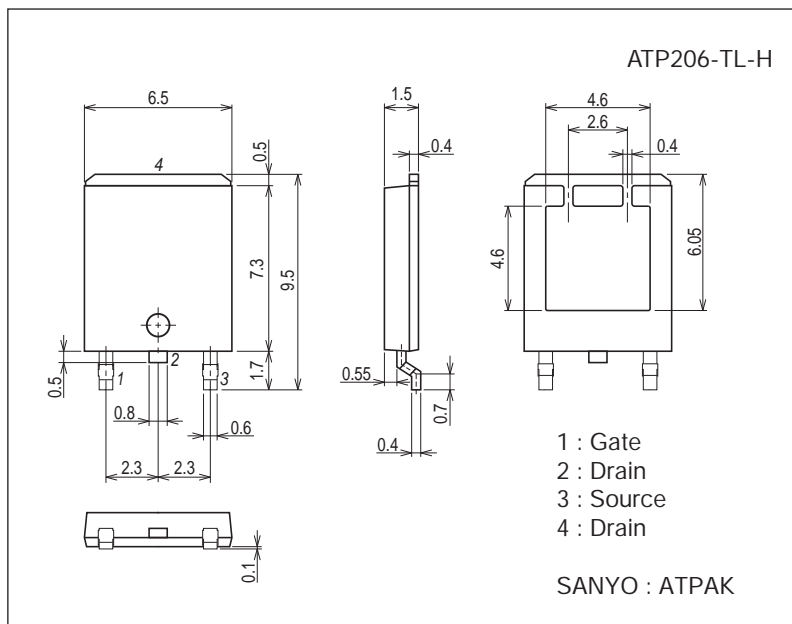
Note : *1 V_{DD}=10V, L=100μH, I_{AV}=20A

*2 L≤100μH, Single pulse

Package Dimensions

unit : mm (typ)

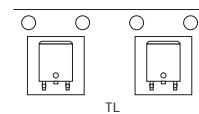
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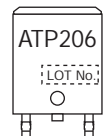
Product & Package Information

- Package : ATPAK
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

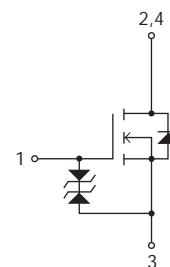
Packing Type: TL



Marking



Electrical Connection

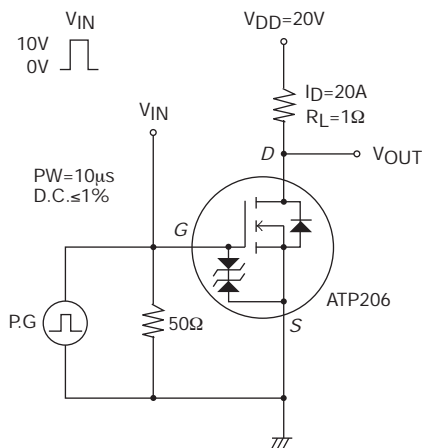


ATP206

Electrical Characteristics at $T_a=25^{\circ}\text{C}$

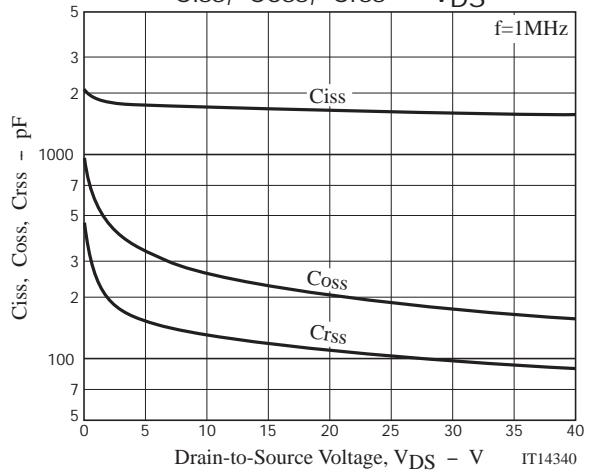
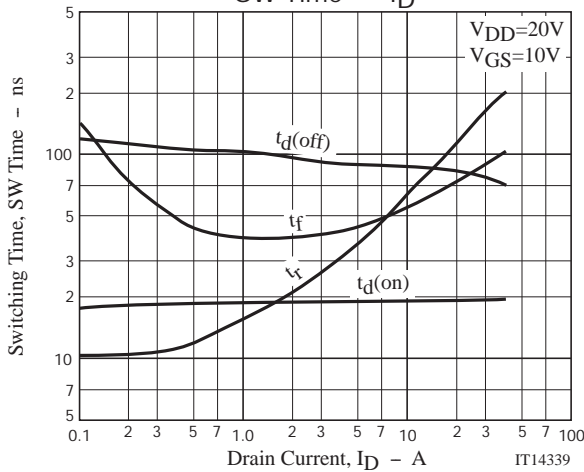
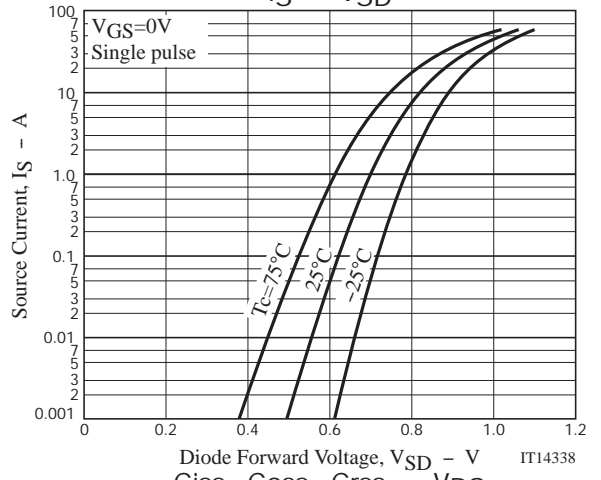
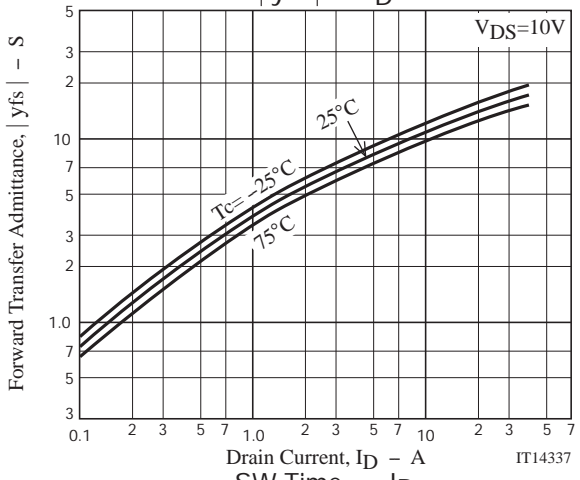
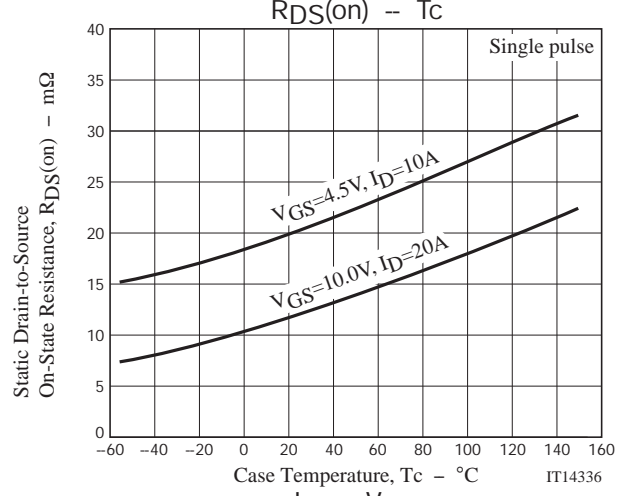
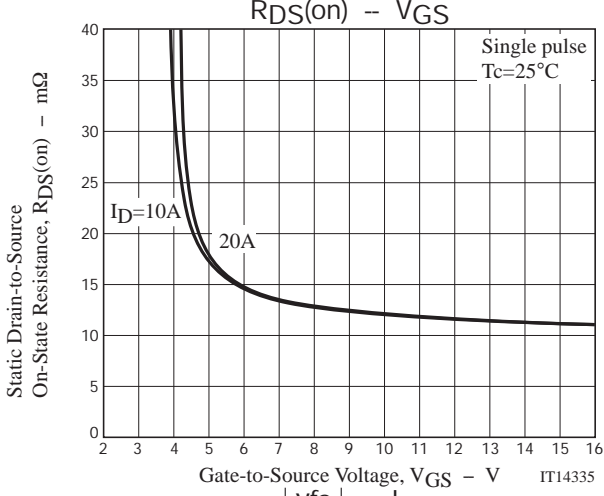
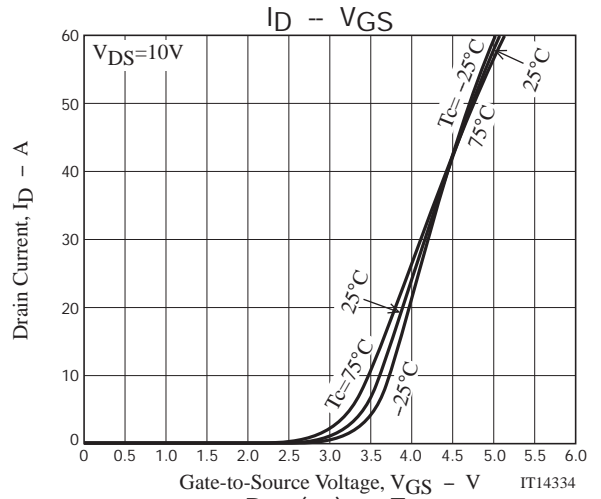
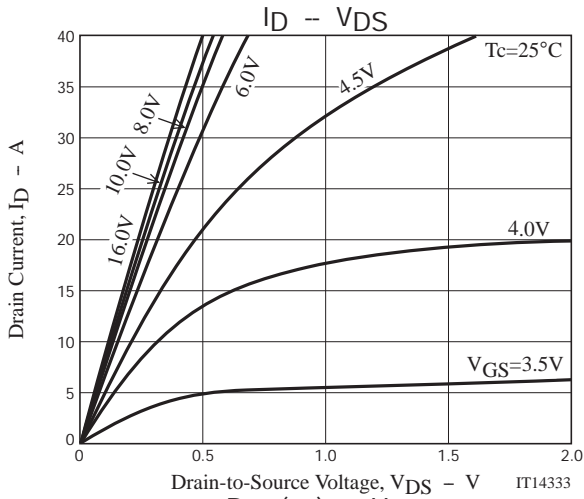
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}$, $V_{GS}=0\text{V}$	40			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=40\text{V}$, $V_{GS}=0\text{V}$			1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16\text{V}$, $V_{DS}=0\text{V}$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10\text{V}$, $I_D=1\text{mA}$	1.5		2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10\text{V}$, $I_D=20\text{A}$	9	15		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=20\text{A}$, $V_{GS}=10\text{V}$		12	16	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D=10\text{A}$, $V_{GS}=4.5\text{V}$		20	28	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS}=20\text{V}$, $f=1\text{MHz}$		1630		pF
Output Capacitance	C_{oss}			205		pF
Reverse Transfer Capacitance	C_{rss}			110		pF
Turn-ON Delay Time	$t_{d(on)}$		See specified Test Circuit.		19	
Rise Time	t_r			110		ns
Turn-OFF Delay Time	$t_{d(off)}$			83		ns
Fall Time	t_f			73		ns
Total Gate Charge	Q_g	$V_{DS}=20\text{V}$, $V_{GS}=10\text{V}$, $I_D=40\text{A}$			27	
Gate-to-Source Charge	Q_{gs}			7.0		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			5.2		nC
Diode Forward Voltage	V_{SD}	$I_S=40\text{A}$, $V_{GS}=0\text{V}$		0.99	1.2	V

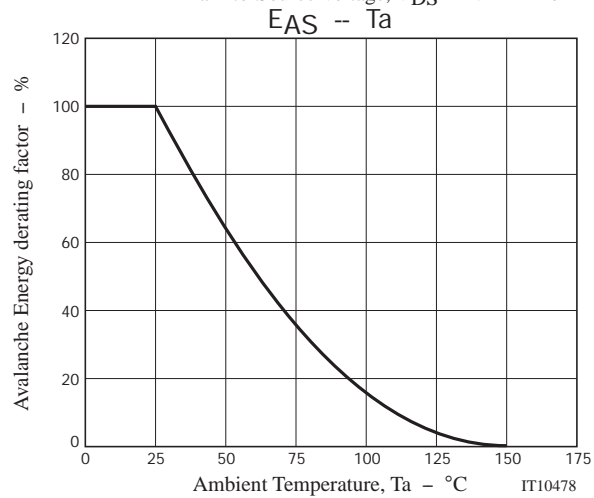
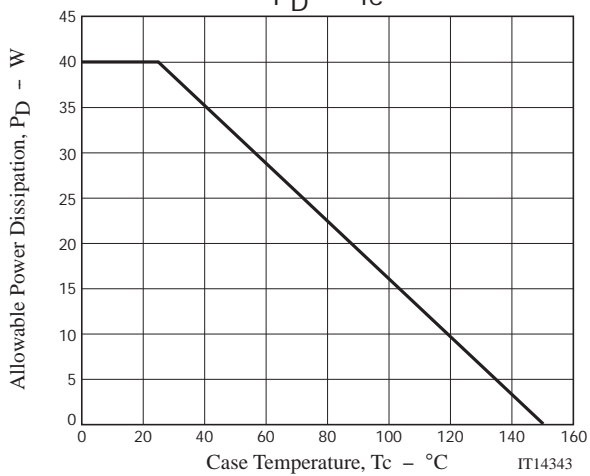
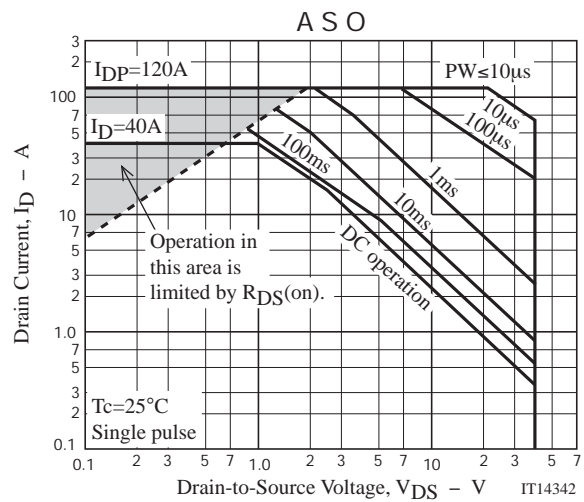
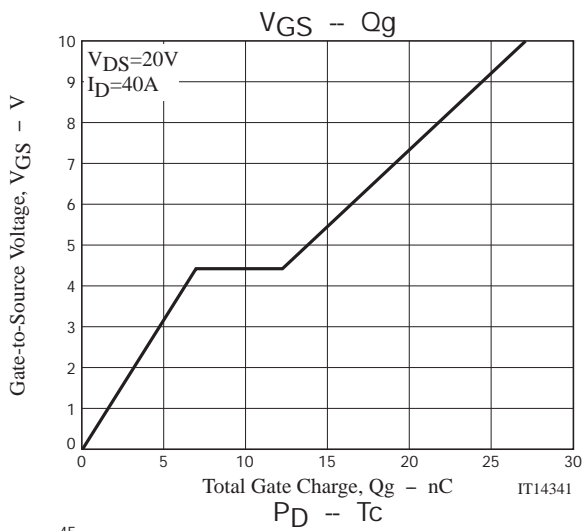
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
ATP206-TL-H	ATPAK	3,000pcs./reel	Pb Free and Halogen Free





Taping Specification

ATP206-TL-H

1. Packing Format (TL)

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	INNER BOX SD-C-18	OUTER BOX SD-A-18
ATPAK	ATP	3,000	3,000	15,000	1 reels contained Dimensions:mm (external) 340×340×28	5 inner boxes contained Dimensions:mm (external) 355×355×165

Packing method



Reel label

Reel label, Inner box label
(unit:mm)



Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

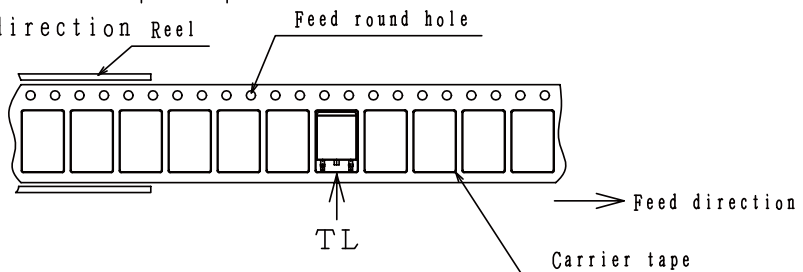
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



2-2. Device placement direction Reel



The one electrode terminals on feed hole side...TL

ATP206

Outline Drawing

ATP206-TL-H



Land Pattern Example



Note on usage : Since the ATP206 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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