

Single P-channel MOSFET

ELM34401AA-N

■ General description

ELM34401AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds} = -30V$
- $I_d = -8A$
- $R_{ds(on)} < 35m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} < 60m\Omega$ ($V_{gs} = -4.5V$)

■ Maximum absolute ratings

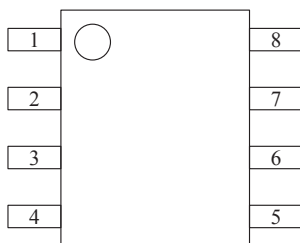
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	-30	V	
Gate-source voltage	V_{gs}	± 20	V	
Continuous drain current	I_d	Ta=25°C	-8	A
		Ta=70°C	-7	
Pulsed drain current	I_{dm}	-30	A	3
Power dissipation	P_d	Ta=25°C	2.5	W
		Ta=70°C	1.3	
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

■ Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-case	Steady-state	$R\theta_{jc}$		25	°C/W	
Maximum junction-to-ambient	Steady-state	$R\theta_{ja}$		50	°C/W	

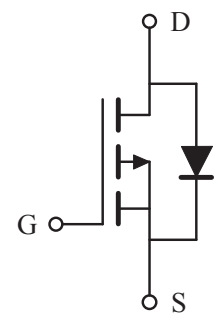
■ Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■ Circuit



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■Electrical characteristics

Ta=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
STATIC PARAMETERS								
Drain-source breakdown voltage	BV _{dss}	I _d =-250μA, V _{gs} =0V	-30			V		
Zero gate voltage drain current	I _{dss}	V _{ds} =-24V, V _{gs} =0V			-1	μA		
		V _{ds} =-20V, V _{gs} =0V, T _j =125°C			-10			
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V			±100	nA		
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =-250μA	-0.8	-1.5	-2.5	V		
On state drain current	I _{d(on)}	V _{gs} =-10V, V _{ds} =-5V	-30			A	1	
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =-10V, I _d =-8A		28	35	mΩ	1	
		V _{gs} =-4.5V, I _d =-6A		44	60	mΩ		
Forward transconductance	G _{fs}	V _{ds} =-10V, I _d =-6A		7		S	1	
Diode forward voltage	V _{sd}	I _s =-1A, V _{gs} =0V			-1	V	1	
Max. body-diode continuous current	I _s				-3	A		
Pulsed body-diode current	I _{sm}				-6	A	3	
DYNAMIC PARAMETERS								
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =-10V, f=1MHz		970		pF		
Output capacitance	C _{oss}				370		pF	
Reverse transfer capacitance	C _{rss}				180		pF	
SWITCHING PARAMETERS								
Total gate charge	Q _g	V _{gs} =-10V, V _{ds} =-15V I _d =-8A		28		nC	2	
Gate-source charge	Q _{gs}				6		nC	2
Gate-drain charge	Q _{gd}				12		nC	2
Turn-on delay time	t _{d(on)}	V _{gs} =-10V, V _{ds} =-15V, I _d ≈-1A, R _l =1Ω, R _{gen} =6Ω		20		ns	2	
Turn-on rise time	t _r				17		ns	2
Turn-off delay time	t _{d(off)}				180		ns	2
Turn-off fall time	t _f				75		ns	2
Body diode reverse recovery charge	Q _{rr}				7.9		nC	

NOTE :

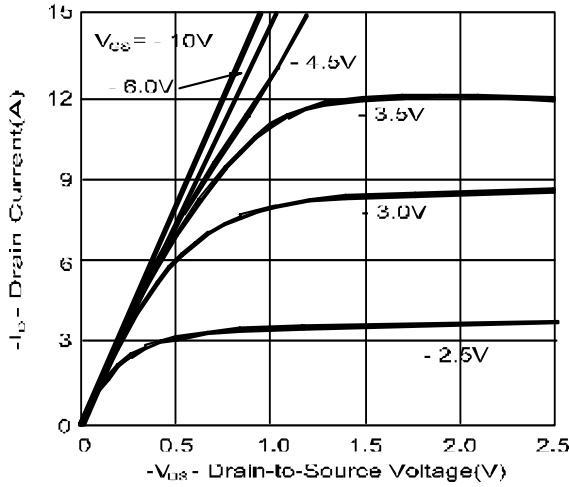
1. Pulsed width ≤ 300μsec and Duty cycle ≤ 2%.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle ≤ 1%.

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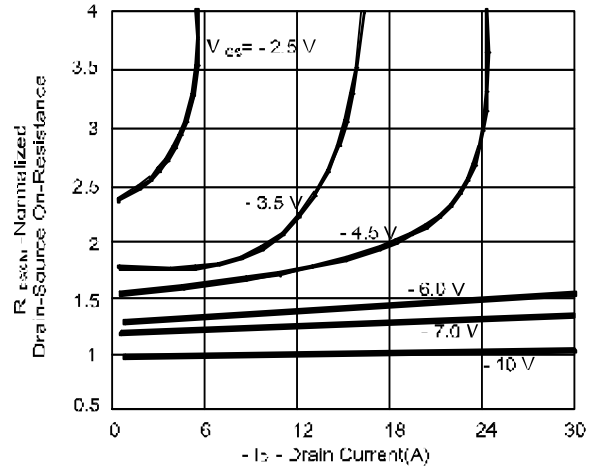
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Typical electrical and thermal characteristics

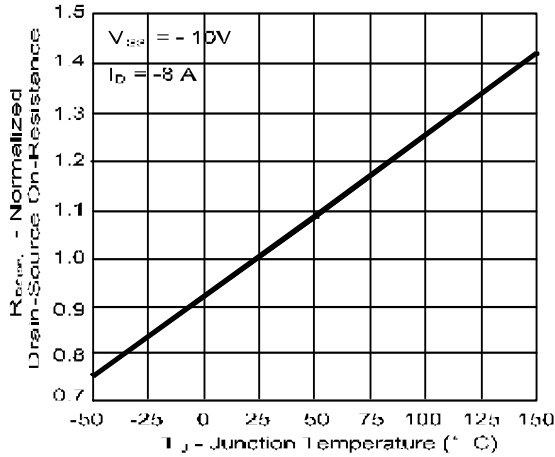
On-Region Characteristics



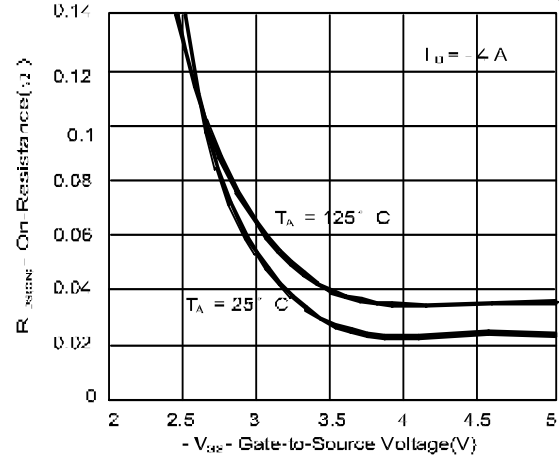
On-Resistance Variation with Drain Current and Gate Voltage



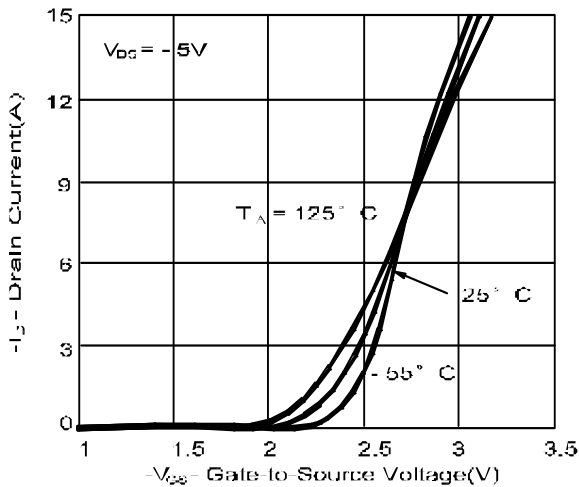
On-Resistance Variation with Temperature



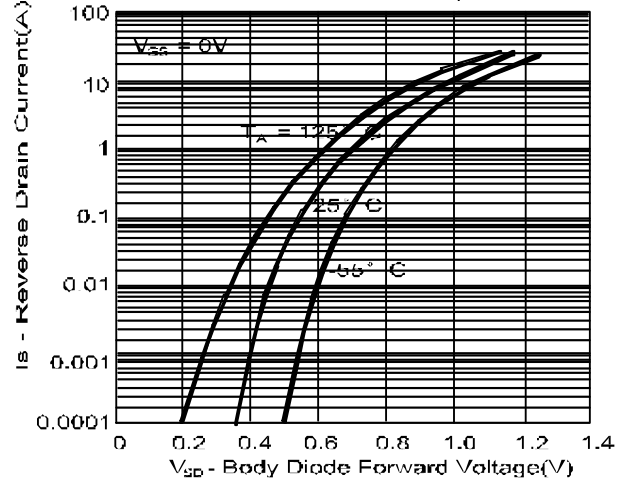
On-Resistance Variation with Gate-to-Source Voltage



Transfer Characteristics



Body Diode Forward Voltage Variation with Source Current and Temperature



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