Amplifier Transistor

NPN Silicon

MPS6507

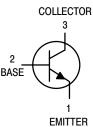
MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|---|-----------------------------------|-------------|----------------|
| Collector–Emitter Voltage | V _{CEO} | 20 | Vdc |
| Collector-Base Voltage | V _{CBO} | 30 | Vdc |
| Emitter-Base Voltage | V _{EBO} | 3.0 | Vdc |
| Collector Current — Continuous | Ic | 50 | mAdc |
| Total Device Dissipation @ T _A = 25°C Derate above 25°C | P _D | 625 5.0 | mW mW/°C |
| Total Device Dissipation @ T _C = 25°C Derate above 25°C | P _D | 1.5 12 | Watts mW/°C |
| Operating and Storage Junction Temperature Range | T _J , T _{stg} | -55 to +150 | °C |



THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|---|---------------------------------|------|------|
| Thermal Resistance, Junction to Ambient | R _{0JA} ⁽¹⁾ | 200 | °C/W |
| Thermal Resistance, Junction to Case | $R_{	heta JC}$ | 83.3 | °C/W |



ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

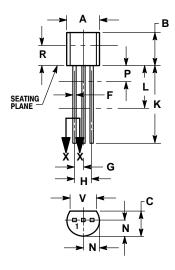
| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|-----------------------|-----|------|-----------|--------------|
| OFF CHARACTERISTICS | | | • | • | • |
| Collector–Emitter Breakdown Voltage ⁽²⁾ (I _C = 1.0 mAdc, I _B = 0) | V _(BR) CEO | 20 | _ | _ | Vdc |
| Collector–Base Breakdown Voltage $(I_C = 100 \mu Adc, I_E = 0)$ | V _(BR) CBO | 30 | _ | _ | Vdc |
| Emitter–Base Breakdown Voltage ($I_E = 100 \mu Adc$, $I_C = 0$) | V _{(BR)EBO} | 3.0 | _ | _ | Vdc |
| Collector Cutoff Current $(V_{CB} = 15 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 15 \text{ Vdc}, I_E = 0, T_A = 60^{\circ}\text{C})$ | І _{СВО} | _ | _ | 50 1.0 | nAdc μAdc |
| ON CHARACTERISTICS | | | | | |
| DC Current $Gain^{(2)}$ ($I_C = 2.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$) | h _{FE} | 25 | 75 | _ | _ |
| SMALL-SIGNAL CHARACTERISTICS | | | • | • | • |
| Current–Gain — Bandwidth Product (I _C = 10 mAdc, V _{CE} = 10 Vdc, f = 100 MHz) | f _T | 700 | 800 | _ | MHz |
| Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz) | C _{obo} | _ | 1.25 | 2.5 | pF |
| Small–Signal Current Gain (I _C = 2.0 mAdc, V _{CE} = 10 Vdc, f = 20 MHz) | h _{fe} | 20 | _ | _ | _ |

- 1. $R_{\theta JA}$ is measured with the device soldered into a typical printed circuit board. 2. Pulse Test: Pulse Width \leq 300 μ s; Duty Cycle \leq 2.0%.

MPS6507

PACKAGE DIMENSIONS

CASE 029-04 (TO-226AA) ISSUE AD





- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSION D A MOJ APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

| | INCHES | | MILLIMETERS | | |
|-----|--------|-------|-------------|------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 0.175 | 0.205 | 4.45 | 5.20 | |
| В | 0.170 | 0.210 | 4.32 | 5.33 | |
| С | 0.125 | 0.165 | 3.18 | 4.19 | |
| D | 0.016 | 0.022 | 0.41 | 0.55 | |
| F | 0.016 | 0.019 | 0.41 | 0.48 | |
| G | 0.045 | 0.055 | 1.15 | 1.39 | |
| Н | 0.095 | 0.105 | 2.42 | 2.66 | |
| J | 0.015 | 0.020 | 0.39 | 0.50 | |
| K | 0.500 | | 12.70 | | |
| L | 0.250 | | 6.35 | | |
| N | 0.080 | 0.105 | 2.04 | 2.66 | |
| P | | 0.100 | | 2.54 | |
| R | 0.115 | | 2.93 | | |
| ٧ | 0.135 | | 3.43 | | |

STYLE 1:
PIN 1. EMITTER
2. BASE
3. COLLECTOR

MPS6507

Notes

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