

# MJF44H11 (NPN), MJF45H11 (PNP)

Preferred Devices

## Complementary Power Transistors

### For Isolated Package Applications

... for general purpose power amplification and switching such as output or driver stages in applications such as switching regulators, converters and power amplifiers.

- Low Collector–Emitter Saturation Voltage –  
 $V_{CE(sat)} = 1.0 \text{ V (Max) @ } 8.0 \text{ A}$
- Fast Switching Speeds
- Complementary Pairs Simplifies Designs

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	$V_{CEO}$	80	Vdc
Emitter–Base Voltage	$V_{EB}$	5	Vdc
Collector Current – Continuous – Peak	$I_C$	10 20	Adc
Total Power Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	50 1.67	Watts W/ $^\circ\text{C}$
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	2.0 0.016	Watts W/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	–55 to 150	$^\circ\text{C}$

#### THERMAL CHARACTERISTICS

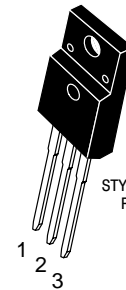
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	3.5	$^\circ\text{C/W}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$



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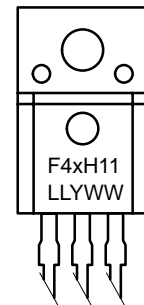
<http://onsemi.com>

**SILICON POWER  
TRANSISTORS  
10 AMPERES  
80 VOLTS  
50 WATTS**



STYLE 2:  
PIN 1. BASE  
2. COLLECTOR  
3. EMITTER

#### MARKING DIAGRAM



**ISOLATED TO–220  
CASE 221D  
PLASTIC**

F4xH11 = Specific Device Code  
x = 4 or 5  
LL = Location Code  
Y = Year  
WW = Work Week

#### ORDERING INFORMATION

Device	Package	Shipping
MJF44H11	TO–220	50 Units/Rail
MJF45H11	TO–220	50 Units/Rail

Preferred devices are recommended choices for future use and best overall value.

## MJF44H11 (NPN), MJF45H11 (PNP)

### ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Collector–Emitter Sustaining Voltage ( $I_C = 30\text{ mA}$ , $I_B = 0$ )	$V_{CEO(sus)}$	80	–	–	Vdc
Collector Cutoff Current ( $V_{CE} = \text{Rated } V_{CEO}$ , $V_{BE} = 0$ )	$I_{CES}$	–	–	1.0	$\mu\text{A}$
Emitter Cutoff Current ( $V_{EB} = 5\text{ Vdc}$ )	$I_{EBO}$	–	–	10	$\mu\text{A}$

### ON CHARACTERISTICS

Collector–Emitter Saturation Voltage ( $I_C = 8\text{ Adc}$ , $I_B = 0.4\text{ Adc}$ )	$V_{CE(sat)}$	–	–	1.0	Vdc
Base–Emitter Saturation Voltage ( $I_C = 8\text{ Adc}$ , $I_B = 0.8\text{ Adc}$ )	$V_{BE(sat)}$	–	–	1.5	Vdc
DC Current Gain ( $V_{CE} = 1\text{ Vdc}$ , $I_C = 2\text{ Adc}$ )	$h_{FE}$	60	–	–	–
DC Current Gain ( $V_{CE} = 1\text{ Vdc}$ , $I_C = 4\text{ Adc}$ )		40	–	–	–

### DYNAMIC CHARACTERISTICS

Collector Capacitance ( $V_{CB} = 10\text{ Vdc}$ , $f_{test} = 1\text{ MHz}$ )	MJF44H11 MJF45H11	$C_{cb}$	–	130	–	$\mu\text{F}$
			–	230	–	
Gain Bandwidth Product ( $I_C = 0.5\text{ Adc}$ , $V_{CE} = 10\text{ Vdc}$ , $f = 20\text{ MHz}$ )	MJF44H11 MJF45H11	$f_T$	–	50	–	MHz
			–	40	–	

### SWITCHING TIMES

Delay and Rise Times ( $I_C = 5\text{ Adc}$ , $I_{B1} = 0.5\text{ Adc}$ )	MJF44H11 MJF45H11	$t_d + t_r$	–	300	–	ns
			–	135	–	
Storage Time ( $I_C = 5\text{ Adc}$ , $I_{B1} = I_{B2} = 0.5\text{ Adc}$ )	MJF44H11 MJF45H11	$t_s$	–	500	–	ns
			–	500	–	
Fall Time ( $I_C = 5\text{ Adc}$ , $I_{B1} = I_{B2} = 0.5\text{ Adc}$ )	MJF44H11 MJF45H11	$t_f$	–	140	–	ns
			–	100	–	

# MJF44H11 (NPN), MJF45H11 (PNP)

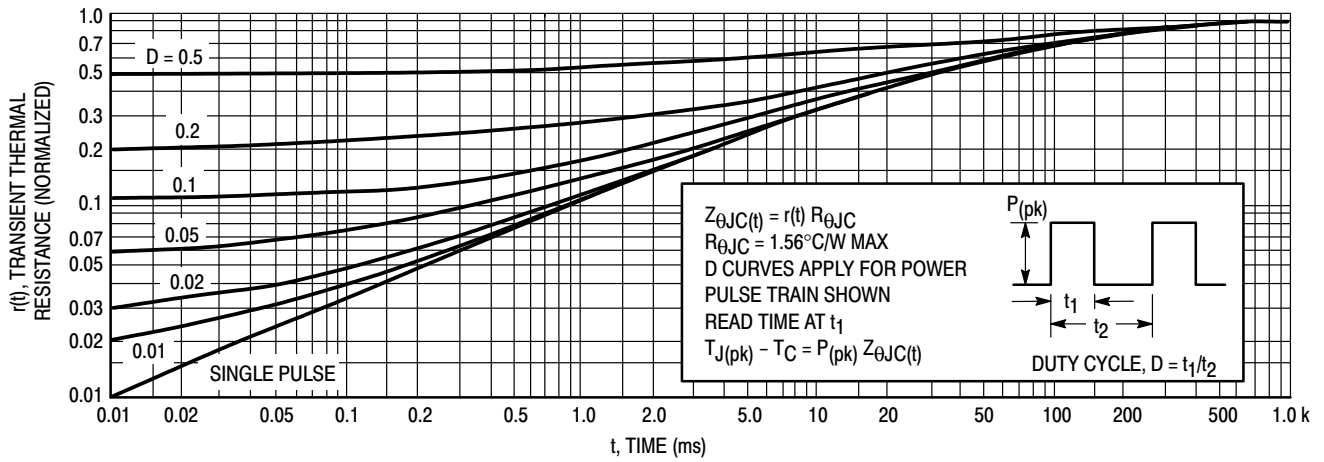


Figure 1. Thermal Response

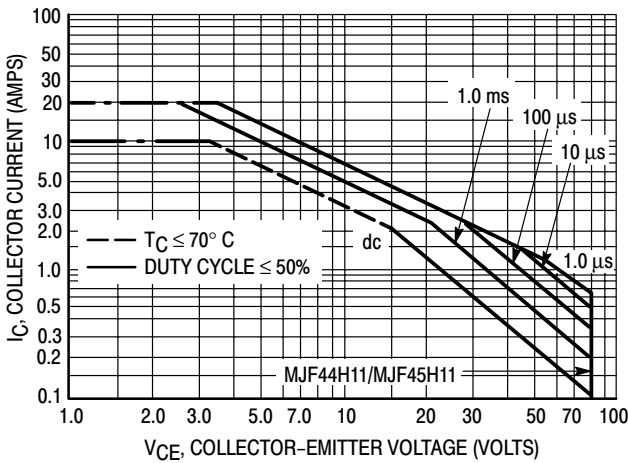


Figure 2. Maximum Rated Forward Bias Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 2 is based on  $T_{J(pk)} = 150^{\circ}\text{C}$ ;  $T_C$  is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} \leq 150^{\circ}\text{C}$ .  $T_{J(pk)}$  may be calculated from the data in Figure 1. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

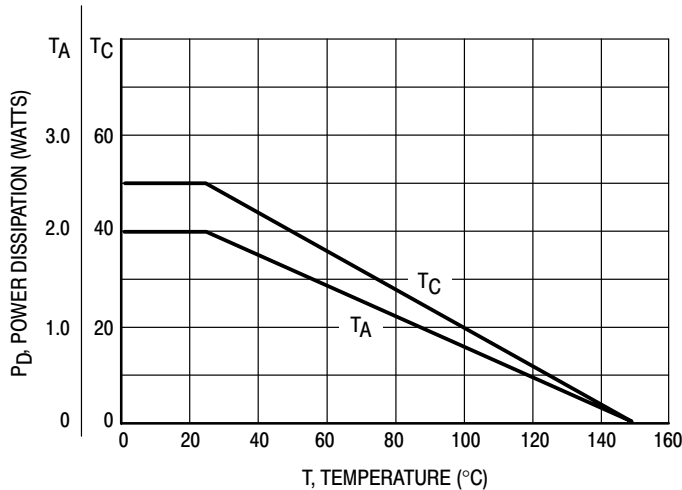


Figure 3. Power Derating

# MJF44H11 (NPN), MJF45H11 (PNP)

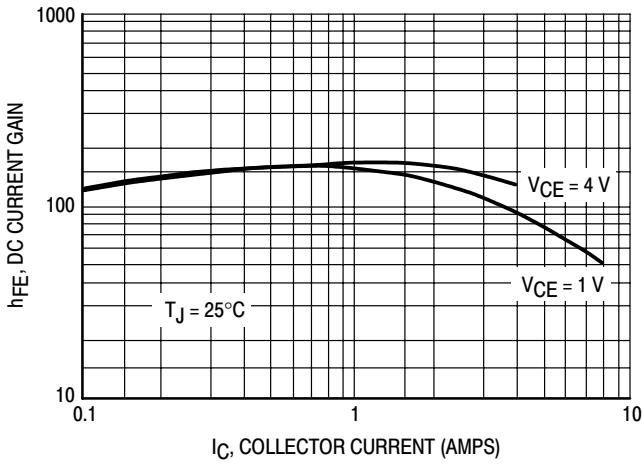


Figure 4. MJF44H11 DC Current Gain

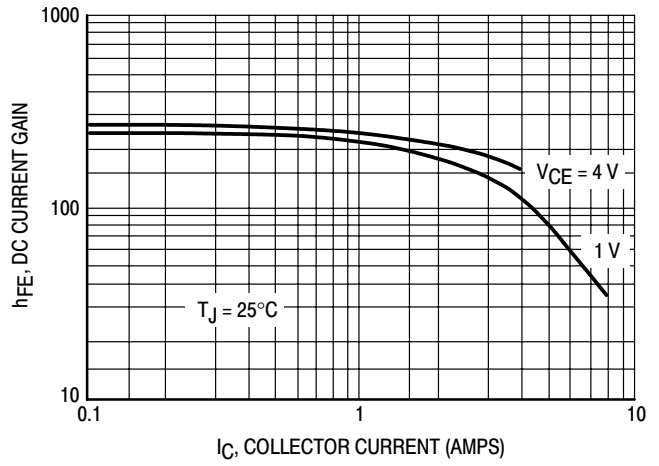


Figure 5. MJF45H11 DC Current Gain

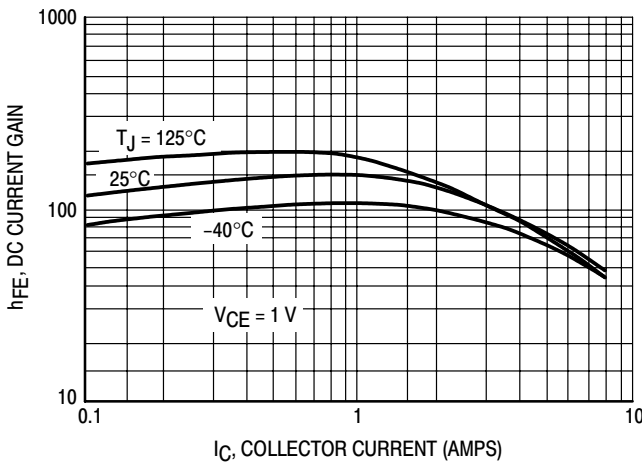


Figure 6. MJF44H11 Current Gain versus Temperature

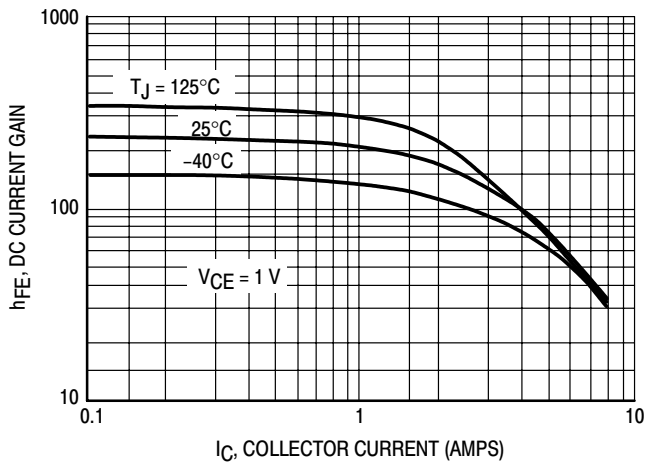


Figure 7. MJF45H11 Current Gain versus Temperature

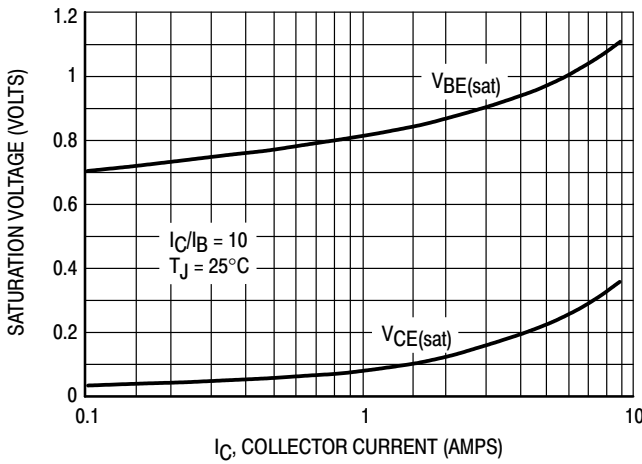


Figure 8. MJF44H11 On-Voltages

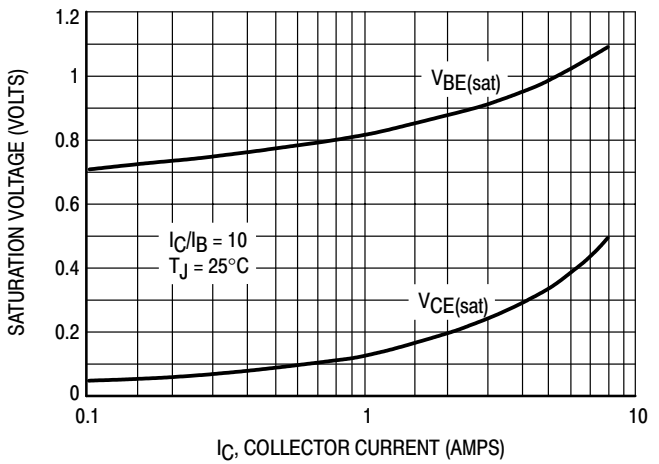
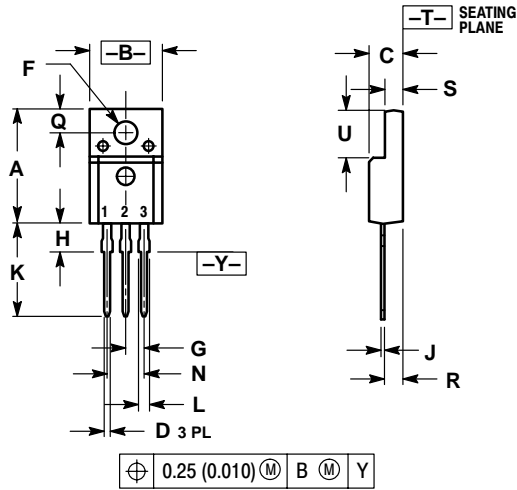


Figure 9. MJF45H11 On-Voltages

# MJF44H11 (NPN), MJF45H11 (PNP)

## PACKAGE DIMENSIONS

### TO-220 FULLPAK TRANSISTOR CASE 221D-02 ISSUE D



- NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.621	0.629	15.78	15.97
B	0.394	0.402	10.01	10.21
C	0.181	0.189	4.60	4.80
D	0.026	0.034	0.67	0.86
F	0.121	0.129	3.08	3.27
G	0.100 BSC		2.54 BSC	
H	0.123	0.129	3.13	3.27
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.14	1.52
N	0.200 BSC		5.08 BSC	
Q	0.126	0.134	3.21	3.40
R	0.107	0.111	2.72	2.81
S	0.096	0.104	2.44	2.64
U	0.259	0.267	6.58	6.78

- STYLE 2:  
 PIN 1. BASE  
 2. COLLECTOR  
 3. EMITTER

**Notes**

**Notes**

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