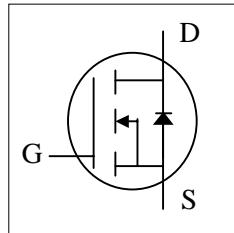
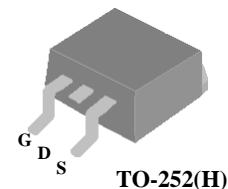




- ▼ Low On-resistance
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free



$BV_{DSS}$	30V
$R_{DS(ON)}$	9.5mΩ
$I_D$	50A



## Description

AP73T03A series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-252 package is widely preferred for all commercial-industrial surface mount applications using infrared reflow technique and suited for high current application due to the low connection resistance.

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	+20	V
$I_D @ T_C=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	50	A
$I_D @ T_C=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	31.7	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	120	A
$P_D @ T_C=25^\circ\text{C}$	Total Power Dissipation	41.6	W
$P_D @ T_A=25^\circ\text{C}$	Total Power Dissipation <sup>3</sup>	2	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Value	Units
$R_{thj-c}$	Maximum Thermal Resistance, Junction-case	3	°C/W
$R_{thj-a}$	Maximum Thermal Resistance, Junction-ambient (PCB mount) <sup>3</sup>	62.5	°C/W



# AP73T03AGH-HF

## Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=30\text{A}$	-	-	9.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=20\text{A}$	-	-	16	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	-	3	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=30\text{A}$	-	40	-	S
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$	-	-	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
$Q_{\text{g}}$	Total Gate Charge	$I_{\text{D}}=20\text{A}$	-	7	11.2	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=24\text{V}$	-	1.4	-	nC
$Q_{\text{gd}}$	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=4.5\text{V}$	-	3.5	-	nC
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DS}}=15\text{V}$	-	7	-	ns
$t_{\text{r}}$	Rise Time	$I_{\text{D}}=20\text{A}$	-	55	-	ns
$t_{\text{d(off)}}$	Turn-off Delay Time	$R_{\text{G}}=3.3\Omega$	-	17	-	ns
$t_{\text{f}}$	Fall Time	$V_{\text{GS}}=10\text{V}$	-	3	-	ns
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	630	1000	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	180	-	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	60	-	pF
$R_{\text{g}}$	Gate Resistance	$f=1.0\text{MHz}$	-	3.2	6.4	$\Omega$

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{\text{SD}}$	Forward On Voltage <sup>2</sup>	$I_{\text{S}}=30\text{A}, V_{\text{GS}}=0\text{V}$	-	-	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_{\text{S}}=10\text{A}, V_{\text{GS}}=0\text{V},$ $dI/dt=100\text{A}/\mu\text{s}$	-	16	-	ns
$Q_{\text{rr}}$	Reverse Recovery Charge		-	3	-	nC

## Notes:

- 1.Pulse width limited by max. junction temperature
- 2.Pulse test
- 3.Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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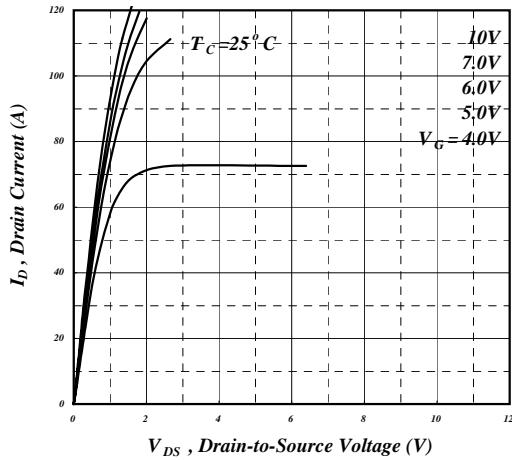


Fig 1. Typical Output Characteristics

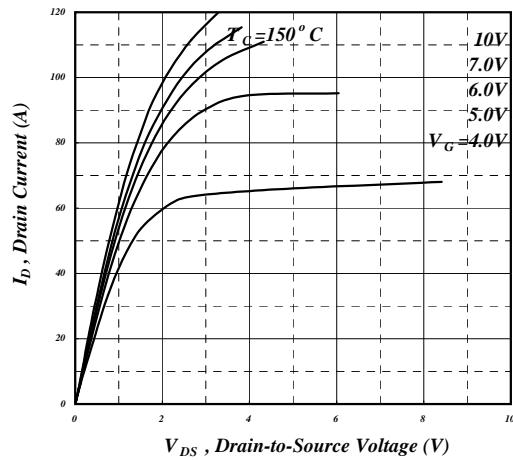


Fig 2. Typical Output Characteristics

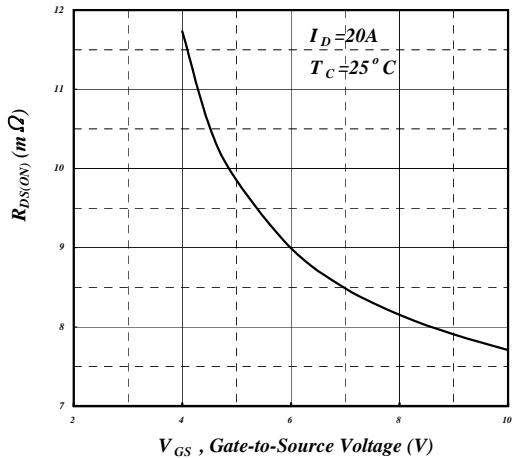


Fig 3. On-Resistance v.s. Gate Voltage

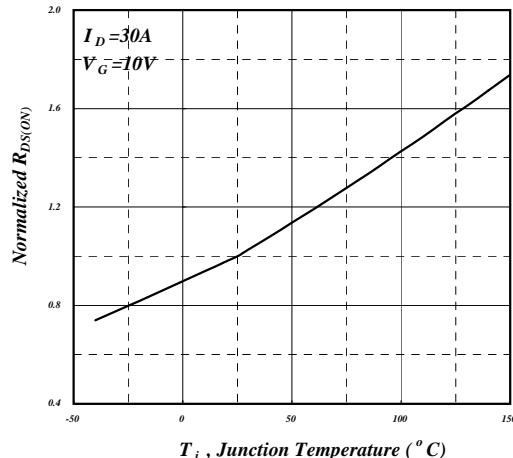


Fig 4. Normalized On-Resistance v.s. Junction Temperature

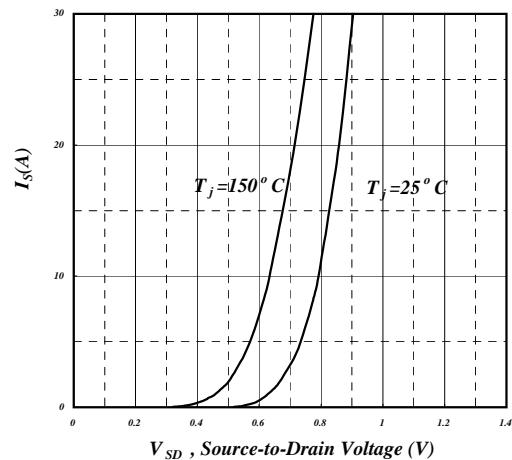


Fig 5. Forward Characteristic of Reverse Diode

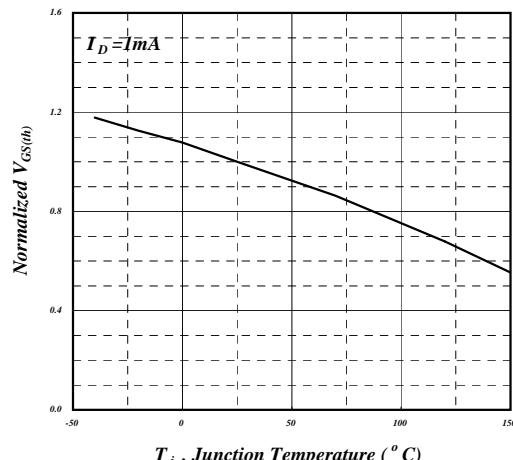


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

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