



TSM1015

Low Consumption Voltage and Current Controller for Battery Chargers and Adaptors

PRELIMINARY DATA

- Constant voltage and constant current control
- Low consumption
- Low voltage operation
- Low external component count
- Current sink output stage
- Easy compensation
- High ac mains voltage rejection

Voltage Reference:

- Fixed output voltage reference 1.25V
- 0.5% and 1% Voltage precision

Description

TSM1015 is a highly integrated solution for SMPS applications requiring CV (constant voltage) and CC (constant current) mode.

TSM1015 integrates one voltage reference and two operational amplifiers.

The voltage reference combined with one operational amplifier makes it an ideal voltage controller. The other operational, combined with few external resistors and the voltage reference, can be used as a current limiter.

TSM1015 is pin to pin compatible with TSM103 and represents an enhanced version low consumption.

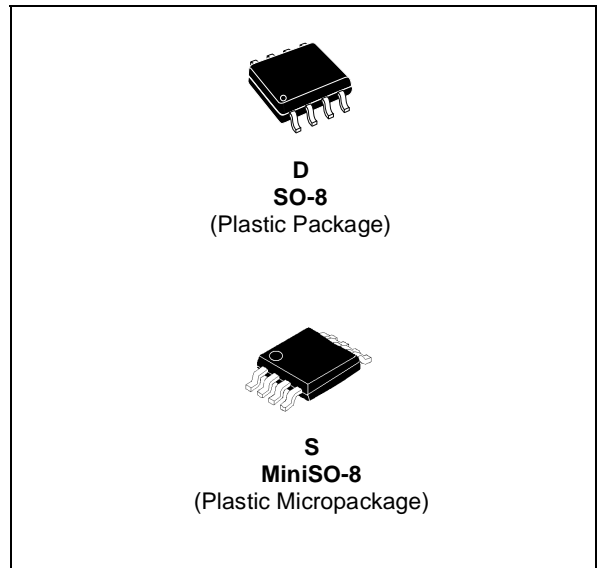
Applications

- Adapters

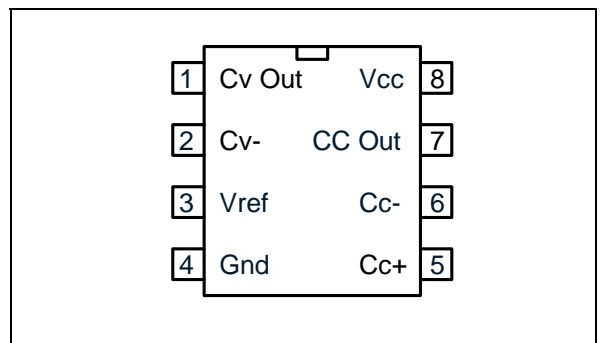
- Battery chargers

Order Codes

Part Number	Temperature Range	Package	Packaging	VRef (%)	Marking
TSM1015ID	-40 to 105°C	SO-8	Tube	1	M1015
TSM1015IDT			Tape & Reel	1	M1015
TSM1015AID			Tube	0.5	M1015A
TSM1015AIDT			Tape & Reel	0.5	M1015A
TSM1015IST		mini SO-8	Tape & Reel	1	M810
TSM1015AIST			Tape & Reel	0.5	M811



Pin Connections (top view)



1 Absolute Maximum Ratings

Table 1: Key parameters and their absolute maximum ratings

Symbol	DC Supply Voltage	Value	Unit
V _{cc}	DC Supply Voltage (50mA =< I _{cc})	-0.3V to V _z	V
V _i	Input Voltage	-0.3 to V _{cc}	V
PT	Power dissipation		W
T _{stg}	Storage temperature	-55 to 150	°C
T _j	Junction temperature	150	°C
I _{ref}	Voltage reference output current	2.5	mA
ESD	Electrostatic Discharge	2	kV
R _{thja}	Thermal Resistance Junction to Ambient Mini SO8 package	180	°C/W
R _{thja}	Thermal Resistance Junction to Ambient SO8 package	175	°C/W

Table 2: Operating conditions

Symbol	Parameter	Value	Unit
V _{cc}	DC Supply Conditions	4.5 to V _z	V
T _{oper}	Operational temperature	-40 to 105	°C

Table 3: Pin description - SO-8 and Mini SO-8 pin out

Name	Pin #	Type	Function
CVOUT	1	Analog Output	Output of the operational amplifier
CV-	2	Analog Input	Input pin of the operational amplifier
V _{ref}	3	Analog Output	Voltage Reference
Gnd	4	Power Supply	Ground Line. 0V Reference For All Voltages
CC+	5	Analog Input	Input pin of the operational amplifier
CC-	6	Analog Input	Input pin of the operational amplifier
CCOUT	7	Analog Output	Output of the operational amplifier
V _{cc}	8	Power Supply	Power supply line.

2 Electrical Characteristics

Table 4: Electrical characteristics Tamb = 25°C and Vcc = +18V (unless otherwise specified)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
Total Current Consumption						
I _{cc}	Total Supply Current, excluding current in Voltage Reference ¹ .	V _{cc} = 18V, no load T _{min.} < T _{amb} < T _{max.}		100	180	μA
V _z	V _{cc} clamp voltage	I _{cc} = 50mA		28		V
Operator 1: Op-amp with non-inverting input connected to the internal Vref						
V _{ref} +V _{io}	Input Offset Voltage + Voltage reference TSM1015	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		1.251	1.266	V
	TSM1015A	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		1.25	1.258 1.267	
DV _{io}	Input Offset Voltage Drift			7		μV/°C
Operator 2						
V _{io}	Input Offset Voltage TSM1015	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		1	4	mV
	TSM1015A	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		0.5	5 2 3	
DV _{io}	Input Offset Voltage Drift			7		μV/°C
I _{ib}	Input Bias Current	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}		20 50	150 200	nA
SVR	Supply Voltage Rejection Ration	V _{CC} = 4.5V to 28V	65	100		dB
V _{icm}	Input Common Mode Voltage Range		0		V _{cc} -1.5	V
CMR	Common Mode Rejection Ratio	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	70 60	85		dB
Output stage						
G _m	Transconduction Gain. Sink Current Only ²	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	0.5	1 1		mA/mV
V _{ol}	Low output voltage at 5 mA sinking current	T _{min.} ≤ T _{amb} ≤ T _{max.}		250	400	mV
I _{os}	Output Short Circuit Current. Output to (V _{cc} -0.6V). Sink Current Only	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	6 5	10		mA
Voltage reference						
V _{ref}	Reference Input Voltage TSM1015 1% precision	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	1.238 1.225	1.25	1.262 1.273	V
	TSM1015A 0.5% precision	T _{amb} = 25°C T _{min.} ≤ T _{amb} ≤ T _{max.}	1.244 1.237	1.25	1.256 1.261	
ΔV _{ref}	Reference Input Voltage Deviation Over Temperature Range	T _{min.} ≤ T _{amb} ≤ T _{max.}		20	30	mV
RegLine	Reference input voltage deviation over V _{cc} range.	I _{load} = 1mA			20	mV
RegLoad	Reference input voltage deviation over output current.	V _{cc} = 18V, 0 < I _{load} < 2.5mA			10	mV

1) Test conditions: pin 6 and 4 connected to GND, pin 2 and 1 connected to 1.25V, pin 5 connected to 200mV.

2) The current depends on the difference voltage between the negative and the positive inputs of the amplifier. If the voltage on the minus input is 1mV higher than the positive amplifier, the sinking current at the output OUT will be increased by G_m*1mA.

Figure 1: Internal schematic

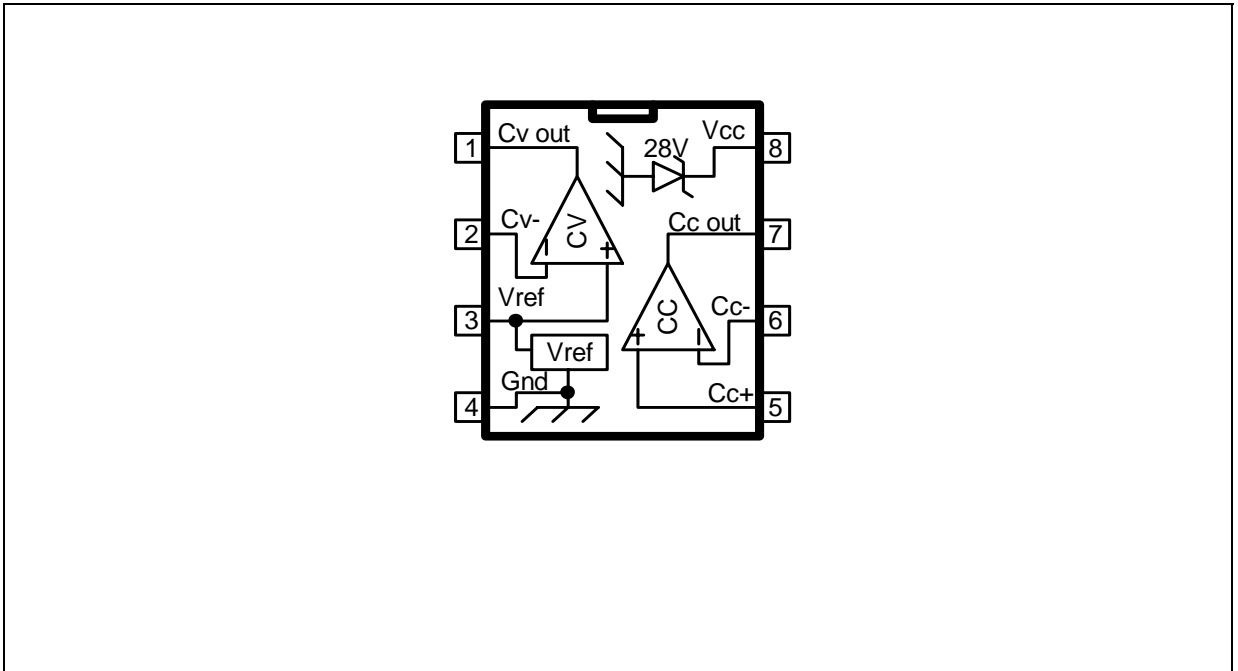
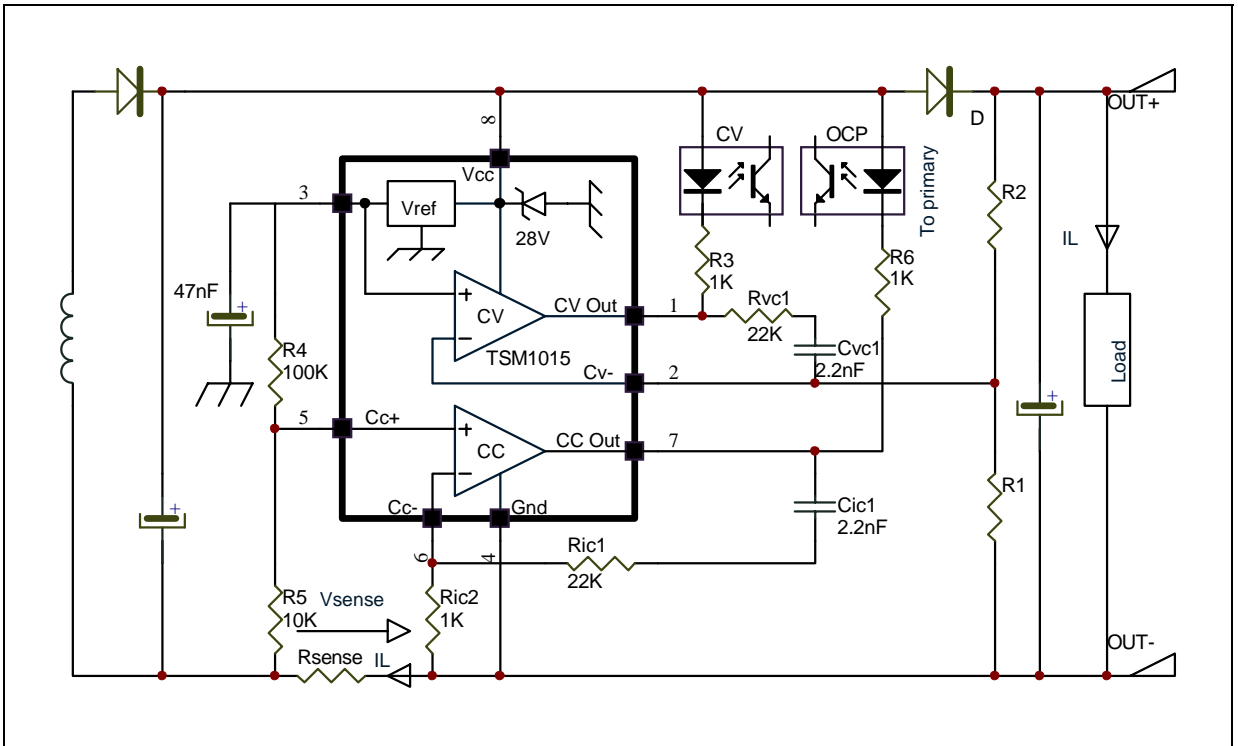


Figure 2: Typical adapter or battery charger application using TSM1015



In the above application schematic, the TSM1015 is used on the secondary side of a flyback adapter (or battery charger) to provide an accurate voltage and current control. The above feedback loop is made with optocoupler.

3 Principles of Operation and Application Tips

3.1 Voltage control

The voltage loop is controlled via a first transconductance operational amplifier, the resistor bridge R1, R2, and the optocoupler which is directly connected to the output.

The relation between the values of R1 and R2 should be chosen as written in [Equation 1](#).

$$R1 = R2 \times V_{Ref} / (V_{out} - V_{Ref}) \quad \text{Equation 1}$$

Where V_{out} is the desired output voltage.

To avoid the discharge of the load, the resistor bridge R1, R2 should be highly resistive. For this type of application, a total value of 100K Ω (or more) would be appropriate for the resistors R1 and R2.

As an example, with $R2 = 100\text{K}\Omega$, $V_{out} = 4.10\text{V}$, $V_{ref} = 1.25\text{V}$, then $R1 = 41.9\text{K}\Omega$.

Note that if the low drop diode should be inserted between the load and the voltage regulation resistor bridge to avoid current flowing from the load through the resistor bridge, this drop should be taken into account in the above calculations by replacing V_{out} by $(V_{out} + V_{drop})$.

3.2 Current control

The current loop is controlled via the second trans-conductance operational amplifier, the sense resistor R_{sense} , and the optocoupler.

V_{sense} threshold is achieved externally by a resistor bridge tied to the V_{ref} voltage reference. Its middle point is tied to the positive input of the current control operational amplifier, and its foot is to be connected to lower potential point of the sense resistor as shown on the following figure. The resistors of this bridge are matched to provide the best precision possible.

The control equation verifies:

$$R_{sense} \times I_{lim} = V_{sense} \quad \text{Equation 2}$$

$$V_{sense} = R5 \times V_{ref} / (R4 + R5)$$

$$I_{lim} = R5 \times V_{ref} / (R4 + R5) \times R_{sense} \quad \text{Equation 2'}$$

where I_{lim} is the desired limited current, and V_{sense} is the threshold voltage for the current control loop.

Note that the R_{sense} resistor should be chosen taking into account the maximum dissipation (P_{lim}) through it during full load operation.

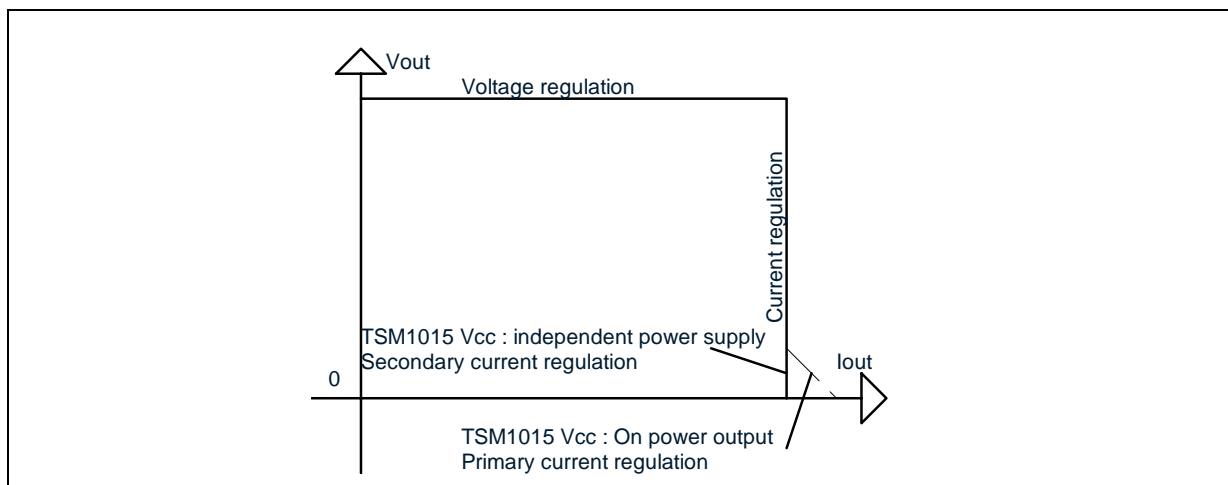
$$P_{lim} = V_{sense} \times I_{lim} \quad \text{Equation 3}$$

Therefore, for most adapter and battery charger applications, a quarter-watt, or half-watt resistor to make the current sensing function is sufficient.

The current sinking outputs of the two trans-conductance operational amplifiers are common (to the output of the IC). This makes an ORing function which ensures that whenever the current or the voltage reaches too high values, the optocoupler is activated.

The relation between the controlled current and the controlled output voltage can be described with a square characteristic as shown in the following V/I output-power graph.

Figure 3: Output voltage versus output current



3.3 Compensation

The voltage-control trans-conductance operational amplifier can be fully compensated. Both of its output and negative input are directly accessible for external compensation components.

An example of a suitable compensation network is shown in Fig.2. It consists of a capacitor $C_{vc1}=2.2nF$ and a resistor $R_{cv1}=22K\Omega$ in series.

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3.4 Start-up and short circuit conditions

Under start-up or short-circuit conditions the TSM1015 is not provided with a high enough supply voltage. This is due to the fact that the chip has its power supply line in common with the power supply line of the system.

Therefore, the current limitation can only be ensured by the primary PWM module, which should be chosen accordingly.

If the primary current limitation is considered not to be precise enough for the application, then a sufficient supply for the TSM1015 has to be ensured under any condition. It would then be necessary to add some circuitry to supply the chip with a separate power line. This can be achieved in numerous ways, including an additional winding on the transformer.

3.5 Voltage clamp

The following schematic shows how to realize a low-cost power supply for the TSM1015 (with no additional windings). Please pay attention to the fact that in the particular case presented here, this low-cost power supply can reach voltages as high as twice the voltage of the regulated line. Since the Absolute Maximum Rating of the TSM1015 supply voltage is 28V. In the aim to protect the TSM1015 against such high voltage values an internal zener clamp is integrated.

$$R_{limit} = (V_{cc} - V_z) / I_z$$

Figure 4: Clamp voltage

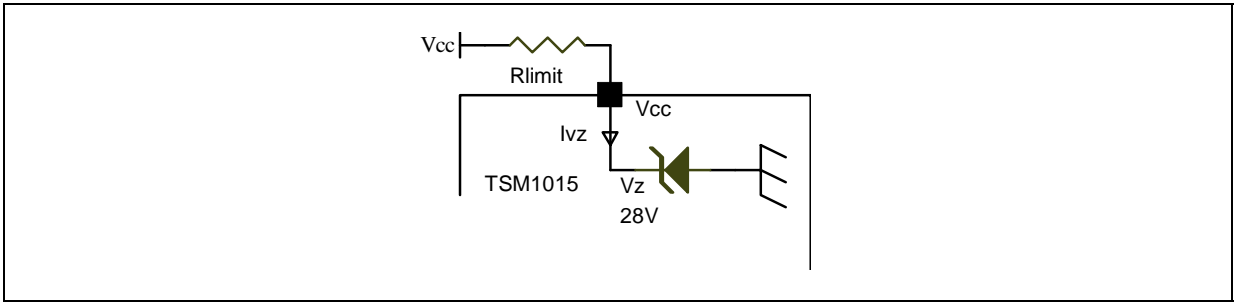
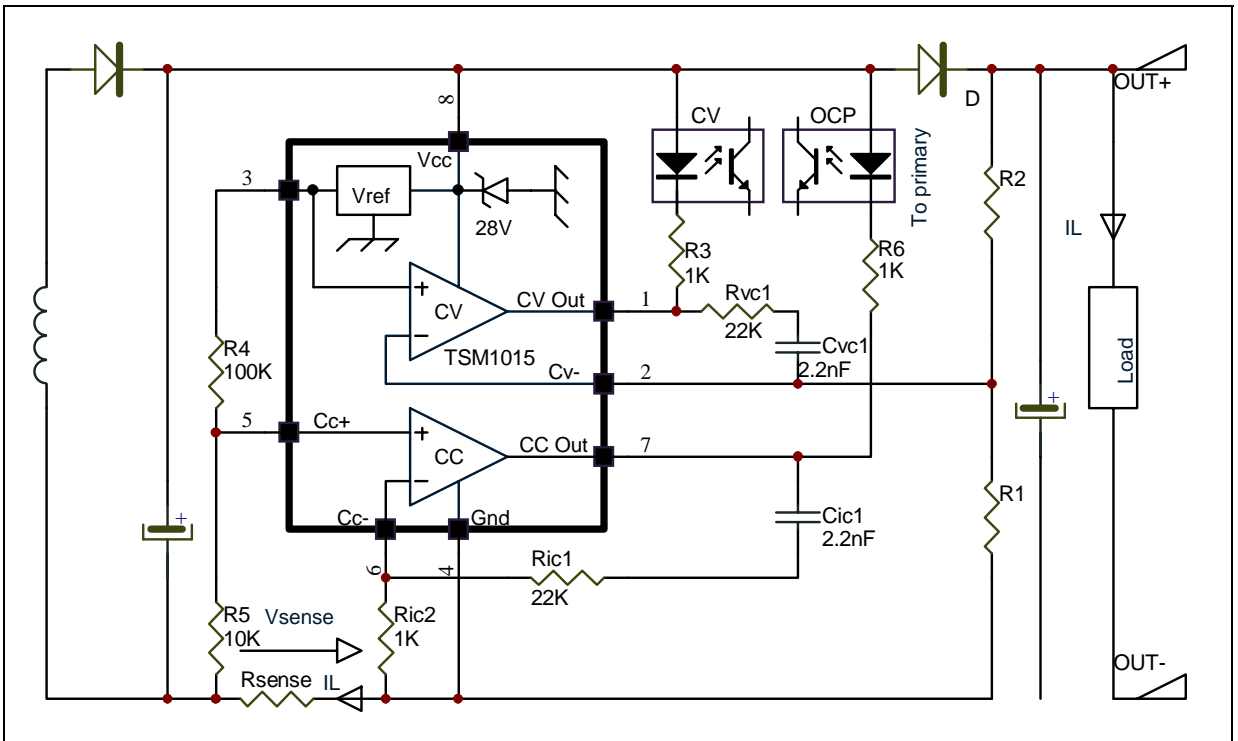


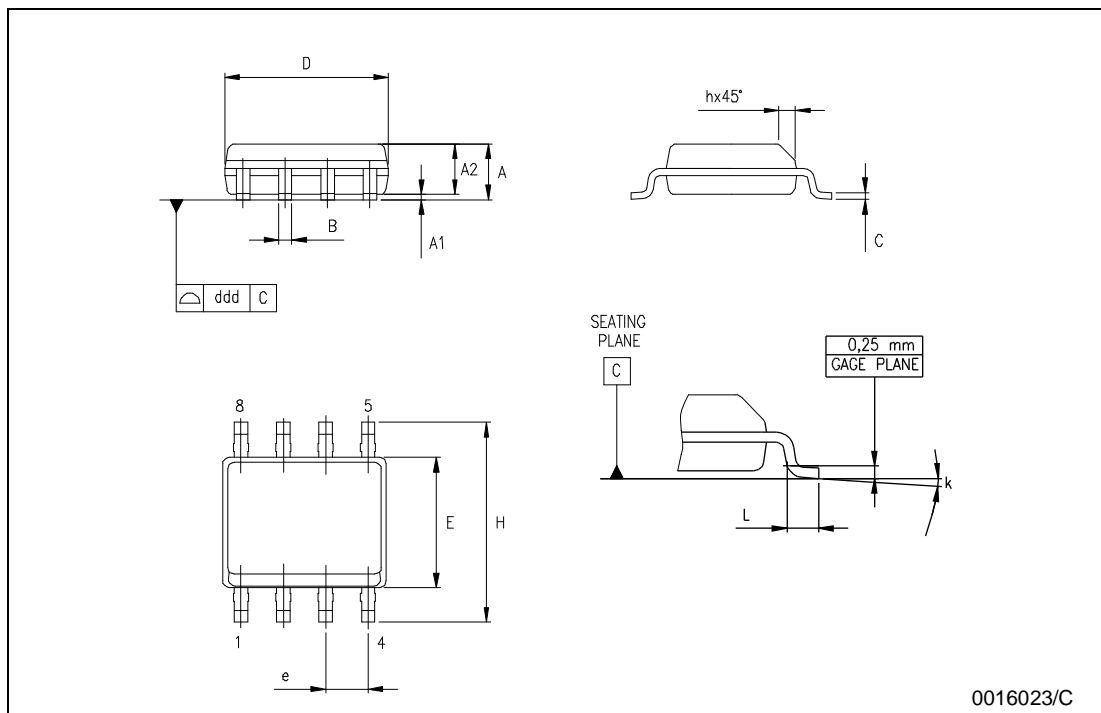
Figure 5: Voltage controller and over current detection schematic



4 Package Mechanical Data

SO-8 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	1.35		1.75	0.053		0.069
A1	0.10		0.25	0.04		0.010
A2	1.10		1.65	0.043		0.065
B	0.33		0.51	0.013		0.020
C	0.19		0.25	0.007		0.010
D	4.80		5.00	0.189		0.197
E	3.80		4.00	0.150		0.157
e		1.27			0.050	
H	5.80		6.20	0.228		0.244
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
k	8° (max.)					
ddd			0.1			0.04



5 Revision History

Date	Revision	Description of Changes
01 Nov 2004	1	First Release

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