

# NE5550234

# Silicon Power MOS FET

R09DS0039EJ0100 Rev.1.00 Apr 25, 2012

#### **FEATURES**

• High Output Power :  $P_{out}$  = 33.0 dBm TYP. ( $V_{DS}$  = 7.5 V,  $I_{Dset}$  = 40 mA, f = 460 MHz,  $P_{in}$  = 15 dBm) • High power added efficiency :  $\eta_{add}$  = 68% TYP. ( $V_{DS}$  = 7.5 V,  $I_{Dset}$  = 40 mA, f = 460 MHz,  $P_{in}$  = 15 dBm) • High Linear gain :  $G_{L}$  = 23.5 dB TYP. ( $G_{DS}$  = 7.5 V,  $G_{Dset}$  = 40 mA, f = 460 MHz,  $G_{Dset}$  = 0 dBm)

• High ESD tolerance

• Suitable for VHF to UHF-BAND Class-AB power amplifier.

#### **APPLICATIONS**

• 150 MHz Band Radio System

• 460 MHz Band Radio System

• 900 MHz Band Radio System

#### ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
NE5550234	NE5550234-AZ	3-pin	V5	12 mm wide embossed taping
		power minimold		Gate pin faces the perforation side of the tape
NE5550234-T1	NE5550234-T1-AZ	(34 PKG)		12 mm wide embossed taping
		(Pb-Free)		Gate pin faces the perforation side of the tape
		, , , , , , , , , , , , , , , , , , ,		Qty 1 kpcs/reel

Remark To order evaluation samples, please contact your nearby sales office.

Part number for sample order: NE5550234

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}$ C, unless otherwise specified)

Operation in excess of any one of these parameters may result in permanent damage.

Parameter	Symbol	Ratings	Unit
Drain to Source Voltage	$V_{DS}$	30	V
Gate to Source Voltage	$V_{GS}$	6.0	V
Drain Current	I <sub>DS</sub>	0.6	Α
Drain Current	I <sub>DS-pulse</sub>	1.2	Α
(50% Duty Pulsed)			
Total Power Dissipation Note	P <sub>tot</sub>	12.5	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	−65 to +150	°C

Note: Value at  $T_C = 25^{\circ}C$ 

#### **CAUTION**

Observe precautions when handling because these devices are sensitive to electrostatic discharge.

# RECOMMENDED OPERATING RANGE ( $T_A = 25$ °C)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Drain to Source Voltage	$V_{DS}$		-	7.5	9.0	V
Gate to Source Voltage	$V_{GS}$		1.65	2.20	2.85	V
Drain Current	I <sub>DS</sub>		_	0.38	_	Α
Input Power	Pin	$f = 460 \text{ MHz}, V_{DS} = 7.5 \text{ V}$	_	15	20	dBm

# **ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, unless otherwise specified)**

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = 6.0 V	_	_	100	nA
Drain to Source Leakage Current	$I_{DSS}$	V <sub>DS</sub> = 25 V	_	_	10	μΑ
(Zero Gate Voltage Drain Current)						
Gate Threshold Voltage	$V_{th}$	$V_{DS} = 7.5 \text{ V}, I_{DS} = 1.0 \text{ mA}$	1.15	1.65	2.25	V
Drain to Source Breakdown Voltage	$BV_{DSS}$	$I_{DS} = 10 \mu A$	25	38	-	V
Transconductance	$G_{m}$	$V_{DS} = 7.5 \text{ V}, I_{DS} = 140 \pm 100 \text{ mA}$	_	0.44	ı	S
Thermal Resistance	$R_{th}$	Channel to Case	_	10.0	ı	°C/W
RF Characteristics						
Output Power	$P_{\text{out}}$	$f = 460 \text{ MHz}, V_{DS} = 7.5 \text{ V},$	31.5	33.0	ı	dBm
Drain Current	I <sub>DS</sub>	$P_{in} = 15 \text{ dBm},$	_	0.38	-	Α
Power Drain Efficiency	$\eta_{\sf d}$	I <sub>Dset</sub> = 40 mA (RF OFF)	_	70	-	%
Power Added Efficiency	$\eta_{add}$		_	68	-	%
Linear Gain	G <sub>L</sub> Note 1		_	23.5	-	dB
Load VSWR Tolerance	Note 2	$f = 460 \text{ MHz}, V_{DS} = 9.0 \text{ V},$	١	lo Destro	У	
		$P_{in} = 15 \text{ dBm},$				
		I <sub>Dset</sub> = 40 mA (RF OFF)				
		Load VSWR=20:1(All Phase)				
Output Power	$P_{out}$	$f = 157 \text{ MHz}, V_{DS} = 7.5 \text{ V},$	_	33.0	-	dBm
Drain Current	$I_{DS}$	$P_{in} = 15 \text{ dBm},$	_	0.36	1	Α
Power Drain Efficiency	$\eta_{\sf d}$	I <sub>Dset</sub> = 40 mA (RF OFF)	_	74	ı	%
Power Added Efficiency	$\eta_{add}$		_	73	ı	%
Linear Gain	G <sub>L</sub> Note 3		_	25.8	ı	dB
Output Power	P <sub>out</sub>	$f = 900 \text{ MHz}, V_{DS} = 7.5 \text{ V},$	_	32.2	-	dBm
Drain Current	I <sub>DS</sub>	P <sub>in</sub> = 17 dBm,	_	0.35	_	Α
Power Drain Efficiency	$\eta_{d}$	I <sub>Dset</sub> = 40 mA (RF OFF)	_	62	_	%
Power Added Efficiency	$\eta_{add}$		_	60	_	%
Linear Gain	G <sub>L</sub> Note 4		_	18.3	_	dB

Notes: 1.  $P_{in} = 0 dBm$ 

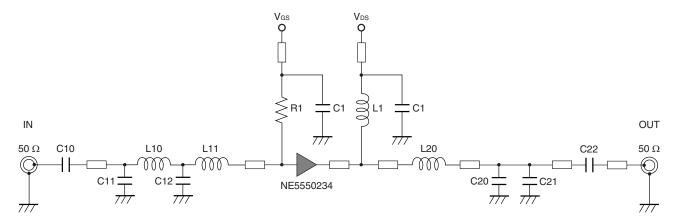
2. These characteristics values are measurement using measurement tools especially by RENESAS.

3.  $P_{in} = -5 \text{ dBm}$ 

4.  $P_{in} = 7 dBm$ 

**Remark** DC performance is 100% testing. RF performance is testing several samples per wafer. The wafer rejection criterion for standard devices is 1 reject for several samples.

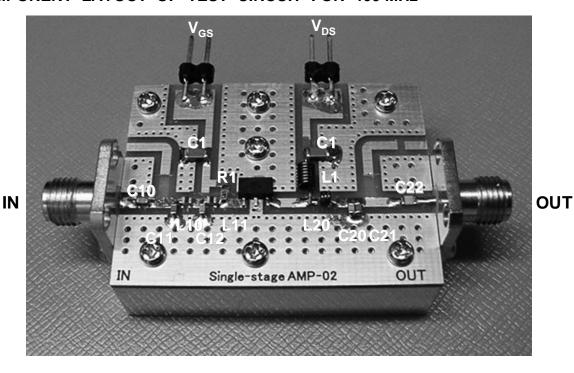
### TEST CIRCUIT SCHEMATIC FOR 460 MHz



### COMPONENTS OF TEST CIRCUIT FOR MEASURING ELECTRICAL CHARACTERISTICS

Symbol	Value	Туре	Maker
C1	1 <i>μ</i> F	GRM31MR71H105KA88L	Murata
C10	27 pF	GRM1882C1H270JA01	Murata
C11	3.9 pF	GRM1882C1H3R9CZ01	Murata
C12	18 pF	GRM1882C1H180JA01	Murata
C20	12 pF	GRM1882C1H120JA01	Murata
C21	1.5 pF	GRM1882C1H1R5CZ01	Murata
C22	100 pF	GRM2162C1H101JA01D	Murata
R1	4.7 kΩ	1/10 W Chip Resistor	SSM
		SSM_RG1608PB472	
L1	47.2 nH	$\phi$ 0.4 mm, $\phi$ D = 2 mm, 7 Turns	Ohesangyou
L10, L11	12 nH	LL1608-FS12NJ	токо
L20	7.8 nH	$\phi$ 0.4 mm, $\phi$ D = 1.4 mm, 3 Turns	Ohesangyou
PCB	_	R1776, $t = 0.8 \text{ mm}$ , $\varepsilon r = 4.8$ , $size = 30 \times 40 \text{ mm}$	Panasonic
SMA Connecter	_	WAKA 01K0790-20	WAKA

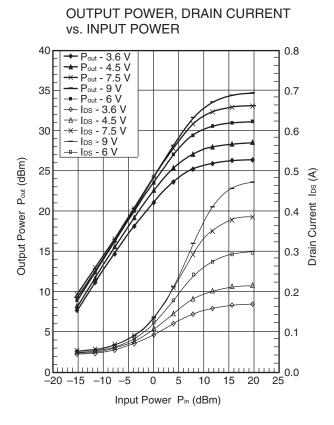
### COMPONENT LAYOUT OF TEST CIRCUIT FOR 460 MHz

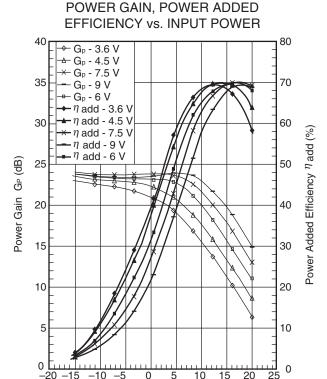


### TYPICAL CHARACTERISTICS 1 ( $T_A = 25^{\circ}C$ )

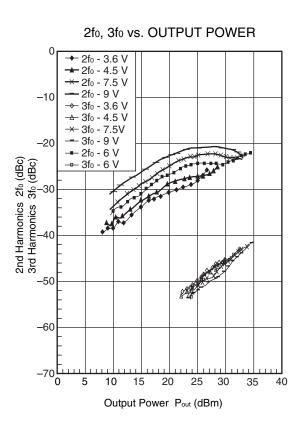
RF: f = 460MHz,  $V_{DS} = 3.6/4.5/6/7.5/9 V$ ,  $I_{Dset} = 40 mA$ ,  $P_{in} = -15 to 20 dBm$ 

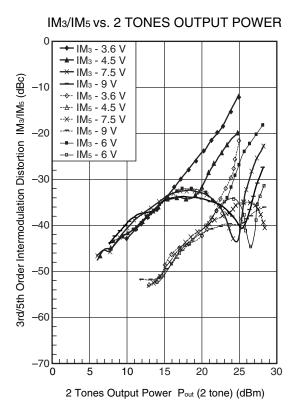
IM: f1 = 460MHz, f2 = 461 MHz,  $V_{DS} = 3.6/4.5/6/7.5/9 V$ ,  $I_{Dset} = 40mA$ ,  $P_{out}$  (2 tone) = 6 to 28 dBm





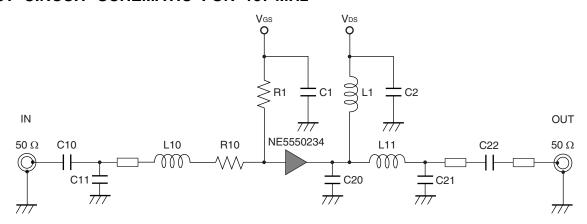
Input Power Pin (dBm)





Remark The graphs indicate nominal characteristics.

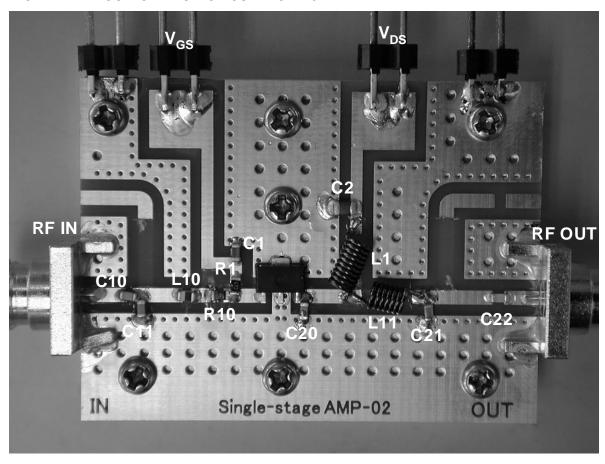
# TEST CIRCUIT SCHEMATIC FOR 157 MHz



### COMPONENTS OF TEST CIRCUIT FOR MEASURING ELECTRICAL CHARACTERISTICS

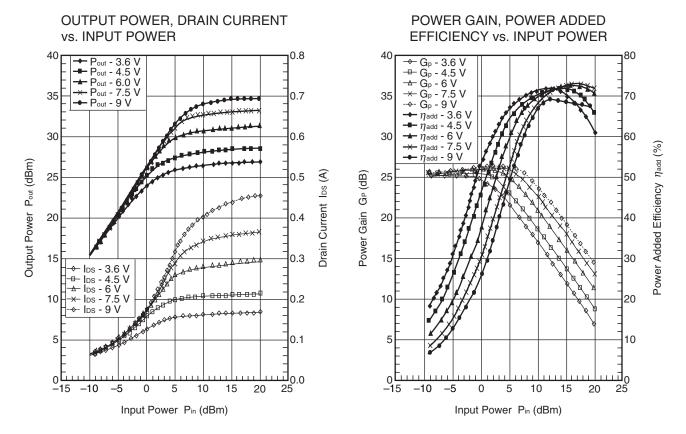
Symbol	Value	Туре	Maker
C10	27 pF	GQM1882C1H270JB01	Murata
C11	6.8 pF	GQM1882C1H6R8DB01	Murata
C20	8.2 pF	GQM1882C1H8R2DB01	Murata
C21	27 pF	GQM1882C1H270JB01	Murata
C22	100 pF	GQM1882C1H101JB01	Murata
C1	1 <i>μ</i> F	GRM21BB31H105KA2L	Murata
C2	1 <i>μ</i> F	GRM21BB31H105KA2L	Murata
L10	100 nH	LL1608-FSLR10J	Toko
L11	47 nH	D20-47N2	Ohesangyou
L1	74 nH	D20-74N7	Ohesangyou
R11	5.6 Ω	MCR03J5R6	Rohm
R1	4.7 kΩ	MCR03J472	Rohm
PCB	_	R1776, t = 0.8 mm, $\varepsilon$ r = 4.8, size = 30 × 40 mm	Panasonic
SMA Connecter	-	WAKA 01K0790-20	WAKA

### COMPONENT LAYOUT OF TEST CIRCUIT FOR 157 MHz



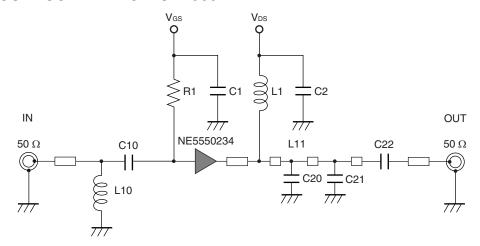
### TYPICAL CHARACTERISTICS 2 ( $T_A = 25^{\circ}C$ )

RF: f = 157 MHz,  $V_{DS} = 3.6/4.5/6/7.5/9$  V,  $I_{Dset} = 40$  mA,  $P_{in} = -10$  to 20 dBm



**Remark** The graphs indicate nominal characteristics.

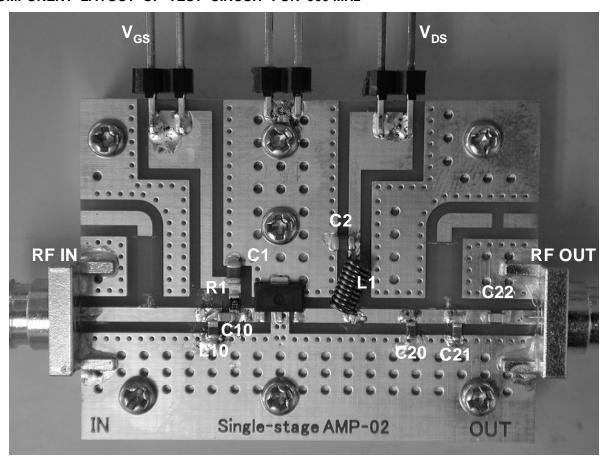
### **TEST CIRCUIT SCHEMATIC FOR 900 MHz**



### COMPONENTS OF TEST CIRCUIT FOR MEASURING ELECTRICAL CHARACTERISTICS

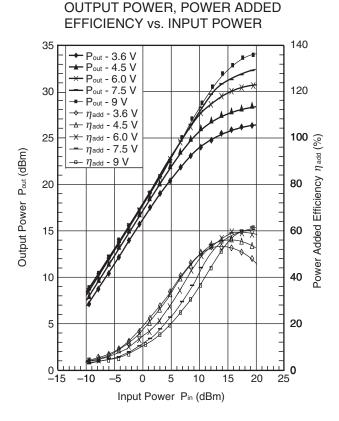
Symbol	Value	Туре	Maker
C10	10 pF	GQM1882C1H100JB01	Murata
C20	6.8 pF	GQM1882C1H6R8DB01	Murata
C21	1 pF	GQM1884C2A1R0CB01	Murata
C22	100 pF	GQM1882C1H101JB01	Murata
C1	1 <i>μ</i> F	GRM21BB31H105KA2L	Murata
C2	1 <i>μ</i> F	GRM21BB31H105KA2L	Murata
L10	2.7 nH	LL1608-FSL2N7S	Toko
L1	74 nH	D20-74N7	Ohesangyou
R1	4.7 kΩ	MCR03J472	Rohm
PCB	_	R1776, $t = 0.8$ mm, $\epsilon r = 4.8$ , size = $30 \times 40$ mm	Panasonic
SMA Connecter	ı	WAKA 01K0790-20	WAKA

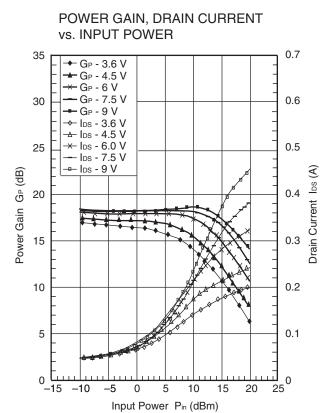
# COMPONENT LAYOUT OF TEST CIRCUIT FOR 900 MHz



# TYPICAL CHARACTERISTICS 3 (T<sub>A</sub> = 25°C)

RF: f = 900 MHz,  $V_{DS} = 3.6/4.5/6/7.5/9 \text{ V}$ ,  $I_{Dset} = 40 \text{ mA}$ ,  $P_{in} = -10 \text{ to } 20 \text{ dBm}$ 





Remark The graphs indicate nominal characteristics.

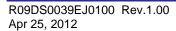
### **S-PARAMETERS**

S-parameters and noise parameters are provided on our web site in a form (S2P) that enables direct import to microwave circuit simulators without keyboard inputs.

Click here to download S-parameters.

 $[Products] \rightarrow [RF \ Devices] \rightarrow [Device \ Parameters]$ 

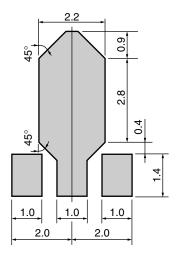
URL http://www.renesas.com/products/microwave/download/parameter/





### **MOUNTING PAD LAYOUT DIMENSIONS**

### 3-PIN POWER MINIMOLD (34 PKG) (UNIT: mm)

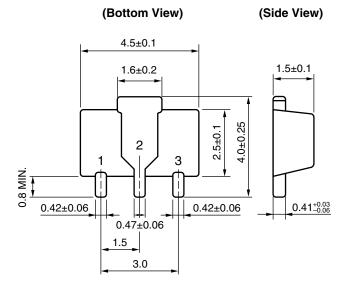


**Remark** The mounting pad layout in this document is for reference only.

When designing PCB, please consider workability of mounting, solder joint reliability, prevention of solder bridge and so on, in order to optimize the design.

# **PACKAGE DIMENSIONS**

### 3-PIN POWER MINIMOLD (34 PKG) (UNIT: mm)



#### **PIN CONNECTIONS**

- 1. Drain
- 2. Source
- 3. Gate

### RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature)	: 260°C or below	IR260
	Time at peak temperature	: 10 seconds or less	
	Time at temperature of 220°C or higher	: 60 seconds or less	
	Preheating time at 120 to 180°C	: 120±30 seconds	
	Maximum number of reflow processes	: 3 times	
	Maximum chlorine content of rosin flux (% mass)	: 0.2% (Wt.) or below	
Wave Soldering	Peak temperature (molten solder temperature)	: 260°C or below	WS260
	Time at peak temperature	: 10 seconds or less	
	Preheating temperature (package surface temperature)		
		: 120°C or below	
	Maximum number of flow processes	: 1 time	
	Maximum chlorine content of rosin flux (% mass)	: 0.2% (Wt.) or below	
Partial Heating	Peak temperature (terminal temperature)	: 350°C or below	HS350
	Soldering time (per side of device)	: 3 seconds or less	
	Maximum chlorine content of rosin flux (% mass)	: 0.2% (Wt.) or below	

### **CAUTION**

Do not use different soldering methods together (except for partial heating).

**Revision History** 

# NE55500234 Data Sheet

			Description		
Rev.	Date	Page	Summary		
1.00	Apr 25, 2012	_	First edition issued		

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