PJSRV05W-4DW

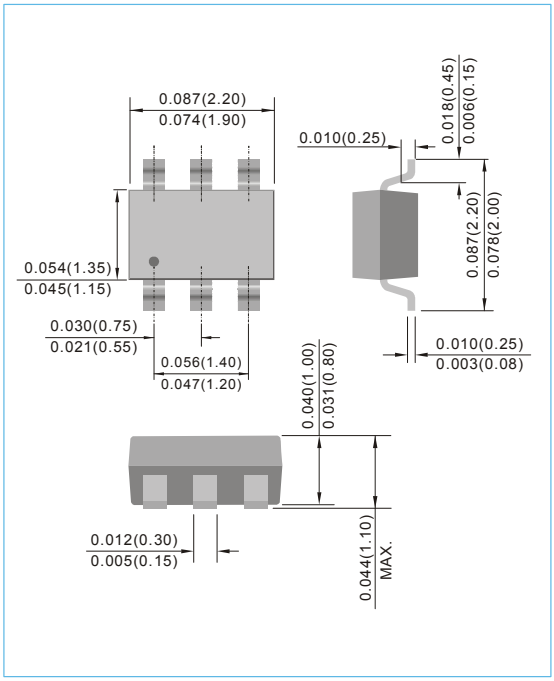
LOW CAPACITANCE TVS DIODE ARRAY

The PJSRV05W-4LC has a low typical capacitance of 0.8pF and operates with virtually no insertion loss to 1GHz.This makes the device ideal for protection of high-speed data lines such as USB2.0,Firewire,DVI,and Gigabit Ethernet interfaces.
The low capacitance array configuration allows the user to prote ct four high-speed data or transmission lines.The low inductance construction minimizes voltage overshoor during high current surge.

FEATURES

- IEC61000-4-2 ESD 15kV Air,8kV Contact compliance
- Low leakage current
- Low clamping voltage
- Peak power dissipation of 150W under 8/20μs waveform
- Protect four I/O lines
- Molded JEDEC SOT-363 package
- Lead free in comply with EU RoHS 2002/95/EC directives.
- Green molding compound as per IEC61249 Std. . (Halogen Free)

SOT-363 Unit : inch(mm)



MECHANICAL DATA

- Case: SOT-363, Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Weight: approximately 0.0002 ounces, 0.006 grams
- Marking : KW

APPLICATIONS

- USB 2.0 Power and Data Line Protection
- Video Graphics Cards
- Mouitors and Flat Panel Displays
- Digital Vedio Interface (DVI)
- 10/100/1000 Ethernet
- ATM Interfaces

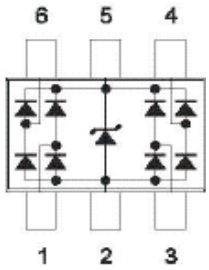


Fig.70

ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

RATING	SYMBOL	VALUE	UNIT
Peak Pulse Power (8/20μs waveform)	P_{PP}	50	W
Peak Pulse Current (8/20μs waveform)	I_{PPM}	5	A
ESD per IEC61000-4-2 (Air) ESD per IEC61000-4-2 (Contact)	V_{ESD}	± 8 ±15	kV
Operating Junction Temperature and Storage Temperature Range	T_J,T_{STG}	-55 to +150	°C

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ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	V_{RWM}		-	-	5	V
Reverse Breakdown Voltage	V_{BR}	I_{BR}=1mA, PIN 5 to 2	6	-	8	V
Reverse Leakage Current	I_R	V_R=5V, PIN 5 to 2	-	-	3	µA
Clamping Voltage (8/20µs)	V_C	I_{PP}=1A,Any I/O pin to pin 2	-	-	8	V
Clamping Voltage (8/20µs)	V_C	I_{PP}=5A,Any I/O pin to pin 2	-	-	10	V
Off State Junction Capacitance	C_J	0 Vdc,f=1MHz between I/O lines and GND	-	1	1.2	pF
Off State Junction Capacitance	C_J	0 Vdc,f=1MHz between I/O lines	-	0.5	0.6	pF

RATING AND CHARACTERISTIC CURVES

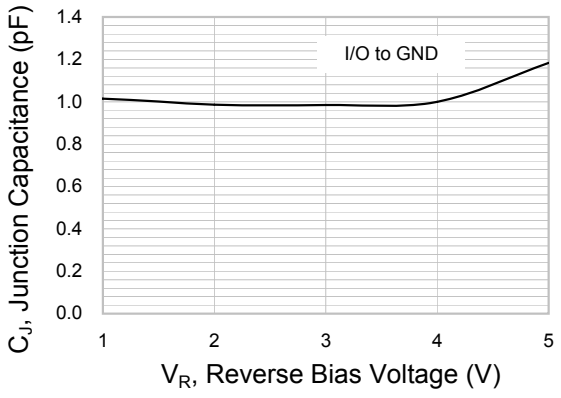

Fig.1 Typical Junction Capacitance

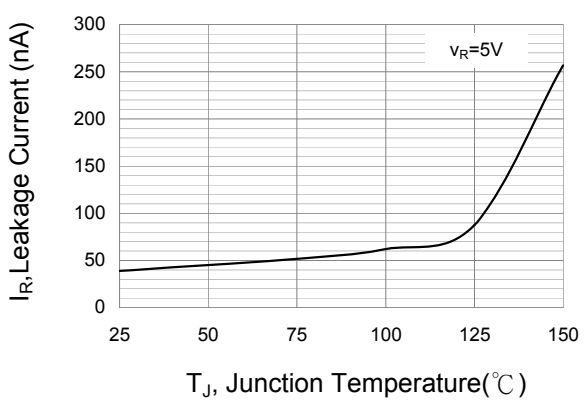

Fig.2 Typical Reverse Characteristics

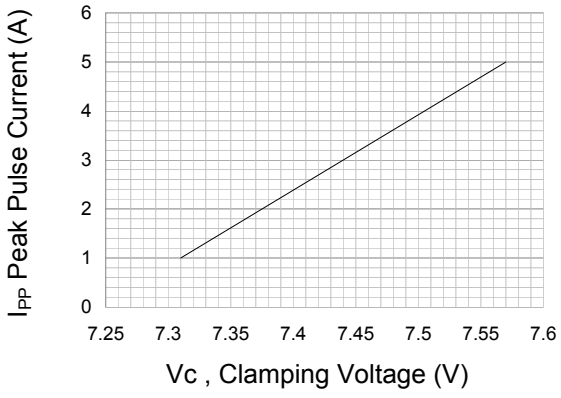

Fig.3 Typical Peak Clamping Voltage(8/20µs)

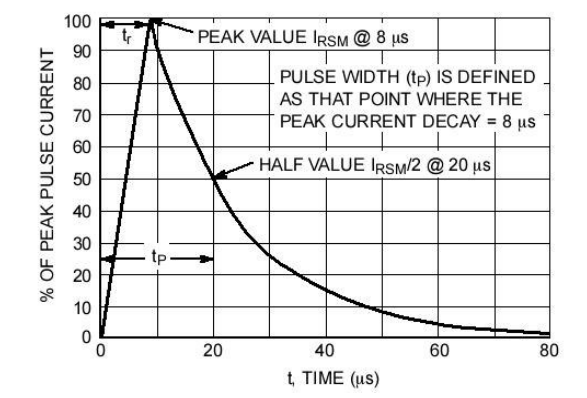

Fig.4 8/20µS Peak Pulse Current Waveform

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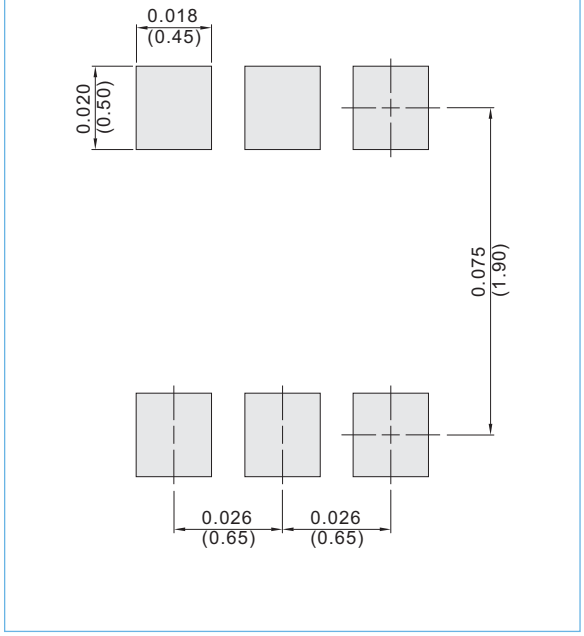
MOUNTING PAD LAYOUT

SOT-363 Unit : inch(mm)



ORDER INFORMATION

- Packing information

  T/R - 10K per 13" plastic Reel

  T/R - 3K per 7” plastic Reel

LEGAL STATEMENT

PJSRV05W-4DW

For example :

RB500V-40_R2_00001

Part No.

Serial number

Version code means HF

Packing size code means 13"

Packing type means T/R

Packing Code XX				Version Code XXXXX		
Packing type	1st Code	Packing size code	2nd Code	HF or RoHS	1st Code	2nd~5th Code
T/B	A	N/A	0	HF	0	serial number
T/R	R	7"	1	RoHS	1	serial number
B/P	B	13"	2			
T/P	T	26mm	X			
TRR	S	52mm	Y			
TRL	L	PBCU	U			
FORMING	F	PBCD	D			

Part No_packing code_Version

PJSRV05W-4DW_R1_00001

PJSRV05W-4DW_R2_00001

PJSRV05W-4DW_00001