

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^a		
30	0.011 at V _{GS} = 10 V	50		
	0.017 at V _{GS} = 4.5 V	43		

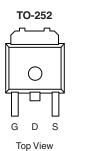
FEATURES

- TrenchFET[®] Power MOSFET
- 175 °C Maximum Junction Temperature

GC

• 100 % R_g Tested





Drain Connected to Tab

Ordering Information: SUD50N03-11-E3 (Lead (Pb)-free)

N-Channel MOSFET

D

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	30	V		
Gate-Source Voltage		V _{GS}	± 20	V		
Continuous Ducin Current /T 175 °O	T _C = 25 °C	1_	50			
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 100 °C	I _D	37			
Pulsed Drain Current		I _{DM}	100	A		
Continuous Source Current (Diode Conduction) ^a		۱ _S	50			
	T _C = 25 °C	Р	62.5 ^c	w		
Maximum Power Dissipation	T _A = 25 °C	P _D	7.5 ^b			
Operating Junction and Storage Temperature Range	•	T _J , T _{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Junction-to-Ambient ^b	t ≤ 10 s	- R _{thJA}	17	20			
Junction-to-Ambient ²	Steady State		50	60			
Junction-to-Case		R _{thJC}	2	2.4	°C/W		
Junction-to-Lead		R _{thJL}	4	4.8			

Notes:

a. Package limited.

b. Surface Mounted on 1" x 1" FR4 board, t \leq 10 s.

c. See SOA curve for voltage derating.

* Pb containing terminations are not RoHS compliant, exemptions may apply.



Parameter	Symbol	Test Conditions Min.		Typ. ^a	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$ $V_{GS} = 0 V, I_D = 250 \mu A$		30			v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.8			v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zana Oata Maltana Duain Ourmant		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}				50		
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 5 V$	50			А	
		V _{GS} = 10 V, I _D = 25 A	0.009 0.011		0.011		
Drain-Source On-State Resistance ^b	R _{DS(on)}	$V_{GS} = 5 \text{ V}, \text{ I}_{D} = 20 \text{ A}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			0.018	Ω	
		V _{GS} = 4.5 V, I _D = 15 A		0.014	0.017		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 20 A	10			S	
Dynamic ^a				•			
Input Capacitance	C _{iss}			1130		pF	
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz		400			
Reverse Transfer Capacitance	C _{rss}			175			
Total Gate Charge ^c	Qg			12	20		
Gate-Source Charge ^c	Q _{gs}	V_{DS} = 15 V, V_{GS} = 5 V, I_{D} = 50 A		4		nC	
Gate-Drain Charge ^c	Q _{gd}			4.5			
Gate Resistance	Rg		0.5		3.4	Ω	
Turn-On Delay Time ^c	t _{d(on)}			8	12	- ns	
Rise Time ^c	t _r	V_{DD} = 15 V, R_L = 0.3 Ω		10	15		
Turn-Off Delay Time ^c	t _{d(off)}	$\rm I_D \cong 50$ A, $\rm V_{GEN}$ = 10 V, $\rm R_G$ = 2.5 Ω		18	30		
Fall Time ^c	t _f			6	9		
Source-Drain Diode Ratings and Cha	racteristics 7	_C = 25 °C		·			
Continuous Current	ا _S				50	٨	
Pulsed Current	I _{SM}				80	A	
Diode Forward Voltage ^b	V_{SD}	I _F = 100 A, V _{GS} = 0 V			1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 50 A, dl/dt = 100 A/μs		30	50	ns	

Notes:

a. Guaranteed by design, not subject to production testing.

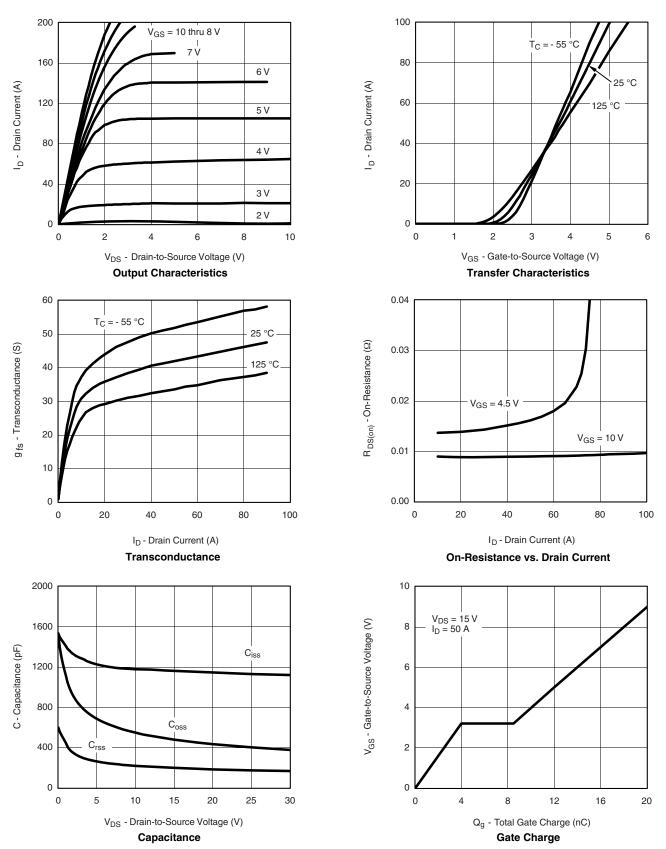
b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





. T_J = 150 °C

0.3

0.6

T_J = 25 °C

1.2

1.5

0.9

V_{SD} - Source-to-Drain Voltage (V)

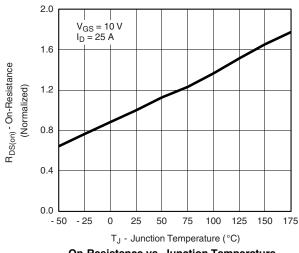
100

10

1 0

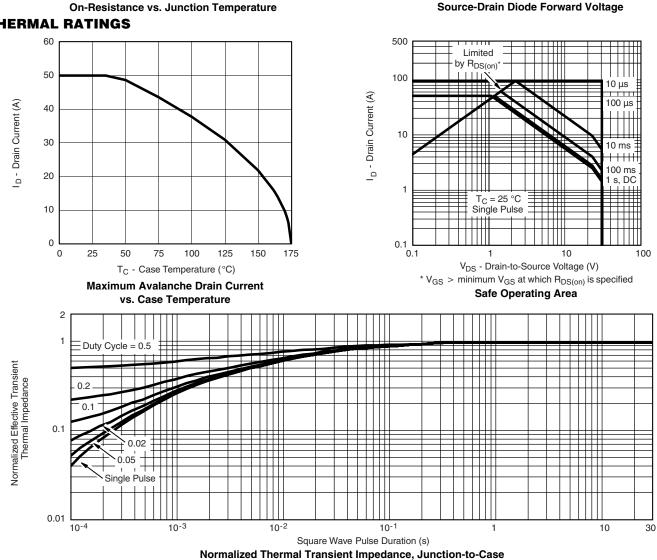
Is - Source Current (A)

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



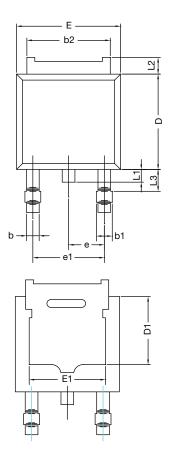
On-Resistance vs. Junction Temperature

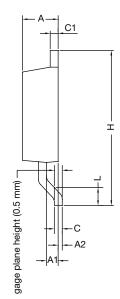
THERMAL RATINGS





TO-252AA CASE OUTLINE





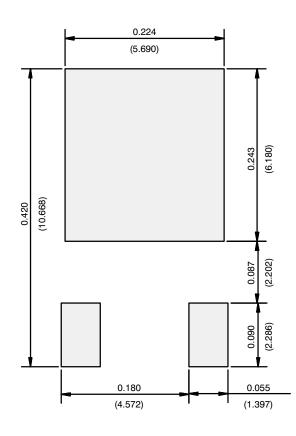
	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	2.21	2.38	0.087	0.094	
A1	0.89	1.14	0.035	0.045	
A2	0.030	0.127	0.001	0.005	
b	0.71	0.88	0.028	0.035	
b1	0.76	1.14	0.030	0.045	
b2	5.23	5.44	0.206	0.214	
С	0.46	0.58	0.018	0.023	
C1	0.46	0.58	0.018	0.023	
D	5.97	6.22	0.235	0.245	
D1	4.10	4.45	0.161	0.175	
E	6.48	6.73	0.255	0.265	
E1	4.49	5.50	0.177	0.217	
е	2.28	BSC	0.090 BSC		
e1	4.57 BSC		0.180	BSC	
Н	9.65	10.41	0.380	0.410	
L	1.40	1.78	0.055	0.070	
L1	0.64	1.02	0.025	0.040	
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.040	0.060	
ECN: T11-0110-Rev. L, 18-Apr-11 DWG: 5347					

Note

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

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