RENESAS

N0412N N-CHANNEL MOSFET FOR SWITCHING

R07DS0554EJ0100

Data Sheet

Rev.1.00 Nov 07, 2011

Description

The N0412N is N-channel MOS Field Effect Transistor designed for high current switching applications.

Features

• Low on-state resistance

 $R_{DS (on)} = 3.7 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, I_D = 50 \text{ A})$

• Low input capacitance

 $C_{iss} = 5550 \text{ pF TYP.} (V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V})$

• High current

 $I_{D(DC)} = \pm 100 \text{ A}$

• RoHS Compliant

Ordering Information

Part No.	Lead Plating	Packing	Package
N0412N-S19-AY *1	Pure Sn (Tin)	Tube	TO-220
		50 p/tube	1.9 g TYP.

Note: *1. Pb-free (This product does not contain Pb in the external electrode.)

Absolute Maximum Ratings (T_A = 25°C, all terminals are connected)

Item	Symbol	Ratings	Unit
Drain to Source Voltage (V_{GS} = 0 V)	V _{DSS}	40	V
Gate to Source Voltage (V _{DS} = 0 V)	V _{GSS}	±20	V
Drain Current (DC)	I _{D(DC)}	±100	А
Drain Current (pulse) *1	I _{D(pulse)}	±400	A
Total Power Dissipation ($T_C = 25^{\circ}C$)	P _{T1}	119	W
Total Power Dissipation ($T_A = 25^{\circ}C$)	P _{T2}	1.5	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Single Avalanche Current *2	I _{AS}	55	A
Single Avalanche Energy *2	E _{AS}	300	mJ

Thermal Resistance

Channel to Case (Drain) Thermal Resistance	R _{th(ch-C)}	1.05	°C/W
Channel to Ambient Thermal Resistance *2	Rth(ch-A)	83.3	°C/W

Notes: *1. PW \leq 10 μ s, Duty Cycle \leq 1%

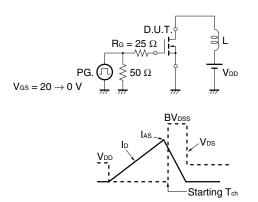
*2. Starting T_{ch} = 25°C, R_G = 25 Ω , V_{DD} = 25 V, V_{GS} = 20 \rightarrow 0 V, L = 100 μ H



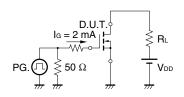
Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I _{DSS}			1	μA	V _{DS} = 40 V, V _{GS} = 0 V
Gate Leakage Current	I _{GSS}			±100	nA	V _{GS} = ±20 V, V _{DS} = 0 V
Gate to Source Cut-off Voltage	V _{GS(off)}	2.0		4.0	V	V _{DS} = 10 V, I _D = 1 mA
Forward Transfer Admittance *1	y _{fs}	26			S	V _{DS} = 10 V, I _D = 50 A
Drain to Source On-state Resistance ^{*1}	R _{DS(on)}		2.7	3.7	mΩ	V _{GS} = 10 V, I _D = 50 A
Input Capacitance	C _{iss}		5550		pF	V _{DS} = 25 V,
Output Capacitance	C _{oss}		580		pF	V _{GS} = 0 V,
Reverse Transfer Capacitance	C _{rss}		320		pF	f = 1 MHz
Turn-on Delay Time	t _{d(on)}		29.0		ns	V _{DD} = 20 V, I _D = 50 A,
Rise Time	tr		15.0		ns	V _{GS} = 10 V,
Turn-off Delay Time	t _{d(off)}		64.0		ns	R _G = 0 Ω
Fall Time	t _f		13.0		ns	
Total Gate Charge	Q _G		100		nC	V _{DD} = 32 V,
Gate to Source Charge	Q _{GS}		26		nC	V _{GS} = 10 V,
Gate to Drain Charge	Q _{GD}		32		nC	I _D = 100 A
Body Diode Forward Voltage *1	V _{F(S-D)}			1.5	V	I _F = 100 A, V _{GS} = 0 V
Reverse Recovery Time	trr		40		ns	I_F = 50 A, V_{GS} = 0 V,
Reverse Recovery Charge	Qrr		44		nC	di/dt = 100 A/µs

Note: *1. Pulsed

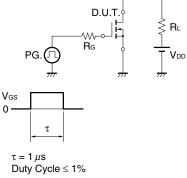
TEST CIRCUIT 1 AVALANCHE CAPABILITY

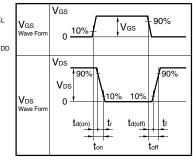


TEST CIRCUIT 3 GATE CHARGE



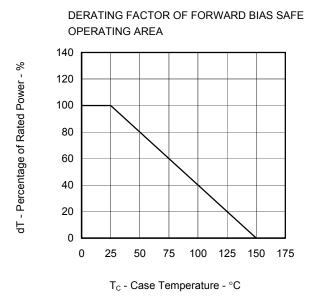
TEST CIRCUIT 2 SWITCHING TIME







l_D - Drain Current - A

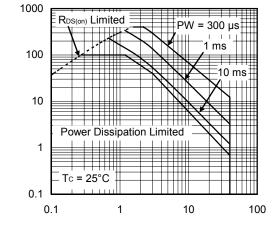


Typical Characteristics (T_A = 25°C)

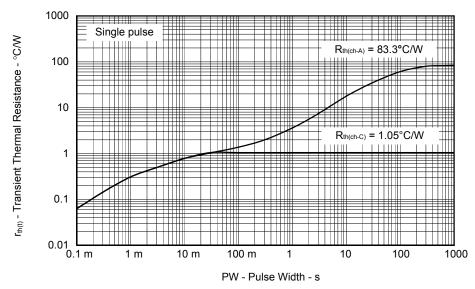
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE 140 120 100 80 60 40 20 0 0 25 50 75 100 125 150 175

T_C - Case Temperature - °C





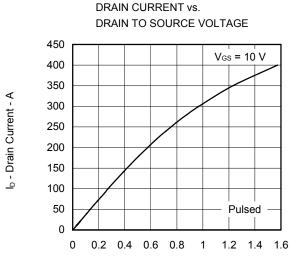
 V_{DS} - Drain to Source Voltage - V



TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

 $P_{\rm T}$ - Total Power Dissipation - W



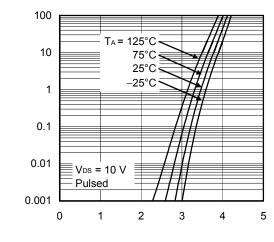




GATE TO SOURCE CUT-OFF VOLTAGE vs.

CHANNEL TEMPERATURE

FORWARD TRANSFER CHARACTERISTICS

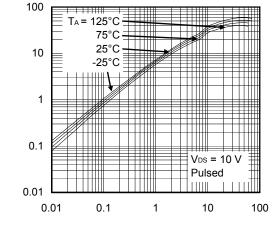


I_D - Drain Current - A

y_{is} | - Forward Transfer Admittance - S

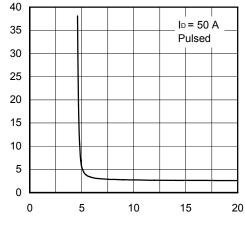


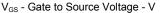
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



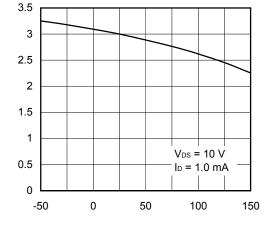


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



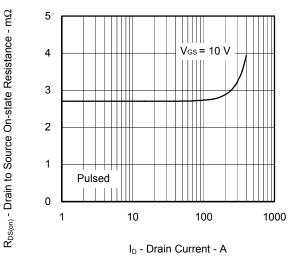


 $V_{GS(off)}$ - Gate to Source Cut-off Voltage - V



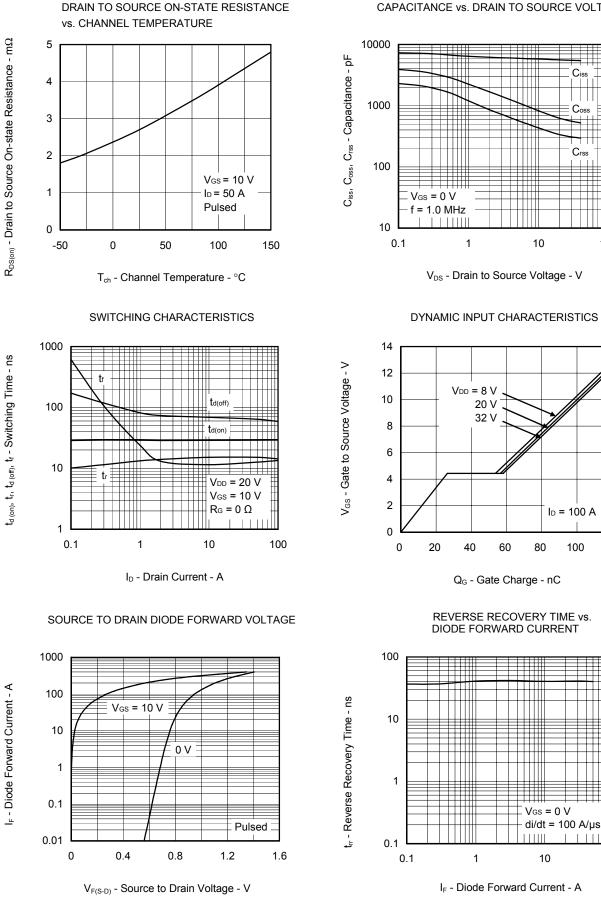
 T_{ch} - Channel Temperature - $^\circ C$

DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT





 $R_{DS(on)}$ - Drain to Source On-state Resistance - $m\Omega$



CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



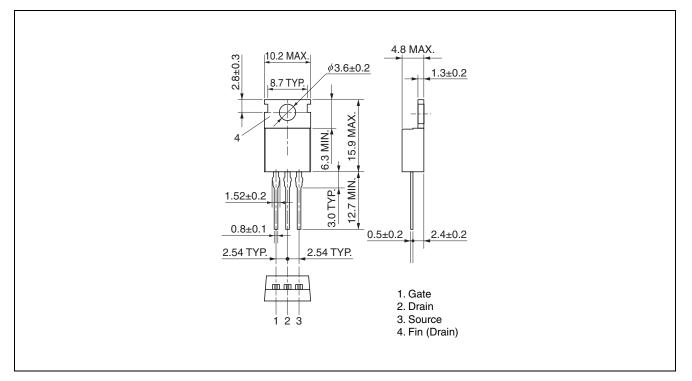
100

100

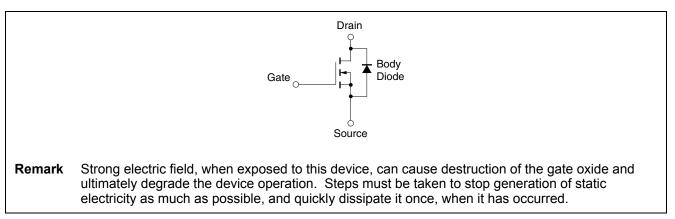
120

Package Drawing (Unit: mm)

TO-220



Equivalent Circuit





Revision History

		Description				
Rev.	Date	Page	Summary			
1.00	Nov 07, 2011	-	First Edition Issued			

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