

# **SRC1211UF**

**NPN Silicon Transistor** 

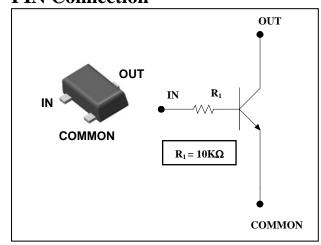
# **Descriptions**

- Switching application
- Interface circuit and driver circuit application

#### **Features**

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

#### **PIN Connection**



# **Ordering Information**

Type NO.	Marking	Package Code
SRC1211UF	<u>RD</u> <u> </u>	SOT-323F

①Device Code ② Year&Week Code

# **Absolute Maximum Ratings**

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	V <sub>I</sub>	30, -5	V
Output current	I <sub>O</sub>	100	mA
Power dissipation	$P_{D}$	200	mW
Junction temperature	TJ	150	°C
Storage temperature range	$T_{stg}$	-55 ~ 150	°C

#### **Electrical Characteristics**

(Ta=25°C)

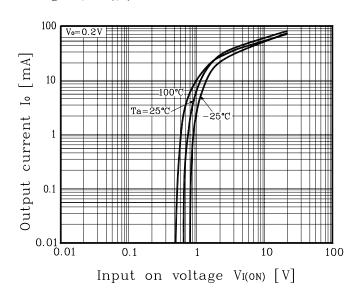
Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	$V_0 = 50V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	$V_0 = 5V$ , $I_0 = 10mA$	120	-	-	-
Output voltage	V <sub>O(ON)</sub>	I <sub>O</sub> =10mA, I <sub>I</sub> =0.5mA	-	0.1	0.3	V
Input voltage (ON)	V <sub>I(ON)</sub>	V <sub>O</sub> =0.2V, I <sub>O</sub> =5mA	-	0.9	1.4	V
Input voltage (OFF)	$V_{I(OFF)}$	V <sub>O</sub> =5V, I <sub>O</sub> =0.1mA	0.3	0.55	-	V
Transition frequency	f <sub>T</sub> *	$V_0=10V$ , $I_0=5mA$ , $f=1MHz$	-	200	-	MHz
Input current	I <sub>1</sub>	$V_1 = 5V, I_0 = 0$	-	-	0.88	mA
Input resistor (Input to base)	$R_1$	-	7	10	13	ΚΩ

<sup>\* :</sup> Characteristic of transistor only

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# **Electrical Characteristic Curves**

Fig. 1  $I_{O}\!-\!V_{I(ON)}$ 



 $Fig.\ 2\ I_O - V_{I(OFF)}$ 

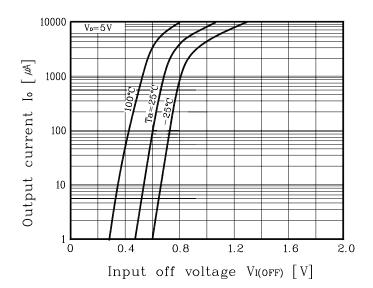
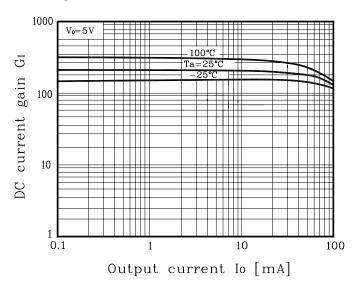
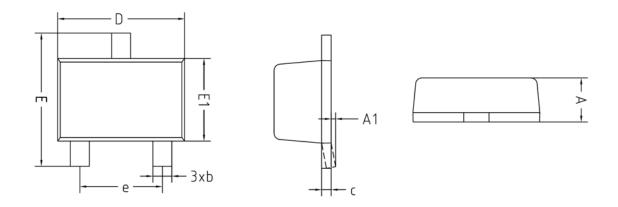


Fig. 3  $G_I$  -  $I_O$ 



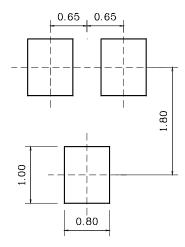
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# **Outline Dimension**



SYMBOL	MILLIMETERS			NOTE
STIBOL	MINIMUM	NOMINAL	MAXIMUM	NOTE
Α	0.60	ı	0.80	
A1	0.00	-	0.10	
Ь	0.30	ı	0.40	
С	0.08	-	0.16	
D	1.90	2.00	2.10	
Е	1.95	2.10	2.25	
E1	1.20	1.30	1.40	
е	1.30BSC			

#### \*Recommend PCB solder land [Unit: mm]



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# **SRC1211UF**

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