

# 2SJ548

# Silicon P Channel MOS FET

REJ03G0895-0300

(Previous: ADE-208-639A)

Rev.3.00 Sep 07, 2005

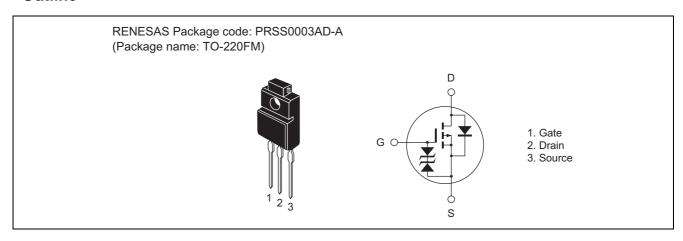
## **Description**

High speed power switching

### **Features**

- Low on-resistance  $R_{DS (on)} = 0.075 \Omega \text{ typ.}$
- Low drive current.
- 4 V gate drive devices.
- High speed switching.

### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	-15	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	-60	A
Body to drain diode reverse drain current	I <sub>DR</sub>	-15	A
Avalanche current	I <sub>AP</sub> Note 3	-15	A
Avalanche energy	E <sub>AR</sub> Note 3	19	mJ
Channel dissipation	Pch Note 2	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tc = 25°C

3. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

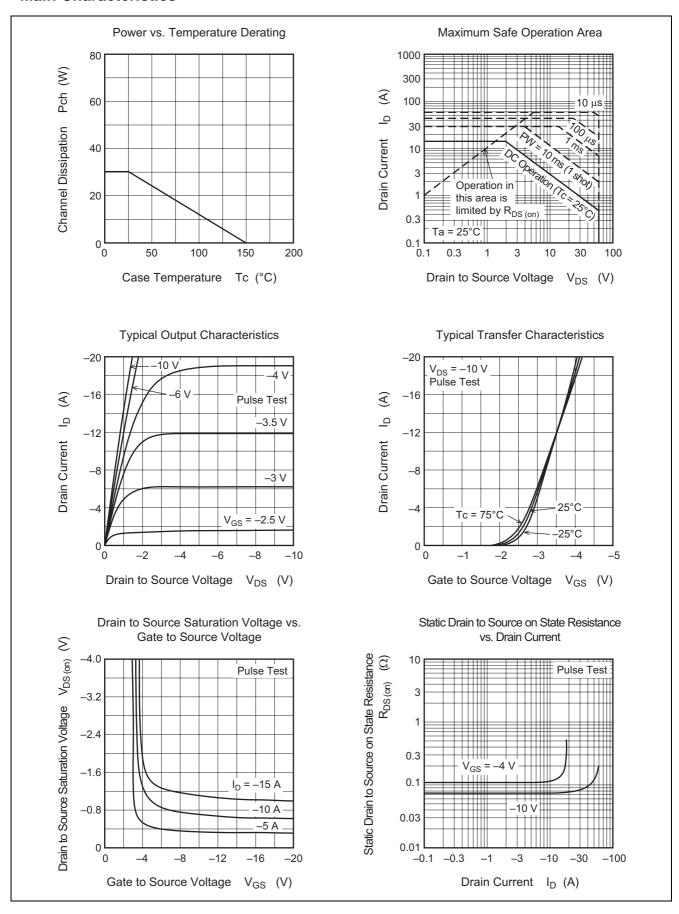
## **Electrical Characteristics**

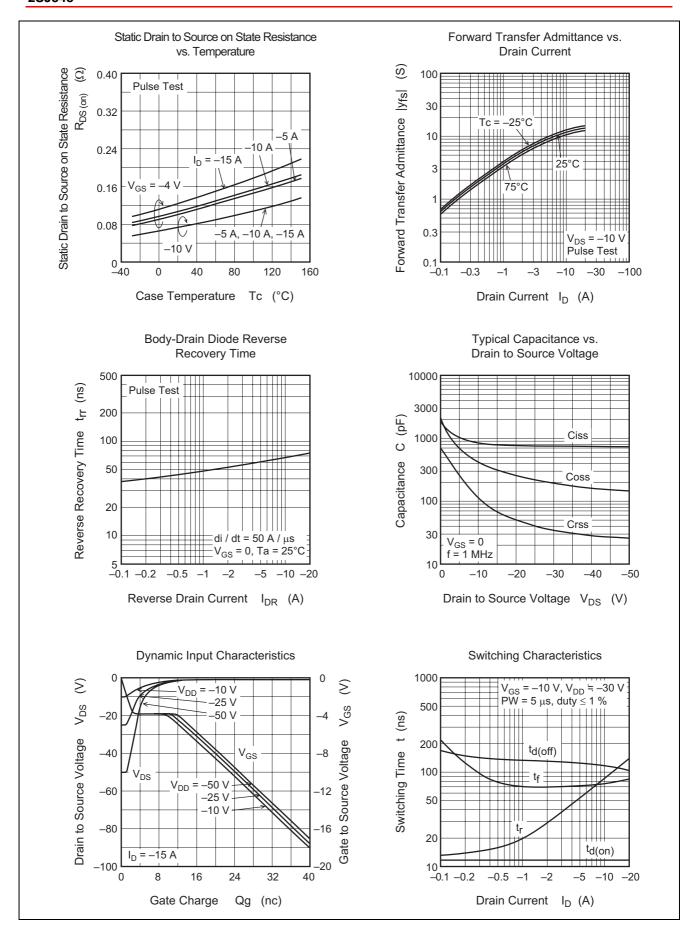
 $(Ta = 25^{\circ}C)$ 

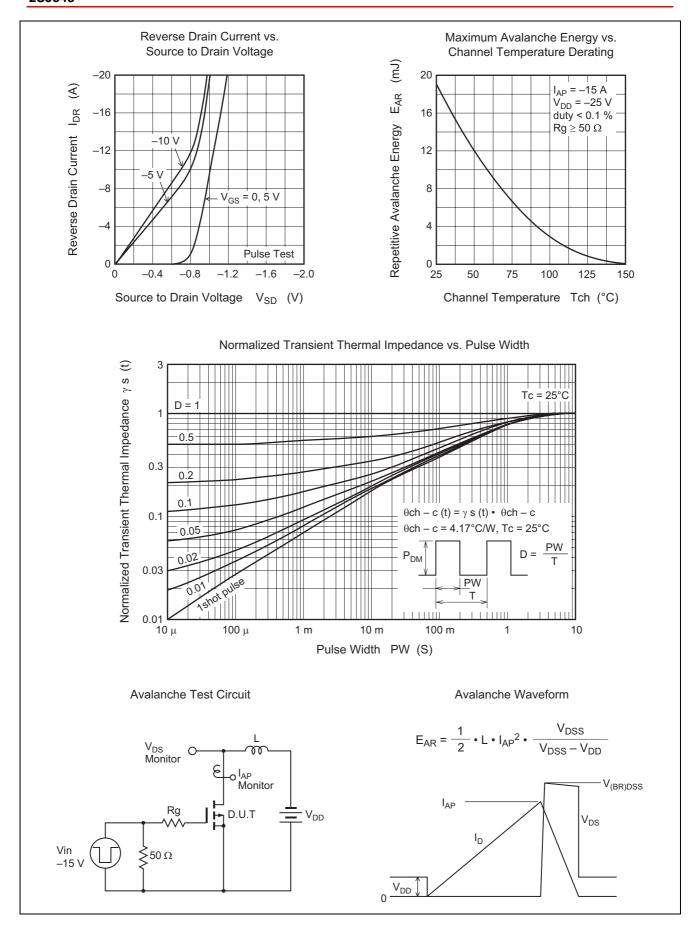
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	-60	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	-10	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-1.0	_	-2.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	0.075	0.095	Ω	$I_D = -8 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 4}}$
	R <sub>DS (on)</sub>	_	0.105	0.155	Ω	$I_D = -8 \text{ A}, V_{GS} = -4 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y <sub>fs</sub>	6.5	11	_	S	$I_D = -8 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	850	_	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	Coss	_	420	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	110	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	_	12	_	ns	V <sub>GS</sub> = -10 V
Rise time	t <sub>r</sub>	_	75	_	ns	I <sub>D</sub> = -8 A
Turn-off delay time	t <sub>d (off)</sub>	_	125	_	ns	$R_L = 3.75 \Omega$
Fall time	t <sub>f</sub>	_	75	_	ns	
Body to drain diode forward voltage	$V_{DF}$	_	-1.1	_	V	$I_F = -15 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	_	70	_	ns	$I_F = -15 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 50 A/μs

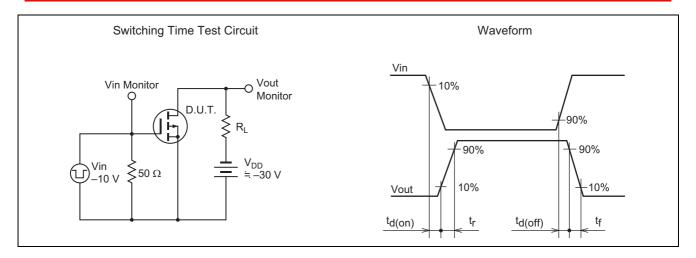
Note: 4. Pulse test

## **Main Characteristics**

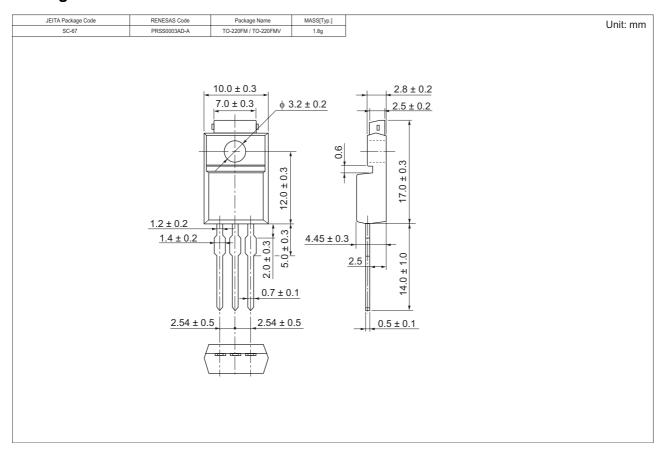








## **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
2SJ548-E	500 pcs	Box (Sack)

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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