

Vishay Semiconductors

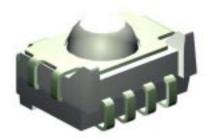
Photo Module for High Data Rates PCM Remote Control Systems

Available Types For Different Carrier Frequencies

Туре	fo	Type	fo
TSOP5130	30 kHz	TSOP5133	33.0 kHz
TSOP5136	36 kHz	TSOP5137	36.7 kHz
TSOP5138	38 kHz	TSOP5140	40.0 kHz
TSOP5156	56 kHz		

Description

The TSOP51.. – series are miniaturized SMD–IR Receiver Modules for infrared remote control systems. PIN diode and preamplifier are assembled on lead frame, the epoxy package is designed as IR filter. The demodulated output signal can directly be decoded by a microprocessor. TSOP51.. is the standard IR remote control SMD–Receiver series, supporting all major transmission codes.



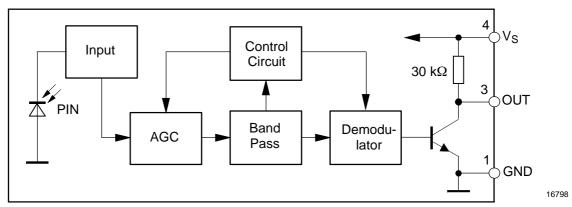
16797

Features

- Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Continuous data transmission possible
- TTL and CMOS compatibility
- Output active low
- Low power consumption

- Enhanced data rate of 3500 bit/s
- Operation with short burst possible (≥6 cycles/burst)
- Taping available for topview and sideview assembly

Block Diagramm



Not for new designs



Absolute Maximum Ratings

 $T_{amb} = 25^{\circ}C$

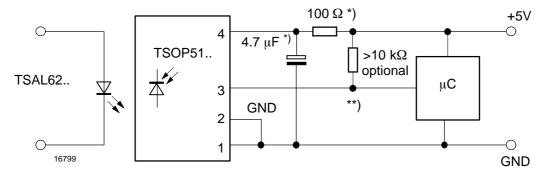
Parameter	Test Conditions	Symbol	Value	Unit
Supply Voltage	Pin 4	Vs	-0.36.0	V
Supply Current	Pin 4	IS	5	mA
Output Voltage	Pin 3	Vo	-0.36.0	V
Output Current	Pin 3	Ιο	15	mA
Junction Temperature		T _i	100	°C
Storage Temperature Range		T _{stg}	-40 + 85	°C
Operating Temperature Range		T _{amb}	-25+85	°C
Power Consumption T _{amb} ≤ 85°C		P _{tot}	50	mW

Basic Characteristics

 $T_{amb} = 25^{\circ}C$

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
Supply Current	$V_S = 5 V, E_V = 0$	I _{SD}	0.8	1.1	1.5	mA
Supply Current	$V_S = 5 \text{ V}, E_V = 40 \text{ klx}, \text{ sunlight}$	I _{SH}		1.4		mA
Supply Voltage		VS	4.5		5.5	V
Transmission Distance	$E_V = 0$, test signal see fig.7, IR diode TSAL6200, $I_F = 400$ mA	d		30		m
Output Voltage Low (Pin 3)	$I_{OSL} = 0.5 \text{ mA,E}_{e} = 0.7 \text{ mW/m}^{2}$	Vosl			250	mV
Irradiance (30-40 kHz)	Pulse width tolerance:	E _{e min}		0.5	0.6	mW/m ²
Irradiance (56 kHz)	$t_{pi} - 5/f_0 < t_{po} < t_{pi} + 6/f_0,$ test signal see fig.8	E _{e min}		0.6	0.7	mW/m ²
Irradiance	$t_{pi} - 5/f_0 < t_{po} < t_{pi} + 6/f_0$	E _{e max}	30			W/m ²
Directivity	Angle of half transmission distance	Ψ1/2		±50		deg

Application Circuit



- *) recommended to suppress power supply disturbances
 **) the output voltage should not be hold continuously at a voltage below 3.3V by the external circuit.



Vishay Semiconductors

Suitable Data Format

The circuit of the TSOP51.. is designed in that way that unexpected output pulses due to noise or disturbance signals are avoided. A bandpassfilter, an integrator stage and an automatic gain control are used to suppress such disturbances.

The distinguishing mark between data signal and disturbance signal are carrier frequency, burst length and duty cycle.

The data signal should fullfill the following condition:

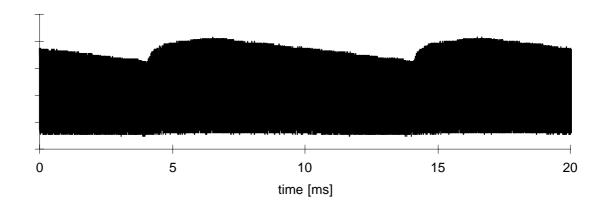
- Carrier frequency should be close to center frequency of the bandpass (e.g. 38kHz).
- Burst length should be 6 cycles/burst or longer.
- After each burst which is between 6 cycles and 70 cycles a gap time of at least 10 cycles is neccessary.
- For each burst which is longer than 1.8 ms a corresponding gap time is necessary at some time in the data stream. This gap time should have at least same length as the burst.
- Up to 2200 short bursts per second can be received continuously.

Some examples for suitable data format are: NEC Code, Toshiba Micom Format, Sharp Code, RC5 Code, RC6 Code, RCMM Code, R–2000 Code, RECS–80 Code.

When a disturbance signal is applied to the TSOP51.. it can still receive the data signal. However the sensitivity is reduced to that level that no unexpected pulses will occure.

Some examples for such disturbance signals which are suppressed by the TSOP51.. are:

- DC light (e.g. from tungsten bulb or sunlight)
- Continuous signal at 38kHz or at any other frequency
- Signals from fluorescent lamps with electronic ballast (an example of the signal modulation is in the figure below).



Not for new designs



Typical Characteristics (T_{amb} = 25°C, unless otherwise specified)

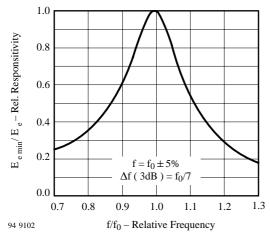


Figure 1. Frequency Dependence of Responsivity

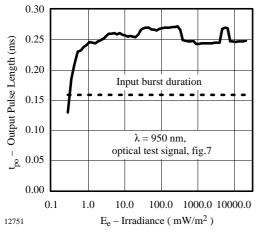


Figure 2. Pulse Length and Sensitivity in Dark Ambient

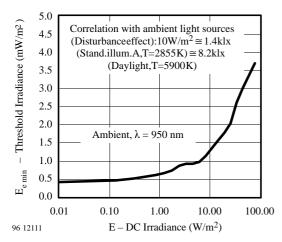


Figure 3. Sensitivity in Bright Ambient

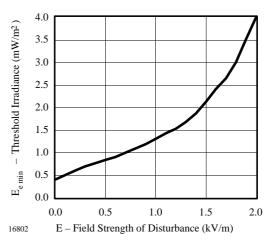
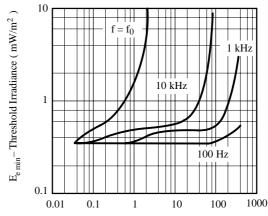


Figure 4. Threshold Irradiance vs. Field Strength of Disturbance



94 9106 ΔV_{sRMS} – AC Voltage on DC Supply Voltage (mV)

Figure 5. Sensitivity vs. Supply Voltage Disturbances

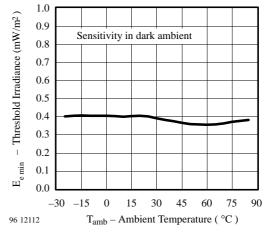
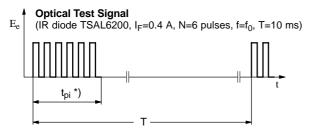


Figure 6. Sensitivity vs. Ambient Temperature



Vishay Semiconductors



*) $t_{pi} \ge 6$ /fo is recommended for optimal function

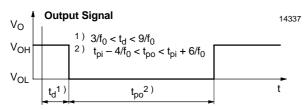
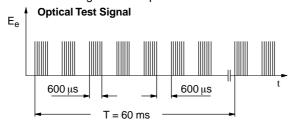


Figure 7. Output Function



94 8134

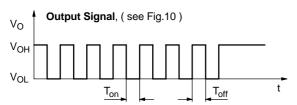


Figure 8. Output Function

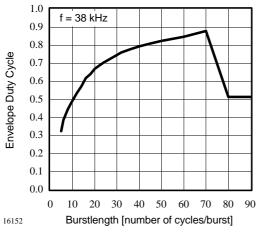


Figure 9. Max. Envelope Duty Cycle vs. Burstlength

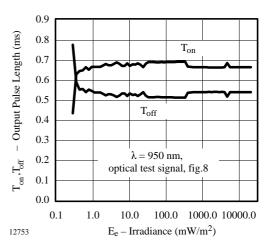


Figure 10. Output Pulse Diagram

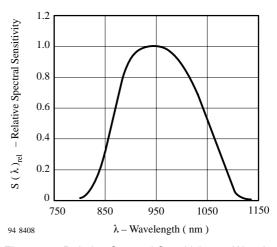


Figure 11. Relative Spectral Sensitivity vs. Wavelength

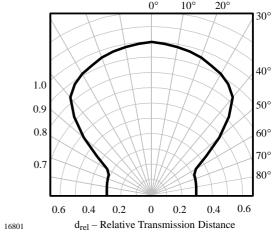


Figure 12. Directivity

Not for new designs

VISHAY

Operating Instructions

Reflow Soldering

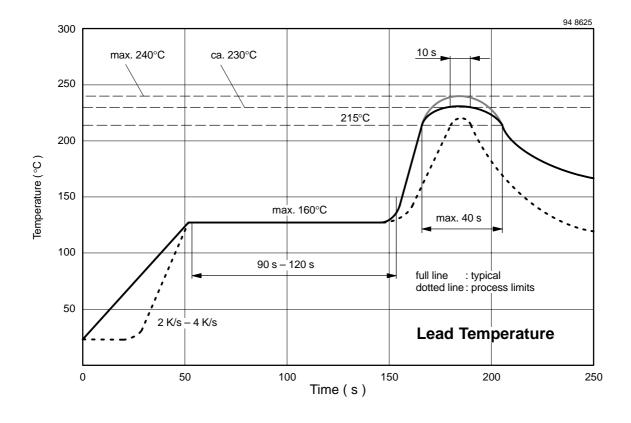
- Reflow soldering must be done within 48 hours stored under max. 30°C, 80% RH after opening envelop
- Recommended soldering paste (composition: SN 63%, Pb 37%)
 Melting temperature 178 to 192°C
- Apply solder paste to the specified soldering pads, by using a dispenser or by screen printing.
- Recommended thickness of metal mask is 0.2 mm for screen printing.
- The recommended reflow furnace is a combinationtype with upper and lower heaters.
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown below. Excercise extreme care to keep the maximum temperature below 230°C. The following temperature profile means the tempera– ture at the device surface. Since temperature differ– ence occurs between the work and the surface of the circuit board depending on the pes of circuit board or reflow furnace, the operating conditions should be verified prior to start of operation.
- Handling after reflow should be done only after the work surface has been cooled off.

Manual Soldering

- Use the 6/4 solder or the solder containing silver.
- Use a soldering iron of 25 W or smaller. Adjust the temperature of the soldering iron below 300°C.
- Finish soldering within three seconds.
- Handle products only after the temperature is cooled off.

Cleaning

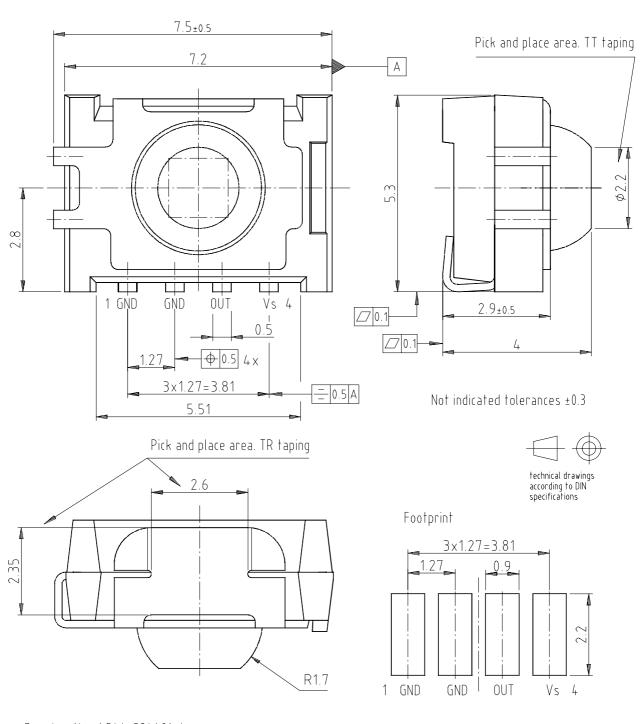
- Perform cleaning after soldering strictly in conformance to the following conditions: Cleaning agent:
 - 2-propanol (isopropyl alcohol). Commercially available grades (industrial use) should be used. Demineralized or distilled water having a resistivity of not less than 500 m Ω corresponding to a conductivity of 2 mS/m.
- Temperature and time: 30 seconds under the temperature below 50°C or 3 minutes below 30°C.
- Ultrasonic cleaning: Below 20 W.





Vishay Semiconductors

Dimensions in mm



Drawing-No.: 6.544-5341.01-4

Issue: 4; 08.11.01

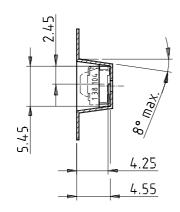
16776

Not for new designs

VISHAY

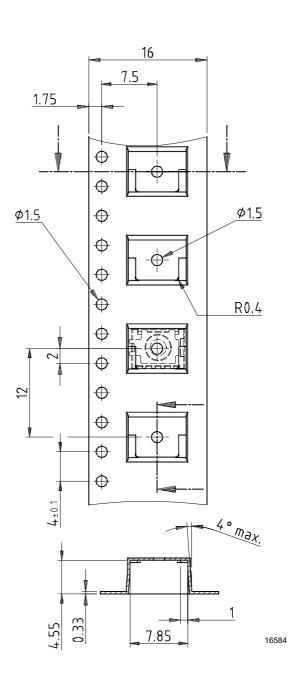
Taping Version TSOP51..TT





Drawing-No.: 9.700-5259.01-4

Issue: 1; 05.09.01

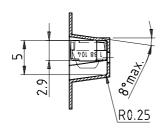




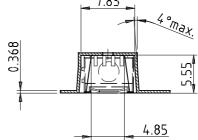
Vishay Semiconductors

Taping Version TSOP51..TR





16 7.5 <u>1.75</u> Ф Ø1.5 Ф <u>Ø1.5</u> Ф Φ Ф Φ Ф R0.83 7 Φ Ф Φ Φ 4 7.85 4°max.



16585

Drawing-No.: 9.700-5260.01-4

Issue: 2; 25.09..01

Not for new designs



Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice. Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Telefunken products for any unintended or unauthorized application, the buyer shall indemnify Vishay Telefunken against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423