SDB360 SCHOTTKY RECTIFIER DIODE

Schottky Barrier Rectifier

General Description

The SDB360 surface mounted Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, free-wheeling diodes, battery charging, and reverse battery protection.



SOD-106

Features and Benefits

- · Low forward drop voltage and low reverse leakage current
- Low power rectified
- "Green" device and RoHS compliant device
- · Available in full lead (Pb)-free device

Applications

- Portable equipment battery applications
- Switching mode power supplies applications

Ordering Information

Part Number	Marking Code	Package	Packaging	
SDB360	3A60	SOD-106	Tape & Reel	

Marking Information



3A60 = Specific Device Code

YWW = Year & Week Code Marking

- -. Y = Year Code
- -. WW = Week Code

= Color band denote cathode

Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol	
1	Cathode	1 2		
2	Anode			

Rev. date: 14-SFP-10 KSD-D6A003-002 www.auk.co.kr

Absolute Maximum Ratings (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Peak reverse voltage	V _{RM}	60	V
Reverse voltage	V _R	60	V
Forward current	I _F	3.0	Α
Peak surge forward current (Non-repetitive 60Hz sine wave)	I _{FSM}	30	А
Junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Electrical Characteristics (T_{amb}=25°C, Unless otherwise specified)

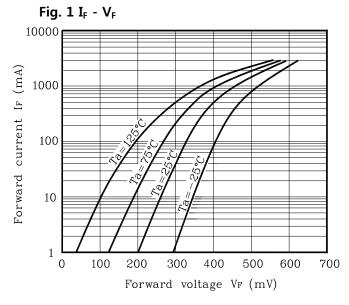
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Forward voltage	V _F 1)	I _F =3A	-	-	0.65	V
Reverse current	I _R ²⁾	V _R =60V	-	-	0.35	mA
Thermal Resistance	R _{th(j-a)} 3)	Junction to ambient	-	-	76	°C /W
Total capacitance	C _T	V _R = 10V, f=1MHz	-	100	-	pF

¹⁾ Pulse test: t_P≤380*µ*s, Duty cycle≤2%

²⁾ Pulse test: t_P≤5ms, Duty cycle≤2%

³⁾ Device mounted on glass epoxy PCB (recommanderable minimum solder land)

Rating and Characteristic Curves



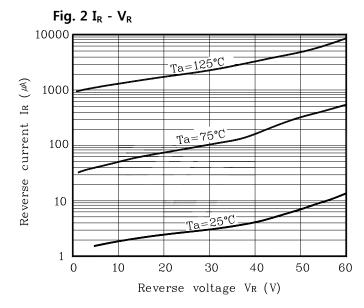
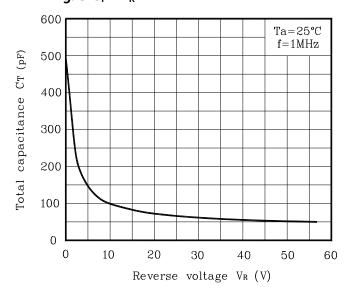
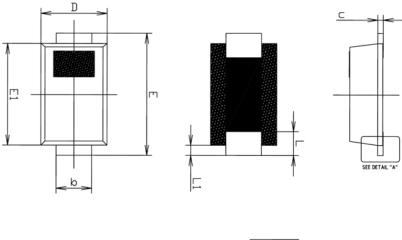
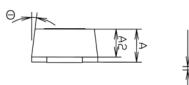


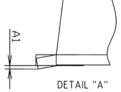
Fig. 3 C_T - V_R



Package Outline Dimensions

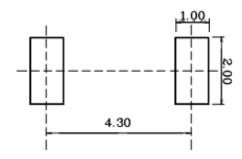






SYMBOL		NOTE		
	MINIMUM	NOMINAL	MAXIMUM	NOTE
Α	1.25	1.30	1.35	
A1	0.00	_	0.10	
A2	1.05	1.10	1.15	
Ь	1.35	1.42	1.49	
С	0.17	0.22	0.27	
D	2.50	2.60	2.70	
E	4.60	4.80	5.00	
E1	3.90	4.00	4.10	
L	0.79	0.94	1.09	
L1	0.30	0.40	0.50	
Θ	4°	_	10°	

**** Recommend PCB solder land (Unit: mm)**



SDB360

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