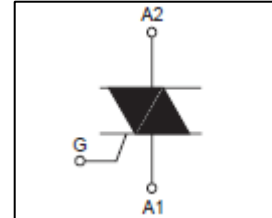


Bi-Directional Triode Thyristor

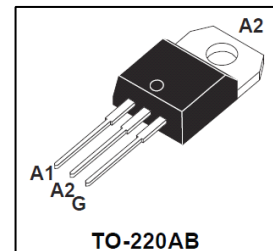
Features

- ◆ Repetitive Peak Off-State Voltage : 600V
- ◆ R.M.S On-State Current ($I_T(\text{RMS})=4\text{ A}$)
- ◆ Low On-State Voltage (1.6V(Typ.) @ I_{TM})
- ◆ High Commutation dv/dt



General Description

Standard gate triggering Triac is suitable for direct coupling to TTL, HTL, CMOS and application such as various logic functions, low power AC switching applications, such as fan speed, small light controllers and home appliance equipment.



Absolute Maximum Ratings ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Condition	Ratings	Units	
V_{DRM}/V_{RRM}	Repetitive Peak Off-State Voltage		600	V	
$I_{T(\text{RMS})}$	R.M.S On-State Current	$T_J = 110^\circ\text{C}$	4.0	A	
I_{TSM}	Surge On-State Current	One cycle, Peak value, non-repetitive full cycle	50Hz	30	A
			60Hz	31	
I_t^2	i_t^2		5.1	A^2s	
P_{GM}	Peak Gate Power Dissipation		5	W	
$P_{G(AV)}$	Average Gate Power Dissipation	$T_J = 125^\circ\text{C}$	1	W	
I_{GM}	Peak Gate Current	$T_J = 125^\circ\text{C}$	4.0	A	
V_{GM}	Peak Gate Voltage		7.0	V	
T_J	Operating Junction Temperature		-40~+150	$^\circ\text{C}$	
T_{STG}	Storage Temperature		-40~+150	$^\circ\text{C}$	

Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta Jc}$	Thermal Resistance Junction to Case(DC)	2.6	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient(DC)	60	$^\circ\text{C}/\text{W}$

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Electrical Characteristics (T_C=25°C unless otherwise noted)

Symbol	Characteristics	Min	Typ.	Max	Unit	
I _{DRM} /I _{RRM}	off-state leakage current (V _{AK} = V _{DRM} /V _{RRM} Single phase, half wave)	T _J =25°C	-	-	5	μA
		T _J =125°C	-	-	1	mA
V _{TM}	Forward "On" voltage (I _T =5A, Inst. Measurement)	-	1.2	1.6	V	
I _{GT}	Gate trigger current (continuous dc) (V _{AK} = 6 Vdc, RL = 10 Ω) Note:1	T2+,G+	-	-	35	mA
		T2+,G-	-	-	35	
		T2-,G-	-	-	35	
V _{GT}	Gate Trigger Voltage (Continuous dc)) (V _{AK} = 6 Vdc, RL = 10 Ω) Note:1	T2+,G+	-	-	1.5	V
		T2+,G-	-	-	1.5	
		T2-,G-	-	-	1.5	
V _{GD}	Gate threshold Voltage V _D = 1/2V _{DRM} , RL = 3.3K Ω	T _J =125°C	0.2	-	-	V
dv/dt	Critical Rate of Rise of Off-State Voltage at Commutation (V _D =0.67V _{DRM} ;gate open) Note:2	T _J =125°C	40	-	-	V/μs
I _H	Holding Current	-	-	15	mA	
I _L	latching current	-	-	30	mA	

Note 1: minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2: for both polarities of A2 referenced to A1 .

Fig 1. Gate Characteristics

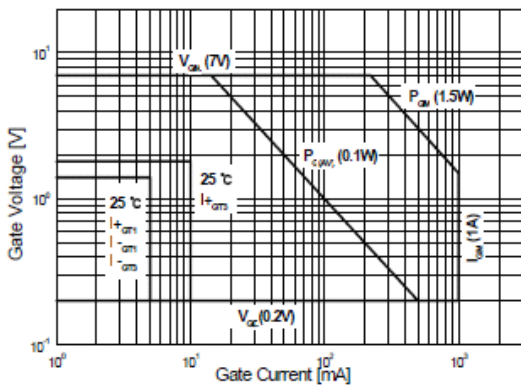


Fig 2. On-State Voltage

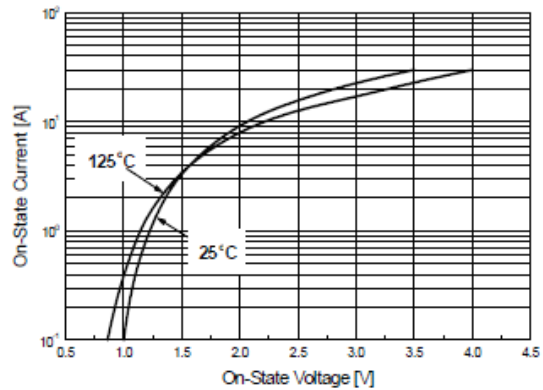


Fig 3. On State Current vs. Maximum Power Dissipation

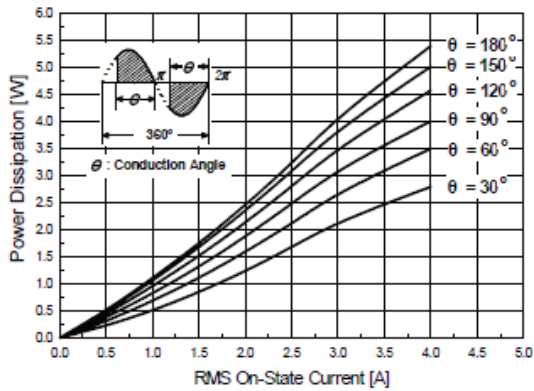


Fig 4. On State Current vs. Allowable Case Temperature

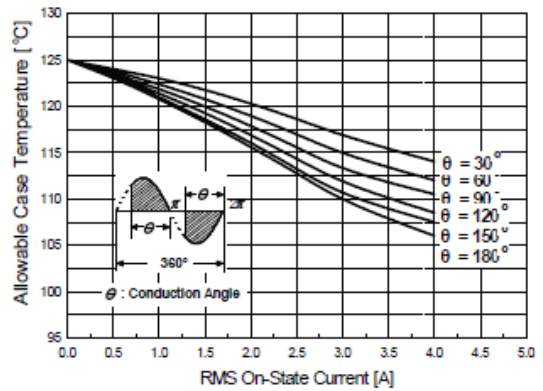


Fig 5. Surge On-State Current Rating (Non-Repetitive)

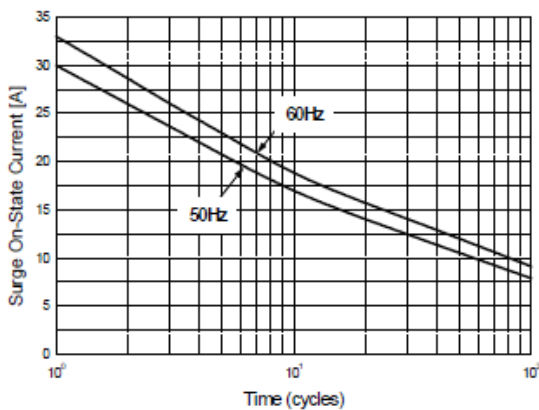


Fig 6. Gate Trigger Voltage vs. Junction Temperature

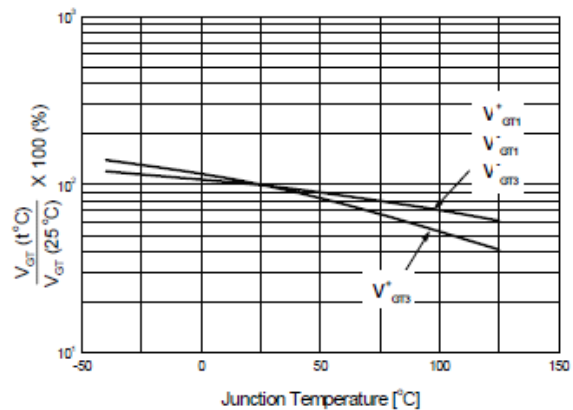


Fig 7. Gate Trigger Current vs. Junction Temperature

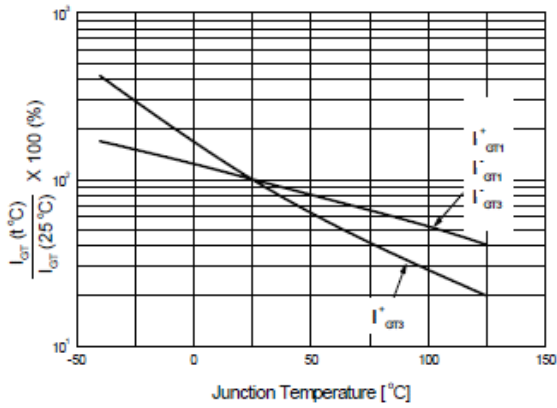


Fig 8. Transient Thermal Impedance

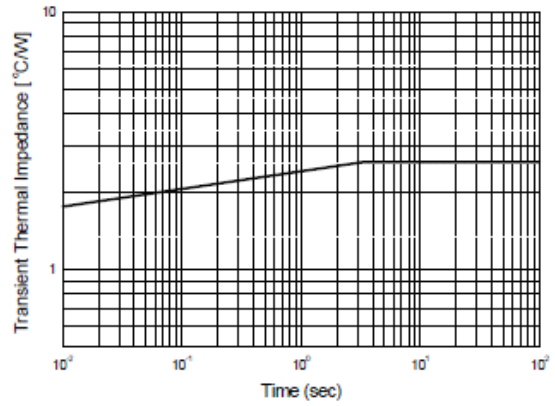
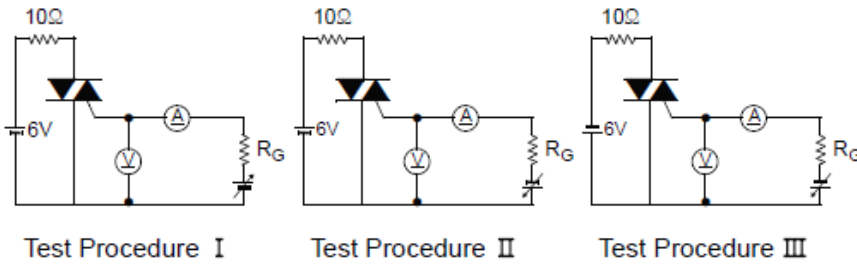


Fig 9. Gate Trigger Characteristics Test Circuit



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TO220 Package Dimension

