# 2SD2178

## Silicon NPN epitaxial planar type

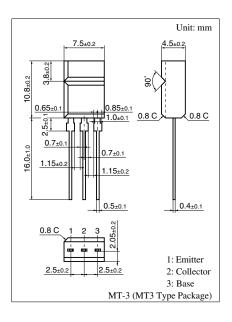
### For low-frequency output amplification

#### ■ Features

- ullet Low collector to emitter saturation voltage  $V_{CE(sat)}$
- ullet Large collector current  $I_C$

### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit
Collector to base voltage	$V_{CBO}$	50	V
Collector to emitter voltage	V <sub>CEO</sub>	50	V
Emitter to base voltage	V <sub>EBO</sub>	5	V
Peak collector current	$I_{CP}$	3	A
Collector current	$I_{C}$	2	A
Collector power dissipation	$P_{C}$	1.5	W
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	$T_{stg}$	-55 to +150	°C



## ■ Electrical Characteristics $T_C = 25$ °C

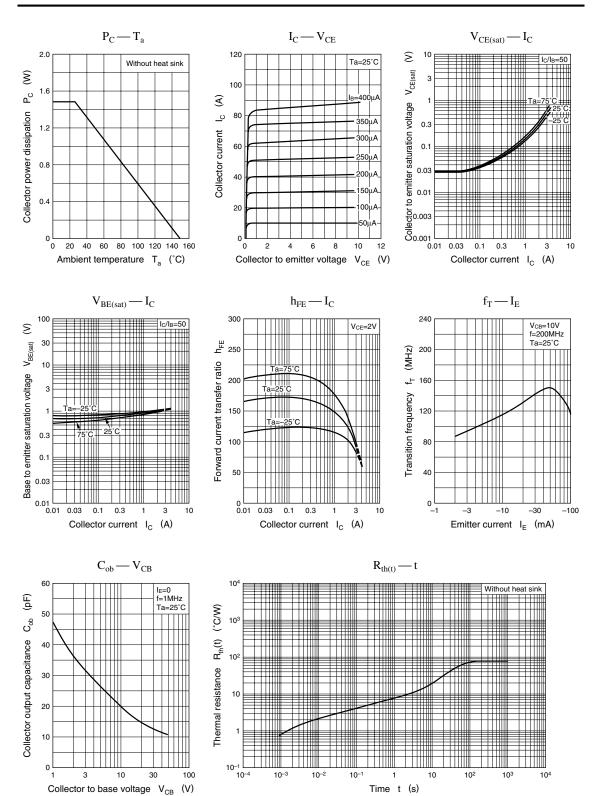
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector cutoff current	$I_{CBO}$	$V_{CB} = 20 \text{ V}, I_E = 0$			0.1	μΑ
Collector to base voltage	V <sub>CBO</sub>	$I_{\rm C} = 10 \; \mu \text{A}, \; I_{\rm E} = 0$	50			V
Collector to emitter voltage	V <sub>CEO</sub>	$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$	50			V
Emitter to base voltage	$V_{EBO}$	$I_{\rm E} = 10 \; \mu \text{A}, \; I_{\rm C} = 0$	5			V
Forward current transfer ratio	h <sub>FE1</sub> *	$V_{CE} = 2 \text{ V}, I_{C} = 200 \text{ mA}$	120		340	
	h <sub>FE2</sub>	$V_{CE} = 2 \text{ V}, I_{C} = 1.0 \text{ A}$	80			
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	$I_{\rm C} = 1 \text{ A}, I_{\rm B} = 50 \text{ mA}$		0.15	0.3	V
Base to emitter saturation voltage	V <sub>BE(sat)</sub>	$I_{\rm C} = 1 \text{ A}, I_{\rm B} = 50 \text{ mA}$		0.9	1.2	V
Transition frequency	$f_T$	$V_{CE} = 10 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$		23	35	pF

Note) \*: Rank classification

Rank	R	S
$h_{FE1}$	120 to 240	170 to 340

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2SD2178 Power Transistors



2

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