

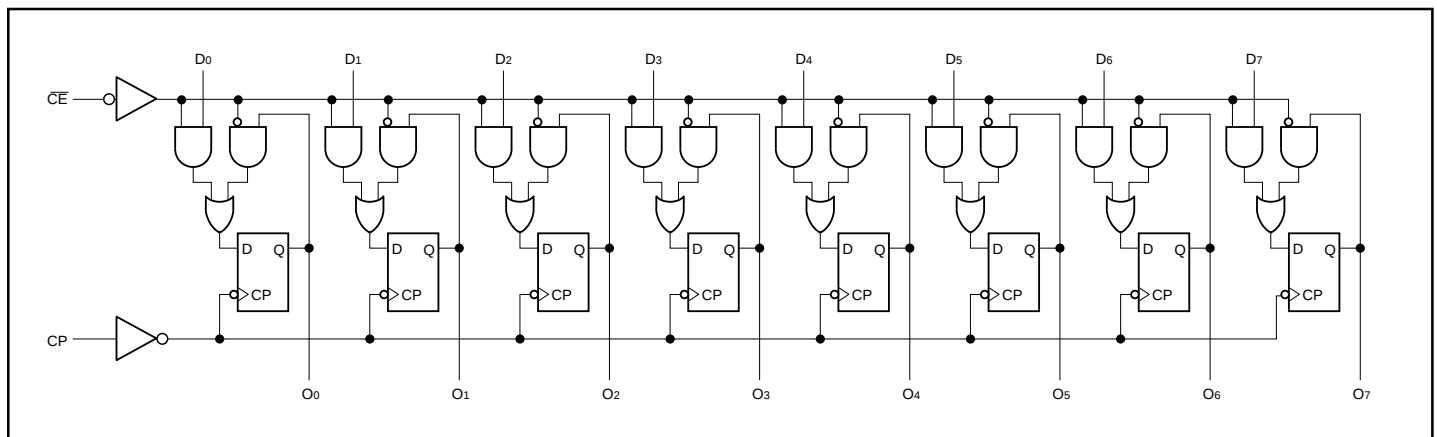
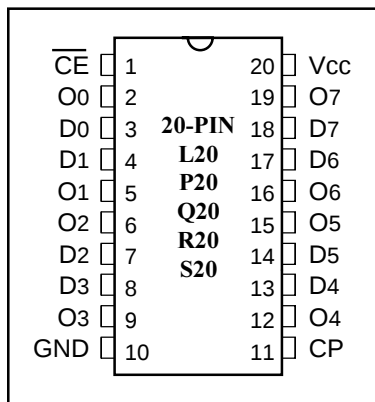
**Product Features:**

- The PI74FCT377T is pin compatible with bipolar FAST™ Series at a higher speed and lower power consumption
- TTL input and output levels
- Octal D flip-flops with Clock Enable
- Extremely low static power
- Hysteresis on all inputs
- Industrial operating temperature range:  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$
- Packages available:
  - 20-pin 173 mil wide plastic TSSOP (L)
  - 20-pin 300 mil wide plastic DIP (P)
  - 20-pin 150 mil wide plastic QSOP (Q)
  - 20-pin 150 mil wide plastic TQSOP (R)
  - 20-pin 300 mil wide plastic SOIC (S)

**Product Description:**

Pericom Semiconductor's PI74FCT series of logic circuits are produced in the Company's advanced 0.6/0.8 micron CMOS technology, achieving industry leading speed grades.

The PI74FCT377T is an 8-bit wide octal designed with eight edge-triggered D-type flip-flops with individual D inputs and O outputs. When Clock Enable (CE) is LOW, the common buffered Clock (CP) loads all flip-flops simultaneously. The register is fully edge-triggered. D input state, one setup time before the LOW-to-HIGH clock transition, is transferred to the corresponding flip-flop's O output. The CE input must be stable only one setup time prior to the LOW-to-HIGH transition for predictable operation.

**Logic Block Diagram**

**Product Pin Configuration**

**Product Pin Description**

| Pin Name               | Description               |
|------------------------|---------------------------|
| $\overline{\text{CE}}$ | Clock Enable (Active LOW) |
| CP                     | Clock Pulse Input         |
| D0-D7                  | Data Inputs               |
| O0-O7                  | Data Outputs              |
| GND                    | Ground                    |
| Vcc                    | Power                     |

**Truth Table<sup>(1)</sup>**

|              | Inputs |                        |                | Outputs        |
|--------------|--------|------------------------|----------------|----------------|
| Mode         | CP     | $\overline{\text{CE}}$ | D <sub>N</sub> | O <sub>N</sub> |
| Load "1"     | ↑      | l                      | h              | H              |
| Load "0"     | ↑      | l                      | l              | L              |
| Hold         | ↑      | h                      | X              | NC             |
| (Do Nothing) | H      | H                      | X              | NC             |

1. H = HIGH Voltage Level  
 h = HIGH Voltage Level one setup time prior to the LOW-to-HIGH Clock Transition  
 L = LOW Voltage Level  
 l = LOW Voltage Level one setup time prior to the LOW-to-HIGH Clock Transition  
 X = Don't Care  
 NC = No Change  
 ↑ = LOW-to-HIGH Clock Transition

### Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

|                                                               |                 |
|---------------------------------------------------------------|-----------------|
| Storage Temperature .....                                     | -65°C to +150°C |
| Ambient Temperature with Power Applied .....                  | -40°C to +85°C  |
| Supply Voltage to Ground Potential (Inputs & Vcc Only) .....  | -0.5V to +7.0V  |
| Supply Voltage to Ground Potential (Outputs & D/O Only) ..... | -0.5V to +7.0V  |
| DC Input Voltage .....                                        | -0.5V to +7.0V  |
| DC Output Current .....                                       | 120 mA          |
| Power Dissipation .....                                       | 0.5W            |

#### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

### DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ , $V_{CC} = 5\text{V} \pm 5\%$ )

| Parameters | Description           | Test Conditions <sup>(1)</sup>                                 |                            | Min. | Typ <sup>(2)</sup> | Max. | Units         |
|------------|-----------------------|----------------------------------------------------------------|----------------------------|------|--------------------|------|---------------|
| $V_{OH}$   | Output HIGH Voltage   | $V_{CC} = \text{Min.}$ , $V_{IN} = V_{IH}$ or $V_{IL}$         | $I_{OH} = -15.0\text{ mA}$ | 2.4  | 3.0                |      | V             |
| $V_{OL}$   | Output LOW Current    | $V_{CC} = \text{Min.}$ , $V_{IN} = V_{IH}$ or $V_{IL}$         | $I_{OL} = 64\text{ mA}$    |      | 0.3                | 0.55 | V             |
| $V_{IH}$   | Input HIGH Voltage    | Guaranteed Logic HIGH Level                                    |                            | 2.0  |                    |      | V             |
| $V_{IL}$   | Input LOW Voltage     | Guaranteed Logic LOW Level                                     |                            |      |                    | 0.8  | V             |
| $I_{IH}$   | Input HIGH Current    | $V_{CC} = \text{Max.}$                                         | $V_{IN} = V_{CC}$          |      |                    | 1    | $\mu\text{A}$ |
| $I_{IL}$   | Input LOW Current     | $V_{CC} = \text{Max.}$                                         | $V_{IN} = \text{GND}$      |      |                    | -1   | $\mu\text{A}$ |
| $V_{IK}$   | Clamp Diode Voltage   | $V_{CC} = \text{Min.}$ , $I_{IN} = -18\text{ mA}$              |                            |      | -0.7               | -1.2 | V             |
| $I_{OS}$   | Short Circuit Current | $V_{CC} = \text{Max.}$ <sup>(3)</sup> , $V_{OUT} = \text{GND}$ |                            | -60  | -120               |      | mA            |
| $I_{OFF}$  | Power Down Disable    | $V_{CC} = \text{GND}$ , $V_{OUT} = 4.5\text{V}$                |                            | —    | —                  | 100  | $\mu\text{A}$ |
| $V_H$      | Input Hysteresis      |                                                                |                            |      | 200                |      | mV            |

### Capacitance ( $T_A = 25^{\circ}\text{C}$ , $f = 1\text{ MHz}$ )

| Parameters <sup>(4)</sup> | Description        | Test Conditions       | Typ | Max. | Units |
|---------------------------|--------------------|-----------------------|-----|------|-------|
| $C_{IN}$                  | Input Capacitance  | $V_{IN} = 0\text{V}$  | 6   | 10   | pF    |
| $C_{OUT}$                 | Output Capacitance | $V_{OUT} = 0\text{V}$ | 8   | 12   | pF    |

#### Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at  $V_{CC} = 5.0\text{V}$ ,  $+25^{\circ}\text{C}$  ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. This parameter is determined by device characterization but is not production tested.

**Power Supply Characteristics**

| Parameters       | Description                                     | Test Conditions <sup>(1)</sup>                                                                                                                                    |                                                            | Min. | Typ <sup>(2)</sup> | Max.                | Units  |
|------------------|-------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------|------|--------------------|---------------------|--------|
| I <sub>CC</sub>  | Quiescent Power Supply Current                  | V <sub>CC</sub> = Max.                                                                                                                                            | V <sub>IN</sub> = GND or V <sub>CC</sub>                   |      | 0.1                | 500                 | μA     |
| ΔI <sub>CC</sub> | Supply Current per Input @ TTL HIGH             | V <sub>CC</sub> = Max.                                                                                                                                            | V <sub>IN</sub> = 3.4V <sup>(3)</sup>                      |      | 0.5                | 2.0                 | mA     |
| I <sub>CCD</sub> | Supply Current per Input per MHz <sup>(4)</sup> | V <sub>CC</sub> = Max., Outputs Open<br>CE = GND<br>One Input Toggling<br>50% Duty Cycle                                                                          | V <sub>IN</sub> = V <sub>CC</sub><br>V <sub>IN</sub> = GND |      | 0.15               | 0.25                | mA/MHz |
| I <sub>C</sub>   | Total Power Supply Current <sup>(6)</sup>       | V <sub>CC</sub> = Max., Outputs Open<br>f <sub>CP</sub> = 10 MHz, 50% Duty Cycle<br>CE = GND<br>50% Duty Cycle<br>One Bit toggling at f <sub>i</sub> = 5 MHz      | V <sub>IN</sub> = V <sub>CC</sub><br>V <sub>IN</sub> = GND |      | 1.57               | 3.5 <sup>(5)</sup>  | mA     |
|                  |                                                 |                                                                                                                                                                   | V <sub>IN</sub> = 3.4V<br>V <sub>IN</sub> = GND            |      | 2.0                | 5.5 <sup>(5)</sup>  |        |
|                  |                                                 | V <sub>CC</sub> = Max., Outputs Open<br>f <sub>CP</sub> = 10 MHz, 50% Duty Cycle<br>CE = GND<br>Eight Bits toggling at f <sub>i</sub> = 2.5 MHz<br>50% Duty Cycle | V <sub>IN</sub> = V <sub>CC</sub><br>V <sub>IN</sub> = GND |      | 3.8                | 7.3 <sup>(5)</sup>  |        |
|                  |                                                 |                                                                                                                                                                   | V <sub>IN</sub> = 3.4V<br>V <sub>IN</sub> = GND            |      | 6.0                | 16.3 <sup>(5)</sup> |        |

**Notes:**

- For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at V<sub>CC</sub> = 5.0V, +25°C ambient.
- Per TTL driven input (V<sub>IN</sub> = 3.4V); all other inputs at V<sub>CC</sub> or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I<sub>CC</sub> formula. These limits are guaranteed but not tested.
- I<sub>C</sub> = I<sub>QUIESCENT</sub> + I<sub>INPUTS</sub> + I<sub>DYNAMIC</sub>  
I<sub>C</sub> = I<sub>CC</sub> + ΔI<sub>CC</sub> D<sub>H</sub>N<sub>T</sub> + I<sub>CCD</sub> (f<sub>CP</sub>/2 + f<sub>i</sub>N<sub>i</sub>)  
I<sub>CC</sub> = Quiescent Current  
ΔI<sub>CC</sub> = Power Supply Current for a TTL High Input (V<sub>IN</sub> = 3.4V)  
D<sub>H</sub> = Duty Cycle for TTL Inputs High  
N<sub>T</sub> = Number of TTL Inputs at D<sub>H</sub>  
I<sub>CCD</sub> = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)  
f<sub>CP</sub> = Clock Frequency for Register Devices (Zero for Non-Register Devices)  
f<sub>i</sub> = Input Frequency  
N<sub>i</sub> = Number of Inputs at f<sub>i</sub> (All currents are in milliamps and all frequencies are in megahertz.)

**Switching Characteristics over Operating Range**

| Parameters                           | Description                                     | Conditions <sup>(1)</sup>                       | 377T |      | 377AT |     | 377CT |     | 377DT |     | Unit |
|--------------------------------------|-------------------------------------------------|-------------------------------------------------|------|------|-------|-----|-------|-----|-------|-----|------|
|                                      |                                                 |                                                 | Com. |      | Com.  |     | Com.  |     | Com.  |     |      |
|                                      |                                                 |                                                 | Min  | Max  | Min   | Max | Min   | Max | Min   | Max |      |
| t <sub>PLH</sub><br>t <sub>PHL</sub> | Propagation Delay<br>CP to ON                   | C <sub>L</sub> = 50 pF<br>R <sub>L</sub> = 500Ω | 2.0  | 13.0 | 2.0   | 7.2 | 2.0   | 5.2 | 2.0   | 4.5 | ns   |
| t <sub>SU</sub>                      | Setup Time, HIGH or LOW<br>Dn to CP             |                                                 | 2.5  | —    | 2.0   | —   | 2.0   | —   | 2.0   | —   | ns   |
| t <sub>H</sub>                       | Hold Time, HIGH or LOW<br>Dn to CP              |                                                 | 2.0  | —    | 1.5   | —   | 1.5   | —   | 1.5   | —   | ns   |
| t <sub>SU</sub>                      | Setup Time HIGH or LOW<br>CE to CP              |                                                 | 4.0  | —    | 3.5   | —   | 3.5   | —   | 2.0   | —   | ns   |
| t <sub>H</sub>                       | Hold Time HIGH or LOW<br>CE to CP               |                                                 | 1.5  | —    | 1.5   | —   | 1.5   | —   | 1.5   | —   | ns   |
| t <sub>w</sub>                       | Clock Pulse Width <sup>(3)</sup><br>HIGH or LOW |                                                 | 7.0  | —    | 6.0   | —   | 6.0   | —   | 3.0   | —   | ns   |

**Notes:**

- See test circuit and wave forms.
- Minimum limits are guaranteed but not tested on Propagation Delays.
- This parameter guaranteed but not production tested.